芯伯乐®

XINBOLE

Product Specification

XBLW MC33063

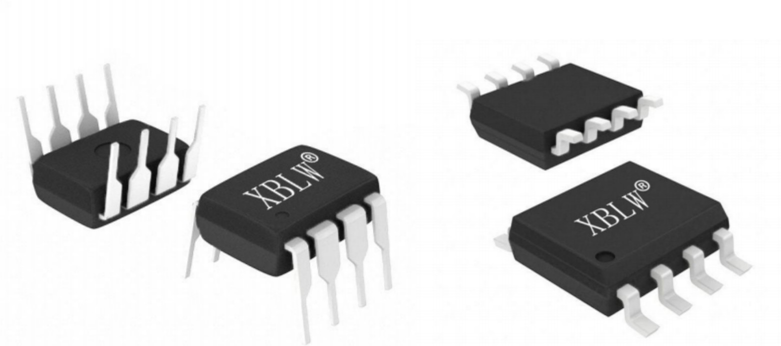
1.5A Step-Up/Down/Inverting Switching Regulator

WEB | www.xinboleic.com

RoHS

Descriptions

The XBLW MC33063 is a monolithic control circuit containing the primary functions required for DC-to-DC converters. This device consists of an internal temperature compensated reference（1.25V）,comparator, controlled duty cycle oscillator with an active current limit circuit, driver and high current output switch. The IC is specifically designed to be used in Step-Down and Step-Up and Voltage-Inverting applications with a minimum number of external components.
The XBLW MC33063 is available in DIP8 and SOP8 package.



DIP-8 SOP-8

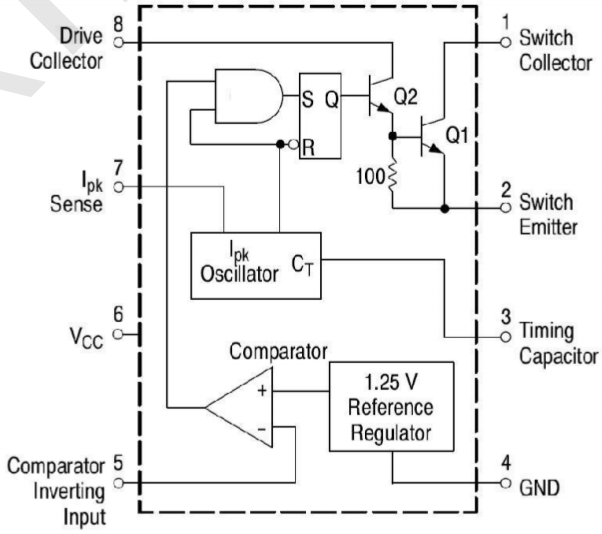
Feature

- Current Limiting
- Low Standby Current
- Precision 2% Reference
- Output Voltage Adjustable
- Output Switch Current to 1.5A
- Frequency Operation to 100kHz
- Operation from 3.0V to 40V Input

Applications

- ADSL Modems
- Battery Chargers
- NICs / Switches / Hubs
- Negative Voltage Power Supplies

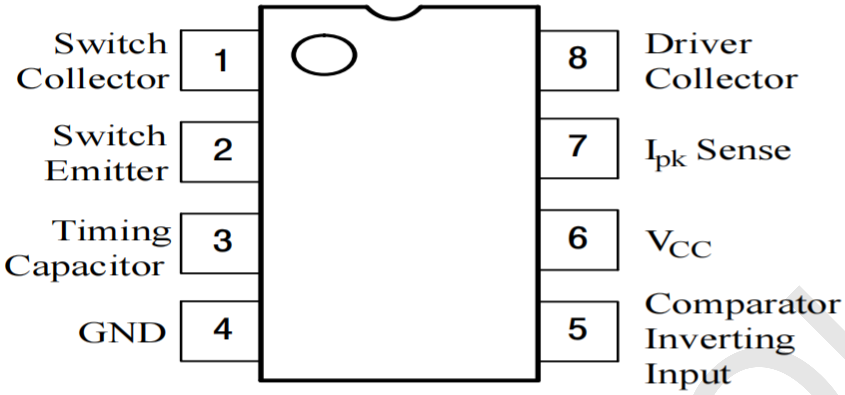
Functional Block Diagram



Ordering Information

Product Model	Package Type	Marking	Packing	Packing Qty
XBLW MC33063AN	DIP-8	MC33063AN	Tube	2000pcs/Box
XBLW MC33063ADTR	SOP-8	MC33063A	Tape	2500pcs/Reel

Pin Configuration



(Top View)

Pin Description

Pin Number	Pin Name	Function Description	Pin Number	Pin Name	Function Description
1	SC	Switch collector	5	C_{II}	Comparator inverting input
2	SE	Switch emitter	6	V_{CC}	Input voltage
3	CT	Timing capacitor	7	Ipk	Ipk sense
4	GND	Ground	8	DC	Drive collector

Absolute Maximum Ratings (Ta=25°C)

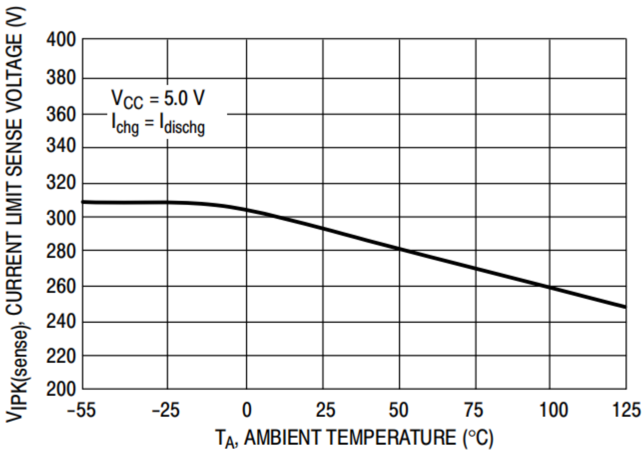
Parameter Name		Symbol	Value	Unit
Power Supply Voltage		Vcc	40	V
Comparator Input Voltage Range		V_{IR}	-0.3~40	V
Switch Collector Voltage		Vc(switch)	40	V
Switch Emitter Voltage (VPin1=40V)		V_E(switch)	40	V
Switch Collector to Emitter Voltage		V_{CE}(switch)	40	V
Driver Collector Voltage		Vc(drive)	40	V
Driver Collector Current		Ic(drive)	100	mA
Switch Current		I_{SW}	1.5	A
Power Dissipation	DIP8	P_D	1.25	W
	SOP8		625	mW
Operating Ambient Temperature Range		Ta	-20~85	℃
Storage Temperature Range		Tstg	-65~150	℃

Electrical Characteristics

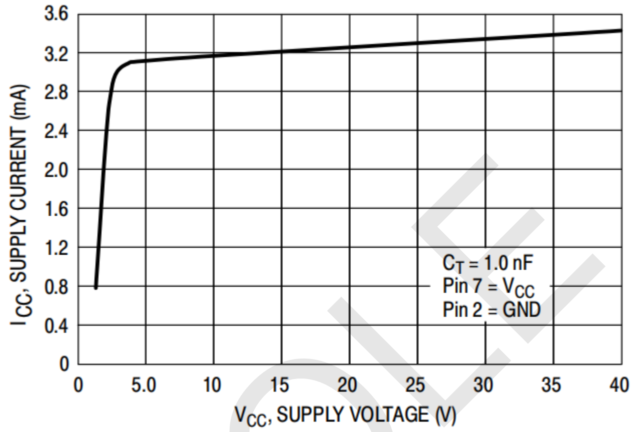
(Unless otherwise noted ,Vcc=5.0V,Ta=25°C)

Parameter Name	Symbol	Min	Typ	Max	Unit
OSCILLATOR					
Frequency (Vpin5=0V,CT=1.0nF, Ta=25°C)	fosc	24	33	42	kHz
Charge Current (Vcc=5.0~40V, Ta=25°C)	Ichg	24	35	42	μA
Discharge Current (Vcc=5.0~40V, Ta=25°C)	Idischg	140	220	260	μA
Discharge to Charge Current Ratio (Pin7 to Vcc, Ta=25°C)	Idischg/Ichg	5.2	6.5	7.5	
Current limit Sense Voltage (Ichg=Idischg, Ta=25°C)	Vipk(sense)	250	300	350	mA
OUTPUT SWITCH					
Saturation Voltage, Darlington Connection (ISW=1.0A, Pins 1,8 Connected)	V_{CE}(sat)		1.0	1.3	V
Saturation Voltage, Darlington Connection (ISW=1.0A, Rpin 8=82Ω to Vcc, Forced β ≈ 20)	V_{CE}(sat)		0.45	0.7	V
DC Current Gain (ISW=1.0A,VCE=5.0V, Ta=25°C)	hFE	50	75		
Collector Off-State Current (VCE=40V)	Ic(off)		0.01	100	μA
COMPARATOR					
Threshold Voltage (Ta=25°C)	Vth	1.225	1.25	1.275	V
Threshold Voltage (Ta=0~70°C)		1.21		1.29	
Threshold Voltage Line Regulation (Vcc=3.0~40V)	Regline		1.4	5.0	mV
Input Bias Current(Vin=0V)	I_{IB}		-20	-400	nA
TOTAL DEVICE					
Supply Current (Vcc=5.0~40V, CT=1.0nF, Pin7=Vcc, Vpin5>Vth, pin2=Gnd, Remaining Pins Open)	Icc			4.0	mA

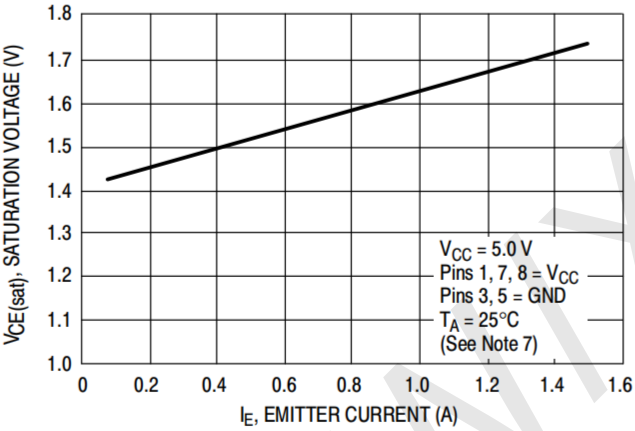
Characteristics Curves



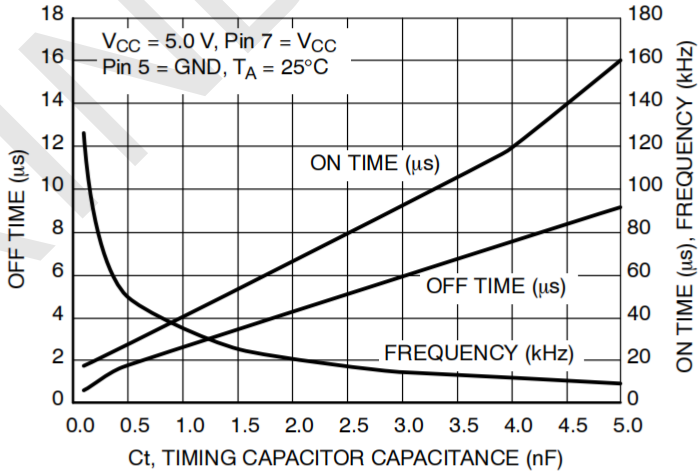
Current Limit Sense Voltage versus Temperature



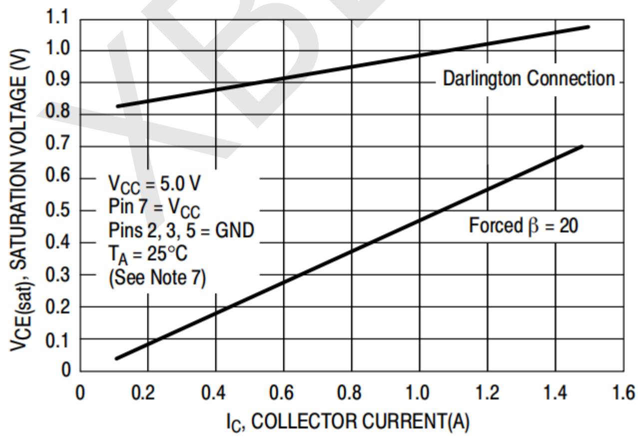
Standby Supply Current versus Supply Voltage



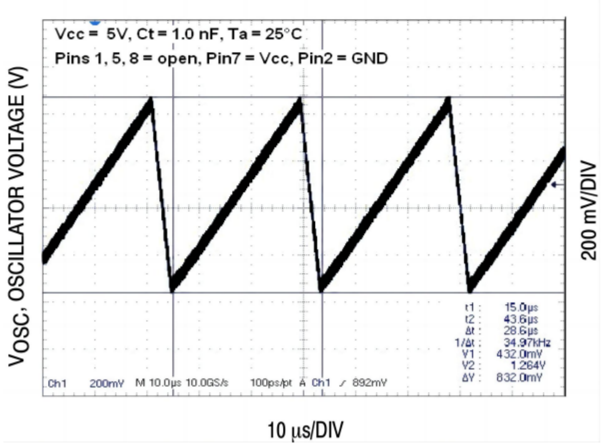
Emitter Follower Configuration Output Saturation Voltage versus Emitter Current



Oscillator Frequency



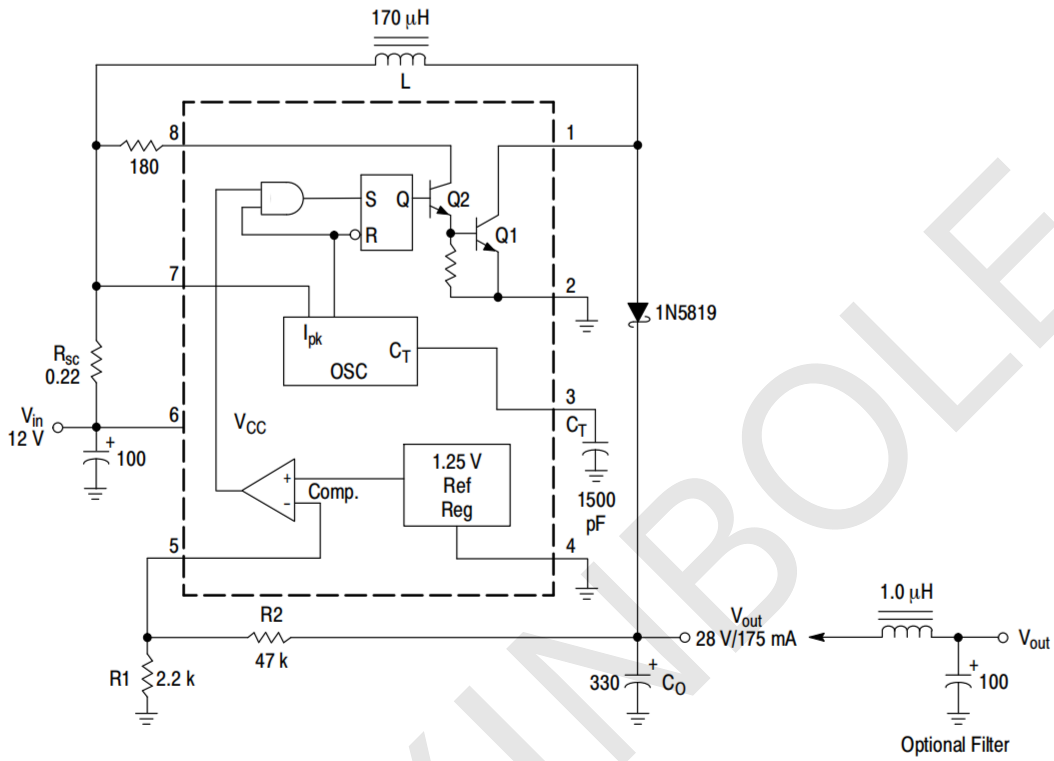
Common Emitter Configuration Output Switch Saturation Voltage versus Collector Current



Timing Capacitor Waveform

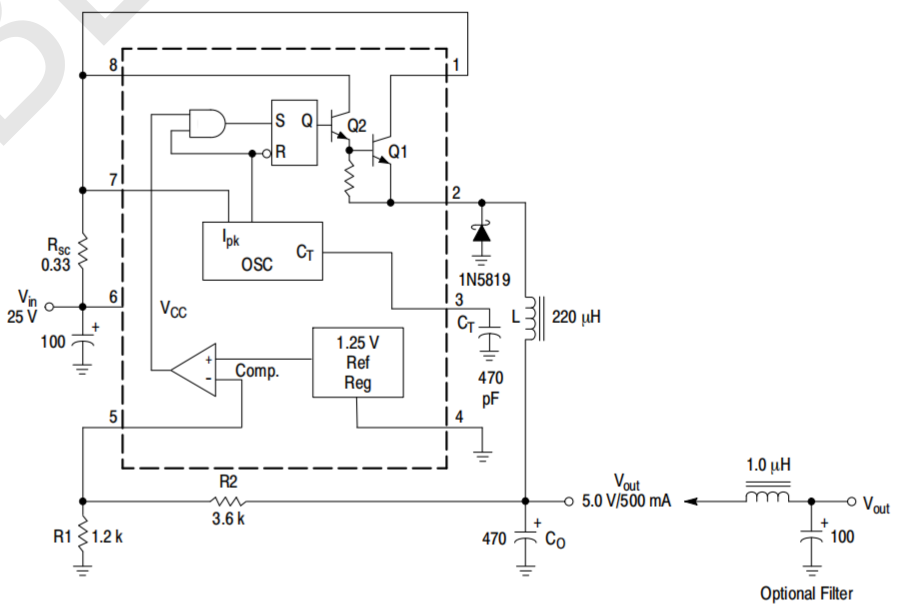
Typical Application

1. Step-Up Converter



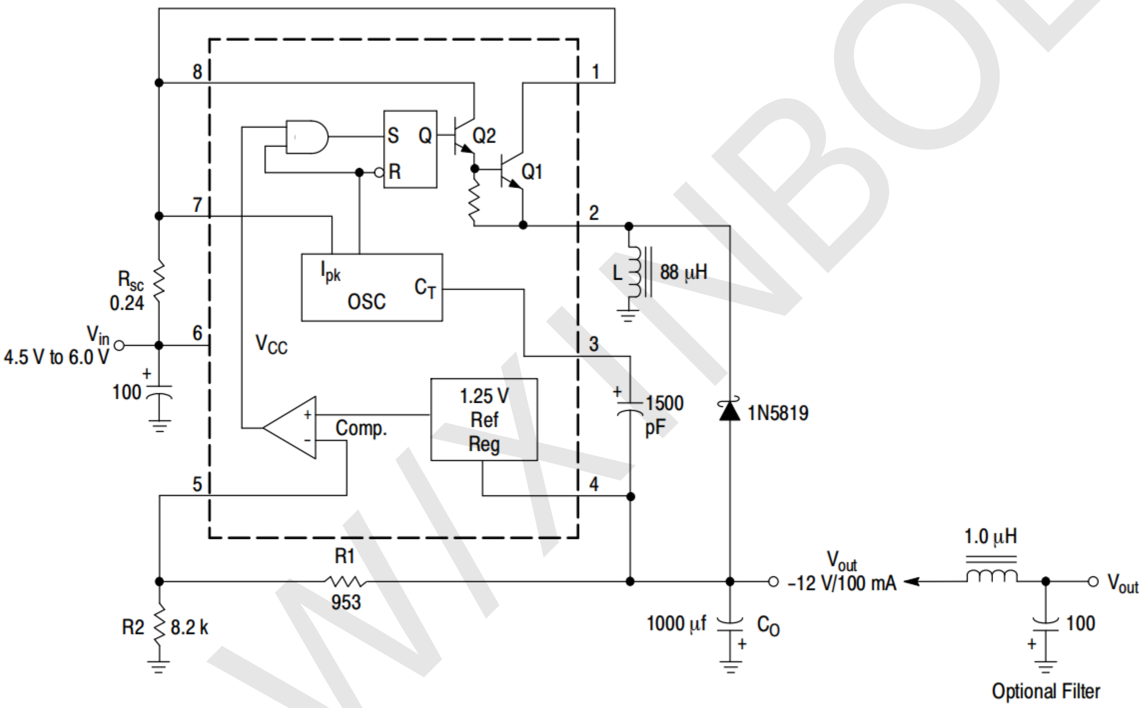
Test	Conditions	Results
Line Regulation	Vin=8.0V~16V, Io=175mA	30mV=±0.05%
Load Regulation	Vin=12V,Io=75mA~175mA	10mV=±0.017%
Output Ripple	Vin=12V, Io=175mA	400mVpp
Efficiency	Vin=12V, Io=175mA	87.7%
Output Ripple With Optional Filter	Vin=12V, Io=175mA	40mVpp

2. Step-Down Converter



Test	Conditions	Results
Line Regulation	Vin=15V~25V, Io=500mA	12mV=±0.12%
Load Regulation	Vin=25V,Io=50mA~500mA	3.0mV=±0.03%
Output Ripple	Vin=25V, Io=500mA	120mVpp
Short Circuit Current	Vin=25V,RL=0.1Ω	1.1A
Efficiency	Vin=25V, Io=500mA	83.7%
Output Ripple With Optional Filter	Vin=25V, Io=500mA	40mVpp

3. Voltage Inverting Converter



Test	Conditions	Results
Line Regulation	Vin=4.5V~6.0V, Io=100mA	3.0mV=±0.012%
Load Regulation	Vin=5.0V,Io=10mA~100mA	0.022V=±0.09%
Output Ripple	Vin=5.0V, Io=100mA	500mVpp
Short Circuit Current	Vin=5.0V,RL=0.1Ω	910mA
Efficiency	Vin=5.0V, Io=100mA	62.2%
Output Ripple With Optional Filter	Vin=5.0V, Io=100mA	70mVpp

Application Information

Calculation	Step–Up	Step–Down	Voltage–Inverting
t_{on}/t_{off}	$\frac{V_{out} + V_F - V_{in(min)}}{V_{in(min)} - V_{sat}}$	$\frac{V_{out} + V_F}{V_{in(min)} - V_{sat} - V_{out}}$	$\frac{\lvert V_{out}\rvert + V_F}{V_{in} - V_{sat}}$
$(t_{on} + t_{off})$	$\frac{1}{f}$	$\frac{1}{f}$	$\frac{1}{f}$
t_{off}	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$
t_{on}	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$
C_T	$4.0 \times 10^{-5}\ t_{on}$	$4.0 \times 10^{-5}\ t_{on}$	$4.0 \times 10^{-5}\ t_{on}$
$I_{pk(switch)}$	$2I_{out(max)}\left(\frac{t_{on}}{t_{off}} + 1\right)$	$2I_{out(max)}$	$2I_{out(max)}\left(\frac{t_{on}}{t_{off}} + 1\right)$
R_{sc}	$0.3/I_{pk(switch)}$	$0.3/I_{pk(switch)}$	$0.3/I_{pk(switch)}$
$L_{(min)}$	$\left(\frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}}\right) t_{on(max)}$	$\left(\frac{(V_{in(min)} - V_{sat} - V_{out})}{I_{pk(switch)}}\right) t_{on(max)}$	$\left(\frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}}\right) t_{on(max)}$
C_O	$9\frac{I_{out}t_{on}}{V_{ripple(pp)}}$	$\frac{I_{pk(switch)}(t_{on} + t_{off})}{8V_{ripple(pp)}}$	$9\frac{I_{out}t_{on}}{V_{ripple(pp)}}$

Vsat = Saturation voltage of the output switch

V_F = Forward voltage drop of the output rectifier

The following power supply characteristics must be chosen:

Vin — Nominal input voltage

Vout — Desired output voltage , $\lvert Vout \rvert = 1.25 \times (1 + \frac{R2}{R1})$

Iout — Desired output current

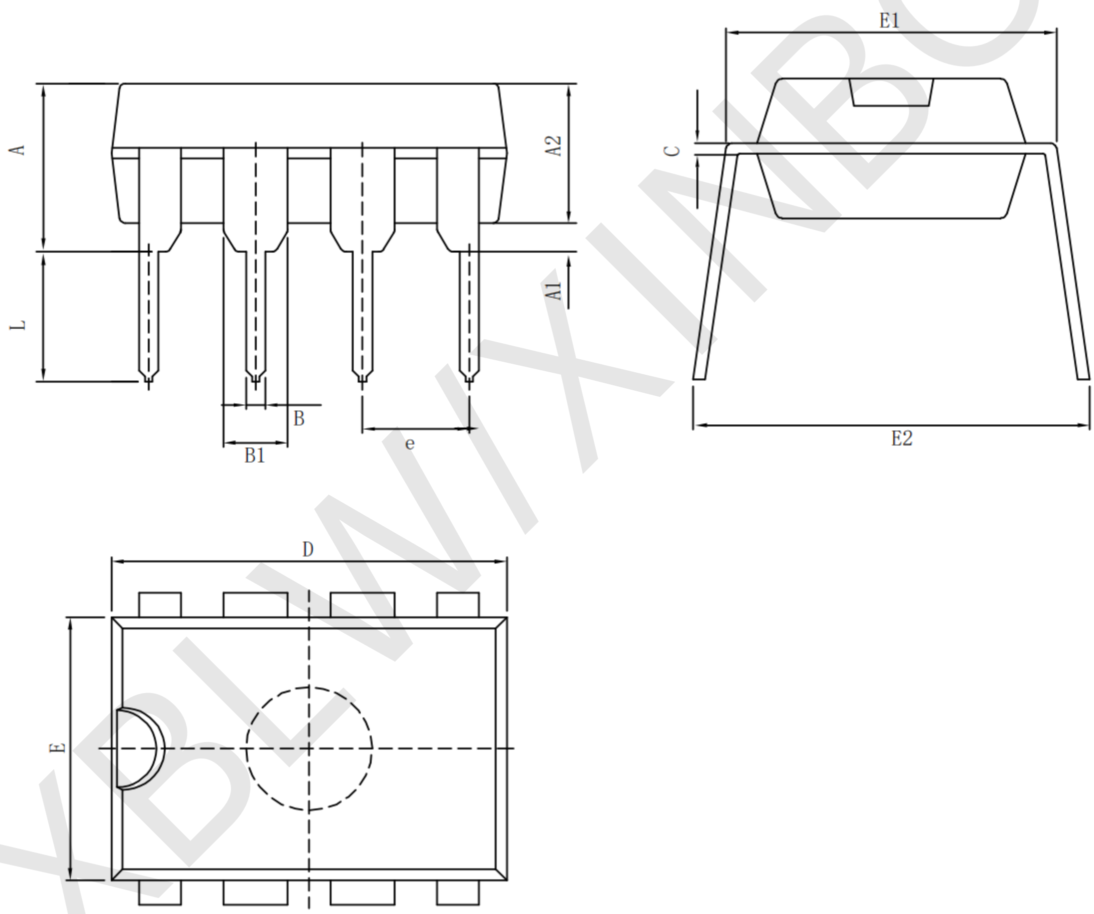
fmin — Minimun desired output switching frequency at the selected values of Vin and Io

Vripple(pp) — Desired peak–to–peak output ripple voltage. In practice, the calculated capacitor value will need to be increased due to its equivalent series resistance and board layout. The ripple voltage should be kept to a low value since it will directly affect the line and load regulation.

Package Information

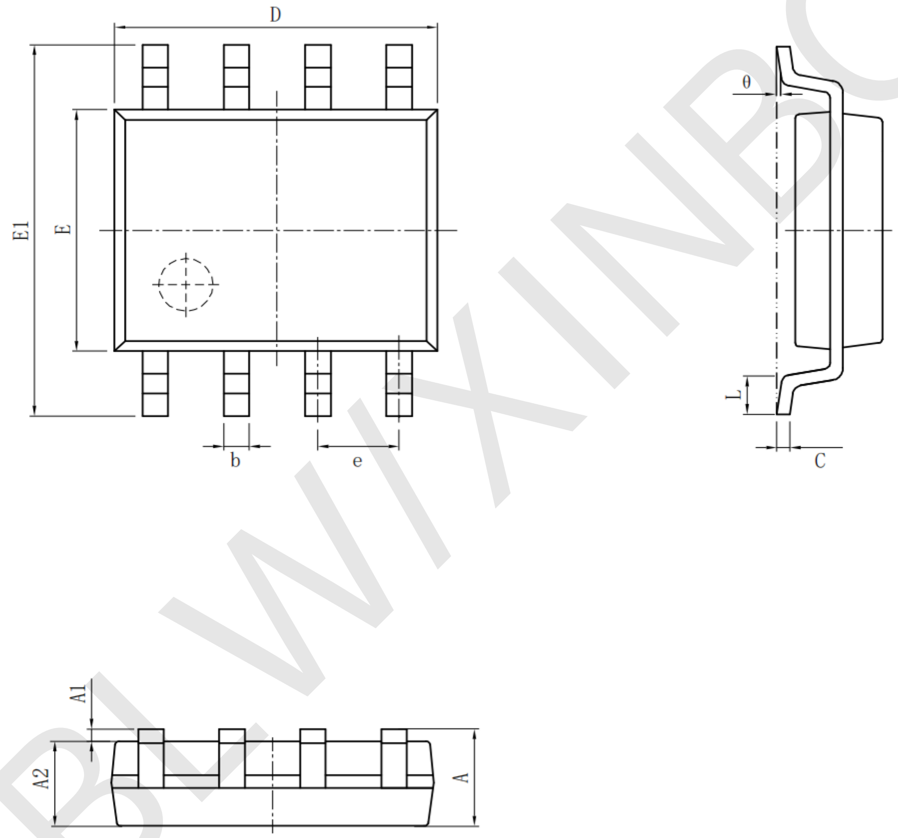
• DIP-8

Size / Symbol	Dimensions In Millimeters		Size / Symbol	Dimensions In Inches	
	Min(mm)	Max(mm)		Min(in)	Max(in)
A	3.710	4.310	A	0.146	0.170
A1	0.510		A1	0.020	
A2	3.200	3.600	A2	0.126	0.142
B	0.380	0.570	B	0.015	0.022
B1	1.524(BSC)		B1	0.060(BSC)	
C	0.204	0.360	C	0.008	0.014
D	9.000	9.400	D	0.354	0.370
E	6.200	6.600	E	0.244	0.260
E1	7.320	7.920	E1	0.288	0.312
e	2.540(BSC)		e	0.100(BSC)	
L	3.000	3.600	L	0.118	0.142
E2	8.400	9.000	E2	0.331	0.354



• SOP-8

Symbol \ Size	Dimensions In Millimeters		Symbol \ Size	Dimensions In Inches	
	Min(mm)	Max(mm)		Min(in)	Max(in)
A	1.350	1.750	A	0.053	0.069
A1	0.100	0.250	A1	0.004	0.010
A2	1.350	1.550	A2	0.053	0.061
b	0.330	0.510	b	0.013	0.020
c	0.170	0.250	c	0.006	0.010
D	4.700	5.100	D	0.185	0.200
E	3.800	4.000	E	0.150	0.157
E1	5.800	6.200	E1	0.228	0.224
e	1.270(BSC)		e	0.050(BSC)	
L	0.400	1.270	L	0.016	0.050
θ	0°	8°	θ	0°	8°



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