

Features

- For surface mounted applications in order to optimize board space.
- Low profile package.
- Glass passivated junction.
- Low inductance.
- Plastic package has Underwriters Laboratory Flammability.



SOD-123FL (SMF)

Mechanical Data

- Case': JEDEC SOD-123FL/SMF molded plastic body
- Terminals: Solderable per MIL-STD-750, Method 2026A
- Polarity: Polarity symbol marking on body
- Mounting Position: Any
- Marking: Date Code and Marking Code See Page 2

Applications

- I/O interface
- AC/DC power supply
- Low frequency signal transmission line (RS232,RS485,etc.)

Maxmim Ratings (Ta=25°C unless otherwise noted)

Peak pulse power dissipation at 10/1000µs waveform (Note1, Note2, Fig.1)	P _{PPM}	200	W
Peak pulse current	I _{PP}	4.4	Α
Steady state power dissipation at T _A =50 ℃ (Fig.5)	P _{M(AV)}	P _{M(AV)} 1.0	
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load, (JEDEC Method) (Note3, Fig.6)	I _{FSM}	30	А
Operating junction and Storage Temperature Range.	T _J ,T _{STG}	-65 to +150	°C
Typical thermal resistance junction to lead	R _{θJL}	38	°C/W
Typical thermal resistance junction to ambient	R _{θJA}	180	°C/W

Notes:1. Non-repetitive current pulse, per Fig.3 and derated above TA=25°C per Fig.2.

- 2. Mounted on 5.0mm×5.0mm (0.03mm thick) copper pads to each terminal.
- 3. 8.3ms single half sine-wave, or equivalent square wave, duty cycle=4 pulses per minutes maximum.

Electrical Characteristics (Ta=25°C)

Part N	Number	Mar	vice king ode	Reverse Stand- Off Voltage	Breakdown Voltage @I⊤	Test Current	Maximum Clamping Voltage @IPP	Peak Pulse Current	Reverse Leakage @Vrwm
Unidirectional	Bidirectional	UNI	ВІ	VRWM(V)	VBR(V)	I⊤(mA)	Vc(V)	IPP(A)	Ir(µA)
HSMF28AT13	HSMF28CAT13	CG	PG	28.0	31.1-34.4	1	45.4	4.4	1



Ratings and Characteristic Curves (TA=25°C unless otherwise noted)

Fig.1 Peak Pulse Power Rating Curve

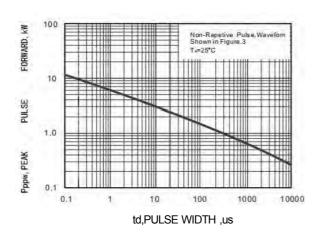


Fig.2 Forward Current Derating Curve

TA, AMBIENT TEMPERATURE, C

Fig.3 Pulse Waveform

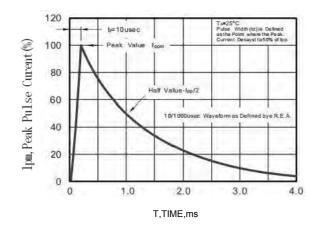
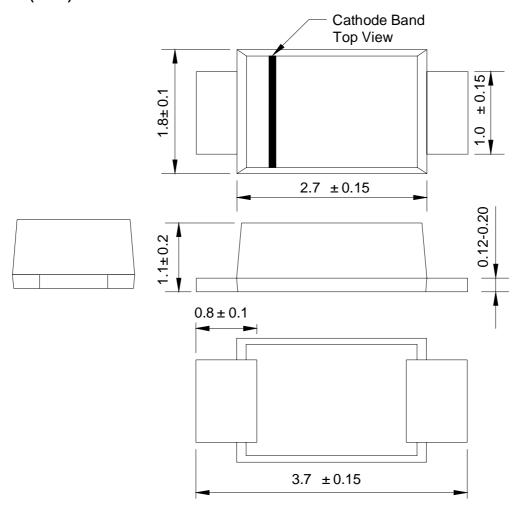


Fig.4 Maximum Non-Repetitive Peak Forward Surge Current CURRENT 30 25 SURGE 20 FORWARD 15 10 05 T_=T_max 8,3ms Single Half Sine; JEDEC Method FSw, PEAK 0 10 100

Number of Cycles at 60Hz



Package Outline Dimensions SOD-123FL(SMF)





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