**Product data sheet** 

## 1. General description

NPN small-signal Darlington transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: BCV48-Q

### 2. Features and benefits

- High current (max. 500 mA)
- Low voltage (max. 60 V)
- High DC current gain (min. 10000)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

· Preamplifier input applications

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>C</sub>	collector current		-	-	500	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C	2000	-	-	

## 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		B C
2	С	collector		
3	В	base	3 2 1 SOT89	TR1 TR2  E sym087



**NPN Darlington transistor** 

## 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
BCV49-Q		plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89		

### 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BCV49-Q	EG

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	80	V
V <sub>CES</sub>	collector-emitter voltage	V <sub>BE</sub> = 0 V		-	60	V
$V_{EBO}$	emitter-base voltage	open collector		-	10	V
I <sub>C</sub>	collector current			-	500	mA
I <sub>CM</sub>	peak collector current			-	1	Α
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.3	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

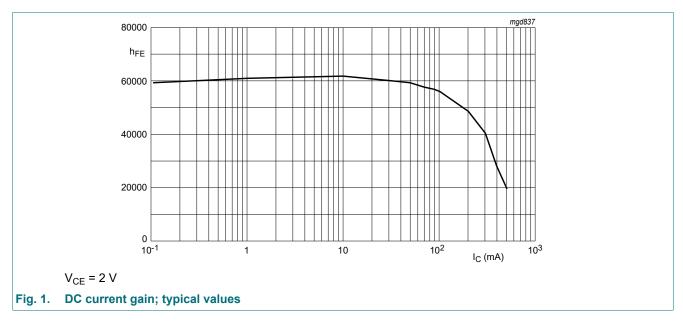
<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

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### 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 60 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 10 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 1 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	2000	-	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; T <sub>amb</sub> = 25 °C	4000	-	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 mA; T <sub>amb</sub> = 25 °C	10000	-	-	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C	2000	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 100 \text{ mA}; I_B = 0.1 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	-	1	V
V <sub>BEsat</sub>	base-emitter saturation voltage		-	-	1.5	V
$V_{BEon}$	base-emitter turn-on voltage	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; T_{amb} = 25 \text{ °C}$	-	-	1.4	V
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 30 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	-	220	-	MHz



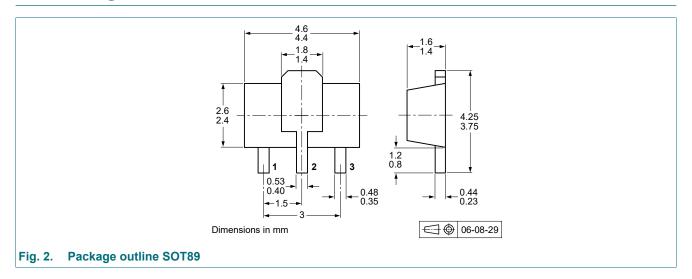
### 11. Test information

### **Quality information**

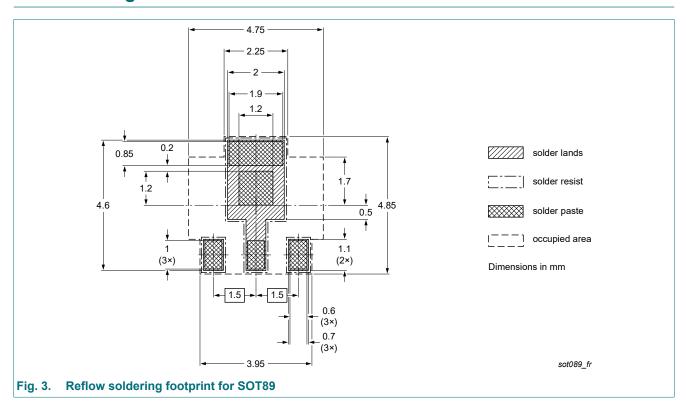
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### **NPN Darlington transistor**

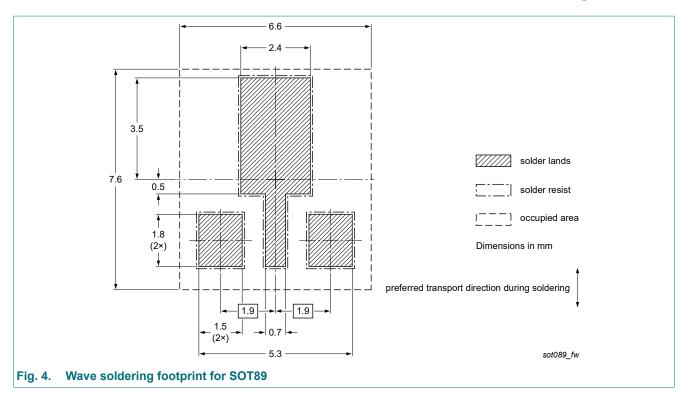
## 12. Package outline



## 13. Soldering



### **NPN Darlington transistor**



### **NPN Darlington transistor**

# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCV49-Q v.1	20230413	Product data sheet	-	-

### **NPN Darlington transistor**

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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