

NSH300N15P3

N-Channel Enhanced Shielded Gate Trench Power MOSFET



VOLTAGE: 150 Volts

CURRENT: 29 Amperes

Package: PDFN3*3

Marking And Polarity

FEATURES

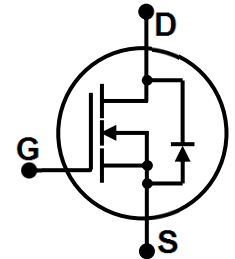
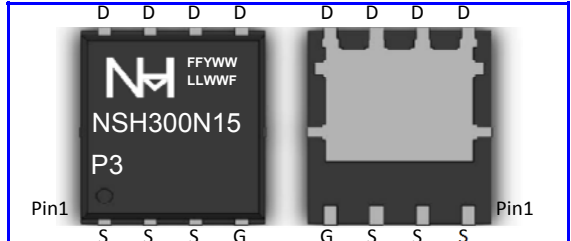
- Low $R_{DS(ON)}$ For High Efficiency
- Low Gate Charge For High Speed Swithing
- High EAS For High Reliability
- 100% UIS And RG Tested

TYPICAL APPLICATIONS

- DC/DC Converter And Synchronous Rectification
- High-Frequency Circuits And Battery Management System(BMS)
- The Motor Drives And Printed Circuit Board Control
- Automotive Electronics And UPS (Uninterruptible Power Supplies)
- **Weight:App. 0.0198 Grams (0.0007 Ounce)**

PRODUCT SUMMARY

VDS Min.@Tj	150	V
I _D Min.@Ta	29	A
RDS(ON) Type@10V	25.5	mΩ



Remark:

- ①. NH=Nihang Trademark
- ②. FF=Product Line Code,According To Actual Changes
YWW=Date Code,According To Actual Changes
LLWWF=Inernal Code,According To Actual Changes
- ③. NSH300N15P3=Model

Absolute Maximum Ratings (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current (Note 1)	Ta= 25 °C	I_D	29	A
	Ta= 100 °C		19	
Drain Current-Pulsed (Note 1)	T _J < 150 °C	I_{DM}	116	A
Maximum Power Dissipation Power	Ta= 25 °C	P_D	52	W
Dissipation Derating Factor Above 25°C	Ta= 100 °C		21	
Derating Factor		D_F	0.42	W/°C
Junction Temperature		T_J	-55 to 150	°C
Storage Temperature Range		T_{STD}	-55 to 150	°C
Avalanche Current,Single Pulse (Note 1)	L= 0.5 mH	I_{AS}	3	A
Single Pulse Avalanche Energy (Note 1) Test Circuit & Waveform See Fig.16	L= 0.5 mH,VDD= 75 V IAS= 2.5 A,RG= 10 Ω Starting Tj=25 °C,VG = 10 V	E_{AS}	1.6	mJ

Thermal Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Thermal Resistance Junction To Ambient	Still Air Environment With Ta =25°C	$R_{\theta JA}$	50	°C/W
Thermal Resistance Junction-Case	Device Mounted On 1 in ² FR-4 Board With 2oz	$R_{\theta JC}$	2.4	

Notes: 1. Pulse Width Limited By Max. Junction Temperature. (See Fig. 13).

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Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V, ID=250uA	BV_{DSS}	150	--	--	V
Bvdss Temperature Coefficient	ID=250uA, Reference 25°C	$\Delta BV_{DSS}/\Delta T_J$	--	0.172	--	V/°C
Drain-Source Leakage Current	VDS= 150 V, VGS=0V	I_{DSS}	--	--	1	uA
Gate-Body Leakage Current	VGS= ±20 V, VDS=0V	I_{GSS}	--	--	±100	nA
Forward Transconductance	ID= 15 A, VDS= 5 V	g_{fs}	--	29	--	S
Static on Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	$V_{GS(TH)}$	2.0	3.0	4.0	V
Drain-Source On Resistance	ID= 15 A, VGS= 10 V	$R_{DS(ON)}$	--	25.50	30.00	mΩ
	ID= 15 A, VGS= 4.5 V		--	29.33	40.20	
Dynamic Characteristics						
Gate Resistance	VGS=0V, VDS=0V, Freq.=1MHz	R_g	--	1.70	--	Ω
Input Capacitance	VDS= 75 V	C_{iss}	--	875.0	--	pF
Output Capacitance	VGS= 0 V	C_{oss}	--	134.0	--	pF
Reverse Transfer Capacitance	F= 1 MHz	C_{rss}	--	9.4	--	pF
Switching Paramters (Test Circuit & Waveform See Fig.14)						
Turn-On Delay Time	VDS= 75 V	$t_{d(on)}$	--	12.8	--	ns
Turn-On Rise Time	VGS= 10 V	t_r	--	3.0	--	ns
Turn-Off Delay Time	RL= 1.2 Ω	$t_{d(off)}$	--	17.4	--	ns
Turn-Off Rise Time	RG= 10 Ω	t_f	--	8.6	--	ns
Gate Charge Paramters (Test Circuit & Waveform See Fig.15)						
Total Gate Charge	VDS= 75 V	Q_g	--	12.0	--	nC
Gate-Source Charge	VGS= 10 V	Q_{gs}	--	4.3	--	nC
Gate-Drain Charge	ID= 15 A	Q_{gd}	--	2.0	--	nC
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.17)						
Max. Diode Forward Current		I_S	--	--	29	A
Max. Pulsed Forward Current		I_{SM}	--	--	102	A
Diode Forward Voltage	ID= 15 A, VGS=0V	V_{SD}	--	0.86	1.2	V
Reverse Recovery Time	ID= 15 A, di/dt= 100 A/us	t_{rr}	--	60	--	ns
Reverse Recovery Charge	VGS= 10 V, VDS= 75 V	Q_{rr}	--	103.0	--	nC

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Typical Characteristics Curves

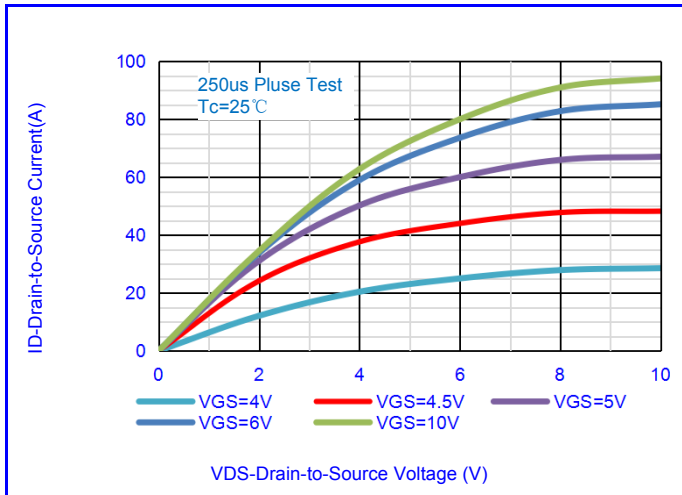


Fig.1-Output Characteristics

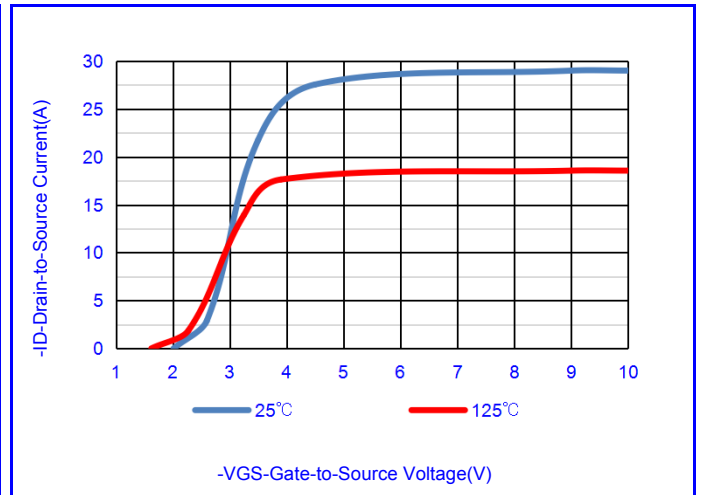


Fig.2- Transfer Characteristics

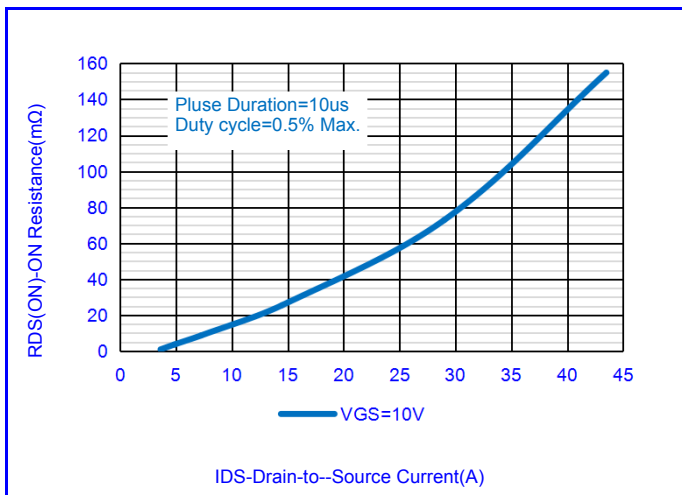


Fig.3- On Resistance vs. Drain Current

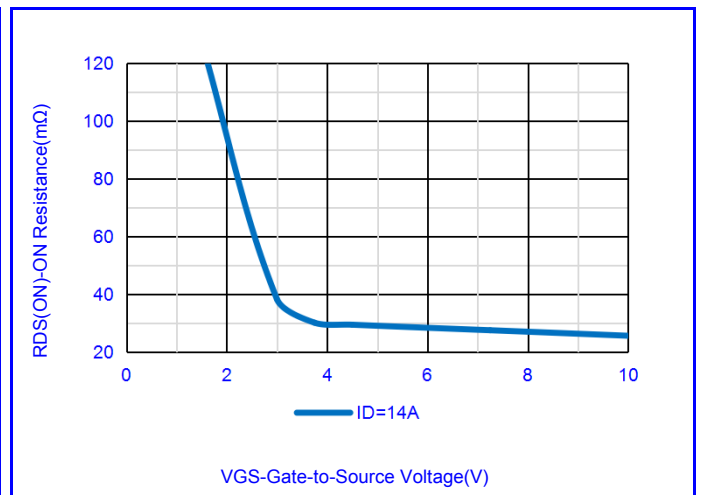


Fig.4- On Resistance vs. Gate Source Voltage

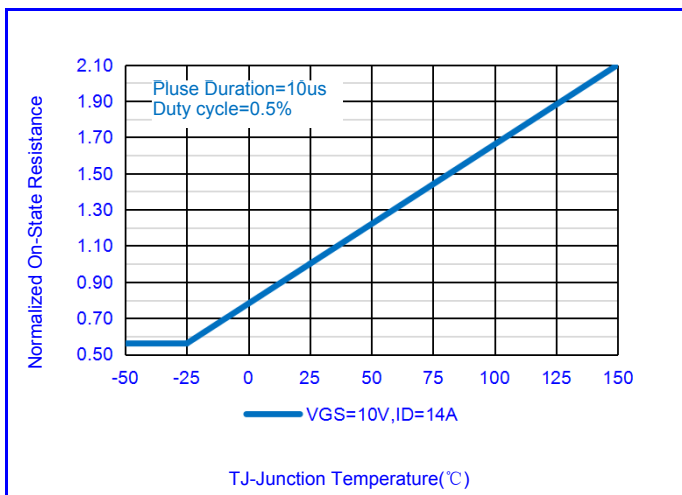


Fig.5- On Resistance vs. Junction Temperature

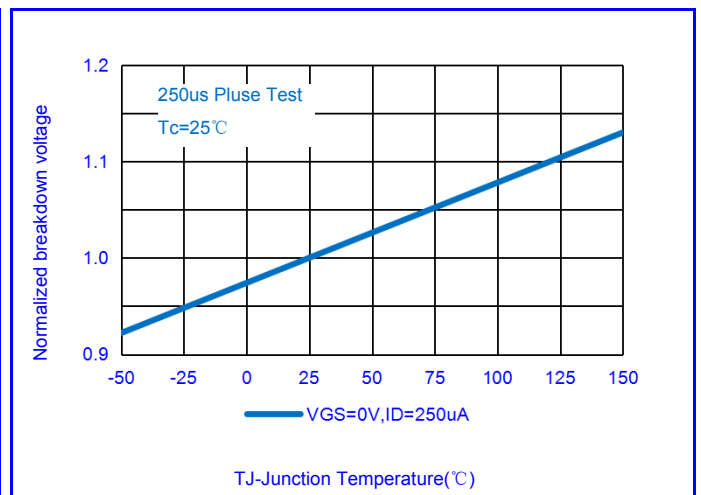


Fig.6- Breakdown Voltage vs. Junction Temperature

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Typical Characteristics Curves

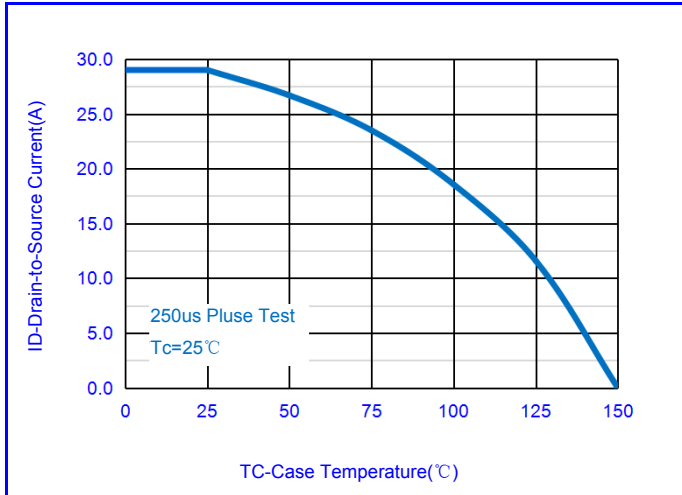


Fig.7-Maximum Continuous Drain Current vs. Case Temperature

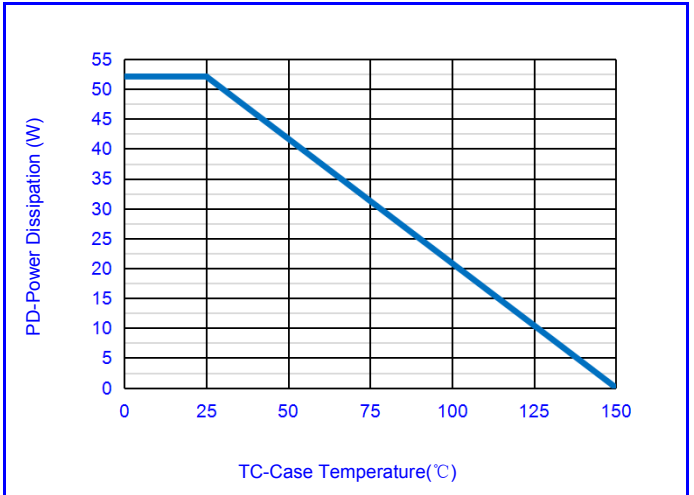


Fig.8-Maximum Power Dissipation vs. Case Temperature

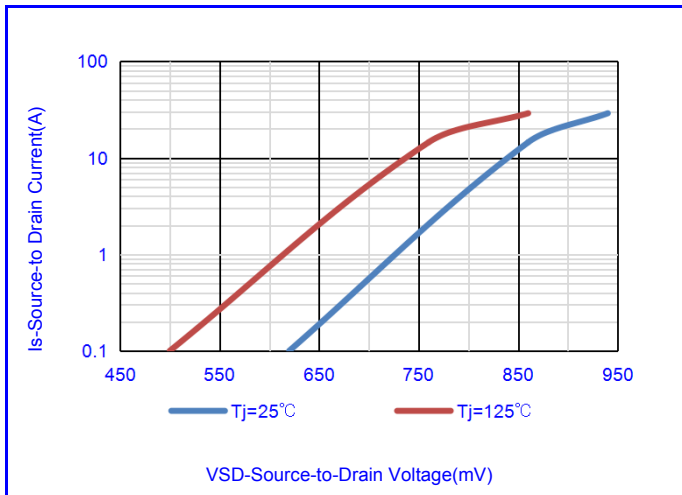


Fig.9- Source-Drain Diode Forward Voltage

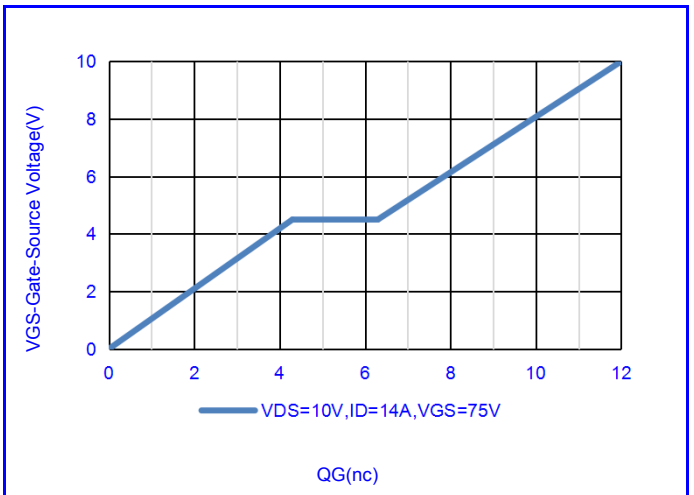


Fig.10-Gate Charge Waveform

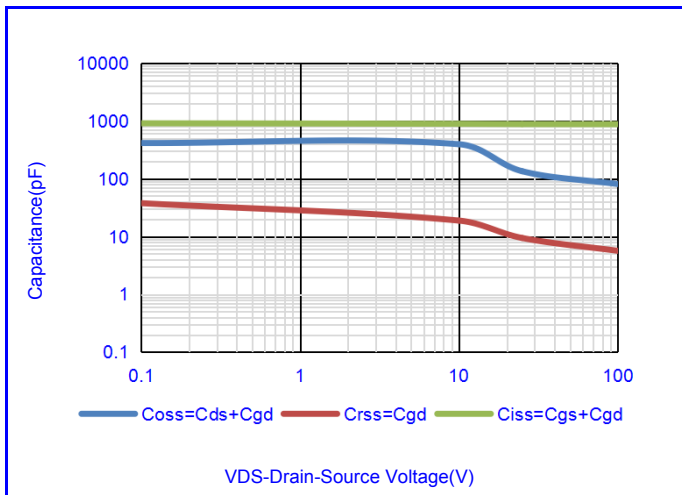


Fig.11- Gate-Source Voltage-VGS(V)

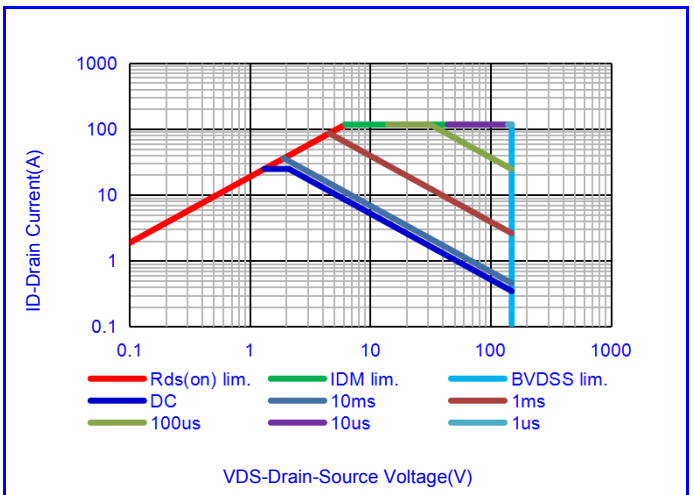


Fig.12-Maximum Safe Operating Area(SOA)

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Typical Characteristics Curves

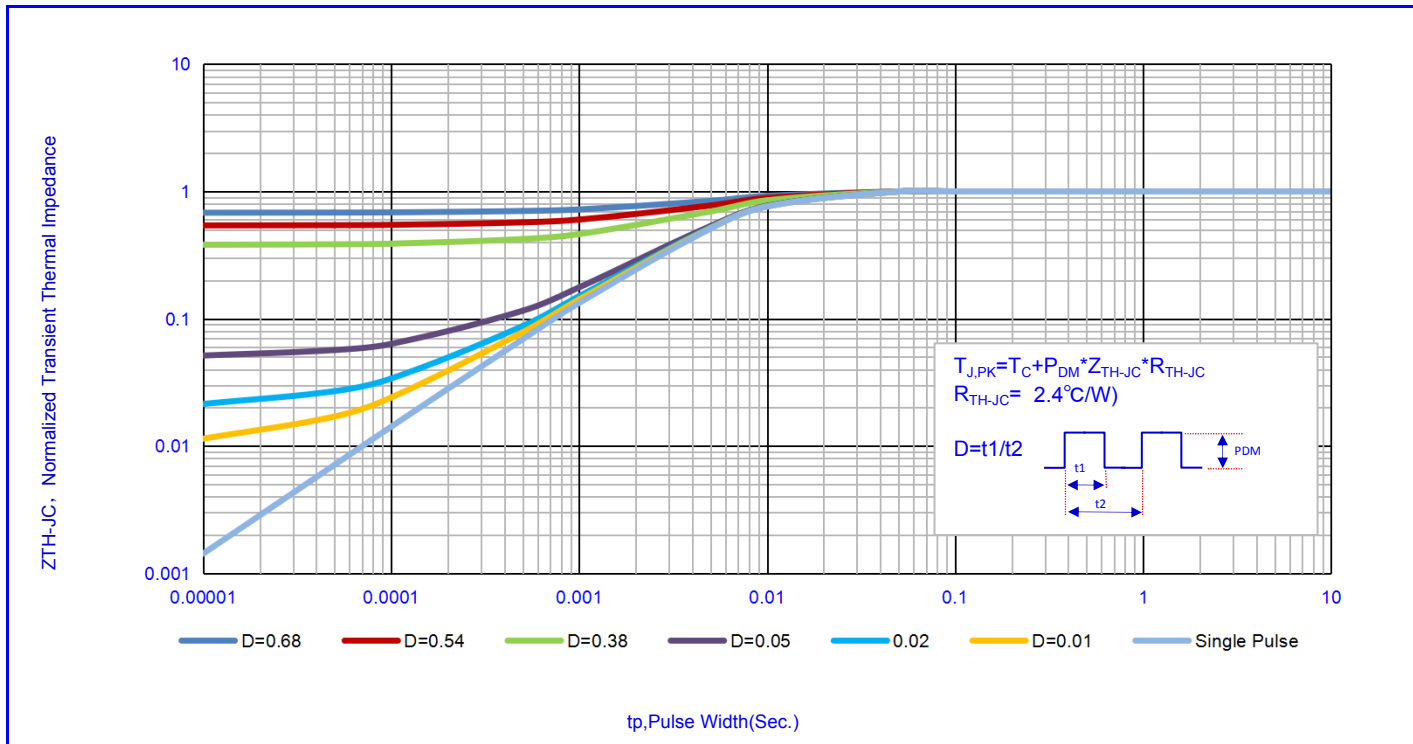


Fig.13- Normalized Maximum Transient Thermal Impedance vs.Pulse Width

Test Circuit & Waveform

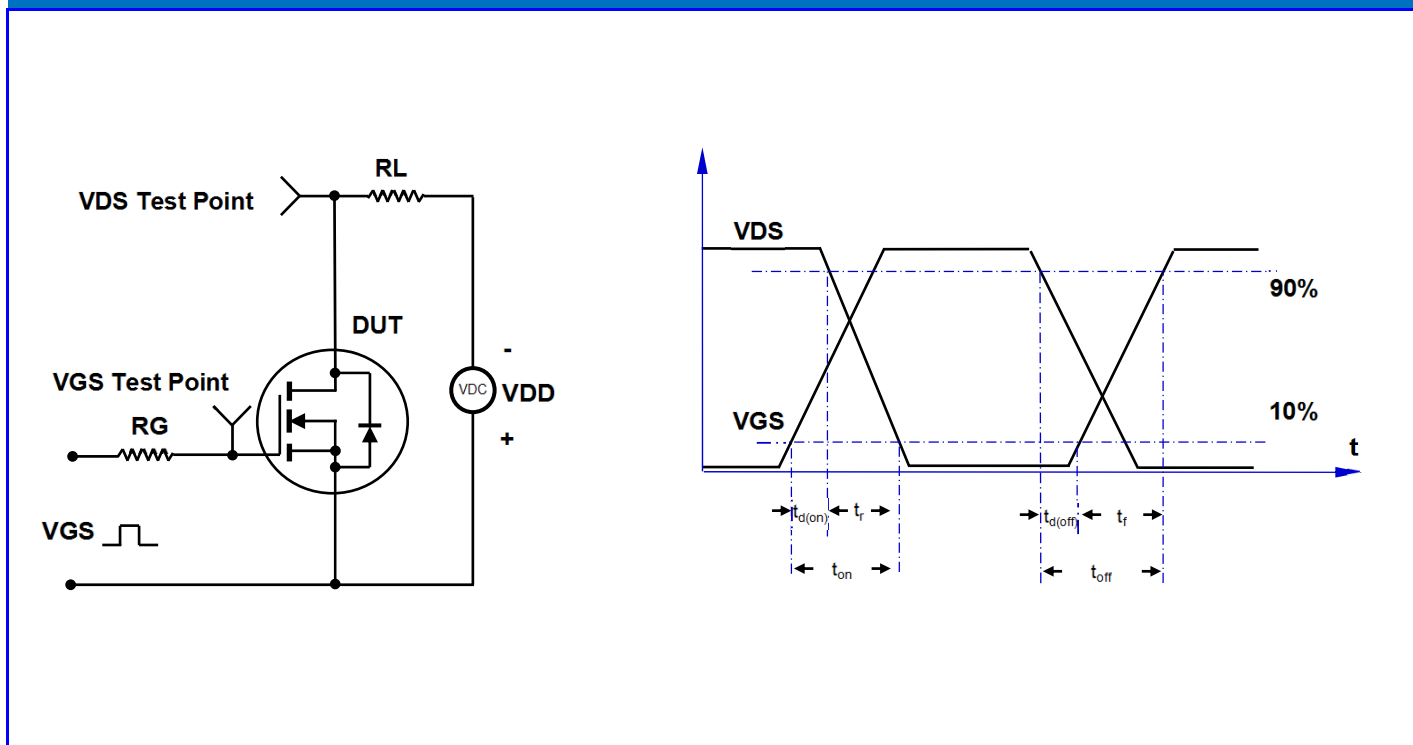


Fig.14- Resistive Switching Test Circuit & Waveform

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Test Circuit & Waveform

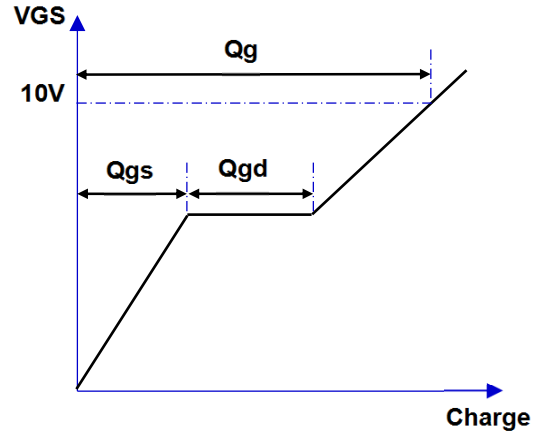
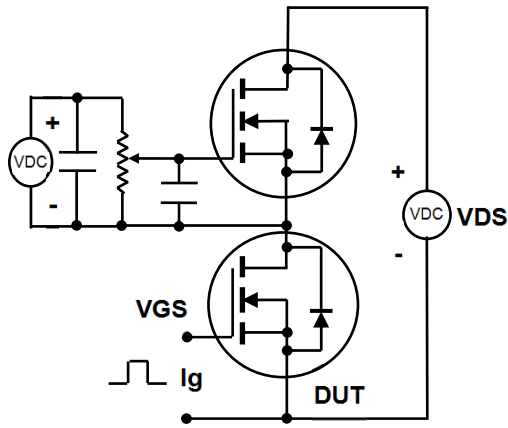


Fig.15-Gate Charge Test Circuit & Waveform

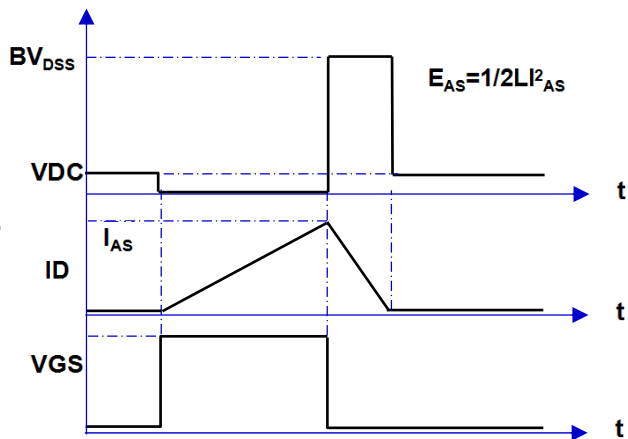
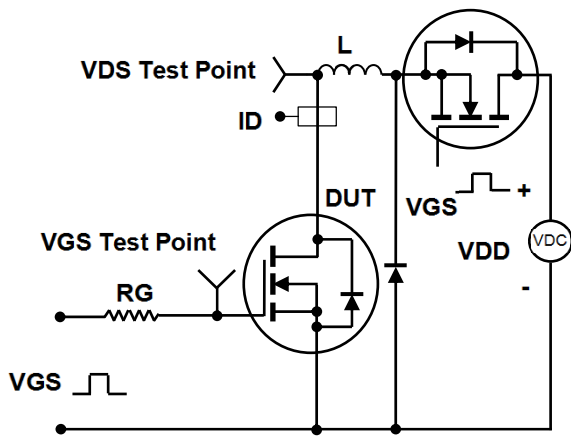


Fig.16- EAS Test Circuit & Waveform

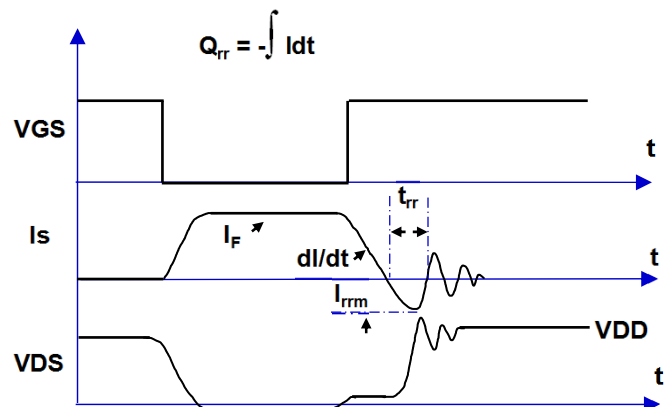
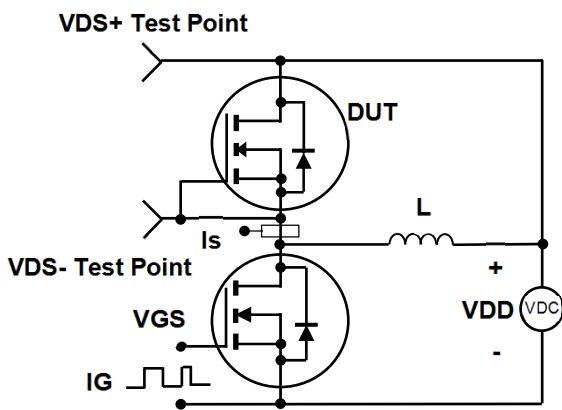


Fig.17- Diode Recovery Test Circuit & Waveform

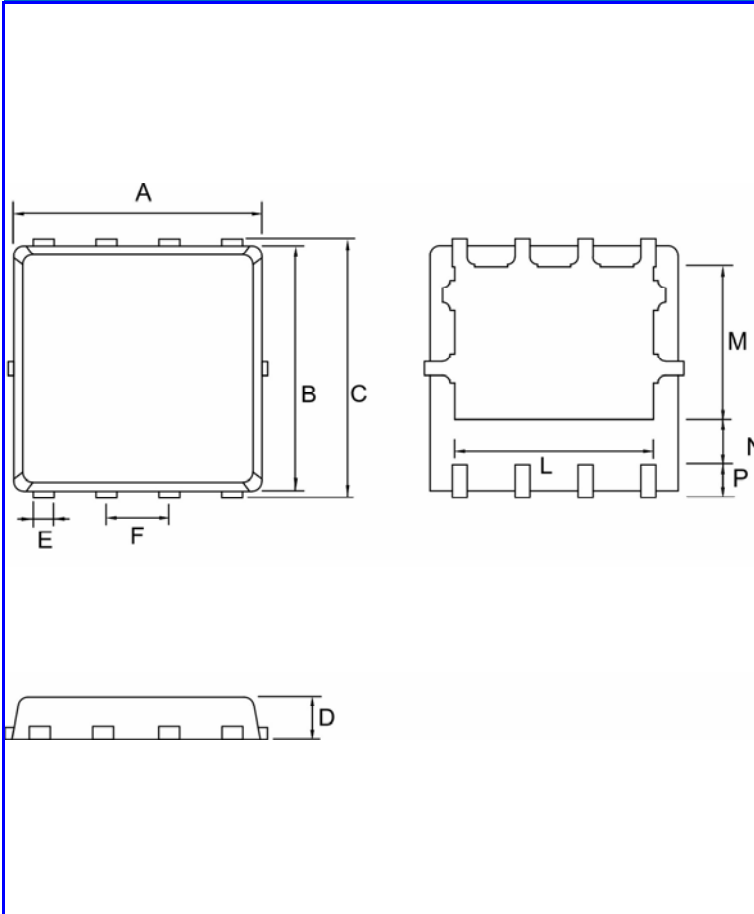
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OUTLINE DRAWINGS

PDFN3*3



OUTLINE DIMENSIONS						
Dim.	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.800	3.150	3.600	0.110	-	0.142
B	2.700	3.100	3.500	0.106	-	0.138
C	3.000	3.340	3.800	0.118	-	0.150
D	0.550	0.790	1.050	0.022	-	0.041
E	0.150	0.280	0.450	0.006	-	0.018
F	0.650	0.900	1.150	0.026	-	0.045
M	1.500	1.800	2.100	0.059	-	0.083
N	0.450	0.700	0.950	0.018	-	0.037
P	0.200	0.420	0.600	0.008	-	0.024

PACKING INFORMATION

Package Code	Package Method	Inner Box Size L×W×H(mm)	Quantity (Pcs/Inner Box)	Outer Carton Size L×W×H(mm)	Quantity (Pcs/Carton)
PDFN3*3	T/R	370×360×47	10000	380×295×375	60000

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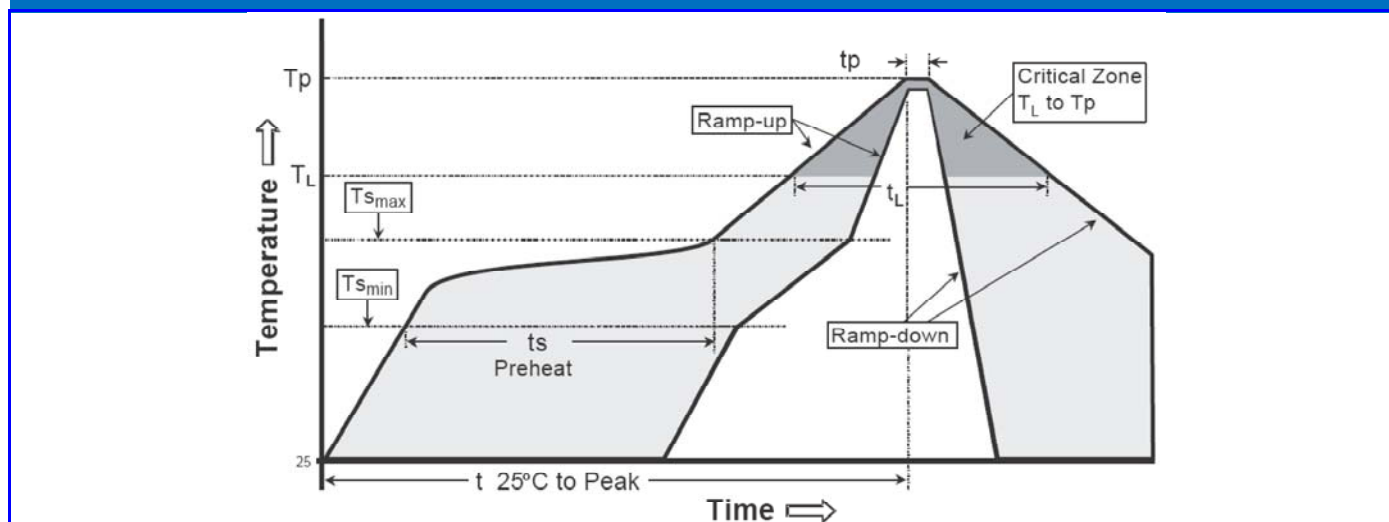
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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(T _{s min}) -Temperature Max(T _{s max}) -Time(t _{s min} to t _{s max})	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t _p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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