**Product data sheet** 

# 1. General description

PNP Darlington transistor in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

NPN complement: BST50-Q

### 2. Features and benefits

- Integrated diode and resistor
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

- Industrial switching applications such as:
  - · Print hammer
  - Solenoid
  - · Relay and lamp driving

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-45	V
I <sub>C</sub>	collector current		-	-	-1	Α
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -10 V; $I_{C}$ = -150 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	1000	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		2
2	С	collector		3
3	В	base	3 2 1 SOT89	sym081



## **PNP Darlington transistor**

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	mber Package				
	Name	Description	Version		
BST60-Q		plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89		

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BST60-Q	BS1

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-1	Α
I <sub>CM</sub>	peak collector current			-	-2	Α
I <sub>B</sub>	base current			-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.3	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

## **PNP** Darlington transistor

## 10. Characteristics

**Table 7. Characteristics** 

Parameter	Conditions		Min	Тур	Max	Unit
emitter-base cut-off current	$V_{EB} = -4 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	-50	nA
collector-emitter cut-off current	$V_{CE} = -45 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ °C}$		-	-	-50	nA
DC current gain	$V_{CE}$ = -10 V; $I_{C}$ = -150 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C		1000	-	-	
	$V_{CE}$ = -10 V; $I_{C}$ = -500 mA; pulsed; $t_{p}$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C		2000	-	-	
collector-emitter saturation voltage	$I_C$ = -500 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C		-	-	-1.3	V
	$I_C$ = 500 mA; $I_B$ = -0.5 mA; $T_j$ = 150 °C		-	-	-1.3	V
base-emitter saturation voltage	$I_C$ = -500 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C		-	-	-1.9	V
transition frequency	$V_{CE}$ = -5 V; $I_{C}$ = -500 mA; f = 100 MHz; $T_{amb}$ = 25 °C		-	200	-	MHz
es (between 10% and 90°	% levels)					'
turn-on time	I <sub>Bon</sub> = -0.5 mA; I <sub>Boff</sub> = 0.5 mA; I <sub>Con</sub> =		-	500	-	ns
turn-off time	-500 mA; T <sub>amb</sub> = 25 °C		-	700	-	ns
	emitter-base cut-off current  collector-emitter cut-off current  DC current gain  collector-emitter saturation voltage  transition frequency  es (between 10% and 90 turn-on time	emitter-base cut-off current $V_{EB} = -4 \text{ V; } I_{C} = 0 \text{ A; } T_{amb} = 25 \text{ °C}$ collector-emitter cut-off current $V_{CE} = -45 \text{ V; } V_{BE} = 0 \text{ V; } T_{amb} = 25 \text{ °C}$ current $V_{CE} = -10 \text{ V; } I_{C} = -150 \text{ mA; pulsed; } t_{p} \leq 300 \text{ µs; } \delta \leq 0.02; T_{amb} = 25 \text{ °C}$ $V_{CE} = -10 \text{ V; } I_{C} = -500 \text{ mA; pulsed; } t_{p} \leq 300 \text{ µs; } \delta \leq 0.02; T_{amb} = 25 \text{ °C}$ $V_{CE} = -10 \text{ V; } I_{C} = -500 \text{ mA; pulsed; } t_{p} \leq 300 \text{ µs; } \delta \leq 0.02; T_{amb} = 25 \text{ °C}$ $I_{C} = -500 \text{ mA; } I_{B} = -0.5 \text{ mA; } T_{amb} = 25 \text{ °C}$ $I_{C} = -500 \text{ mA; } I_{B} = -0.5 \text{ mA; } T_{j} = 150 \text{ °C}$ base-emitter saturation voltage $I_{C} = -500 \text{ mA; } I_{B} = -0.5 \text{ mA; } T_{j} = 150 \text{ °C}$ transition frequency $V_{CE} = -5 \text{ V; } I_{C} = -500 \text{ mA; } f = 100 \text{ MHz; } T_{amb} = 25 \text{ °C}$ es (between 10% and 90% levels) $I_{Bon} = -0.5 \text{ mA; } I_{Boff} = 0.5 \text{ mA; } I_{Con} = 500 \text{ mA; } T_{con} = 25 \text{ °C}$	emitter-base cut-off current $V_{EB} = -4 \text{ V; } I_{C} = 0 \text{ A; } T_{amb} = 25 \text{ °C}$ collector-emitter cut-off current $V_{CE} = -45 \text{ V; } V_{BE} = 0 \text{ V; } T_{amb} = 25 \text{ °C}$ current $DC \text{ current gain}$ $V_{CE} = -10 \text{ V; } I_{C} = -150 \text{ mA; pulsed; } t_{p} \leq 300 \text{ µs; } \delta \leq 0.02; T_{amb} = 25 \text{ °C}$ $V_{CE} = -10 \text{ V; } I_{C} = -500 \text{ mA; pulsed; } t_{p} \leq 300 \text{ µs; } \delta \leq 0.02; T_{amb} = 25 \text{ °C}$ collector-emitter saturation voltage $I_{C} = -500 \text{ mA; } I_{B} = -0.5 \text{ mA; } T_{amb} = 25 \text{ °C}$ $I_{C} = 500 \text{ mA; } I_{B} = -0.5 \text{ mA; } T_{j} = 150 \text{ °C}$ base-emitter saturation voltage $I_{C} = -500 \text{ mA; } I_{B} = -0.5 \text{ mA; } T_{j} = 150 \text{ °C}$ transition frequency $V_{CE} = -5 \text{ V; } I_{C} = -500 \text{ mA; } f = 100 \text{ MHz; } T_{amb} = 25 \text{ °C}$ es (between 10% and 90% levels) $I_{Bon} = -0.5 \text{ mA; } I_{Boff} = 0.5 \text{ mA; } I_{Con} = 500 \text{ mA; } T_{con} = 25 \text{ °C}$	$\begin{array}{c} \text{emitter-base cut-off} \\ \text{current} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	emitter-base cut-off current $V_{EB} = -4 \text{ V}; \ I_{C} = 0 \text{ A}; \ T_{amb} = 25 \text{ °C}$ Collector-emitter cut-off current $V_{CE} = -45 \text{ V}; \ V_{BE} = 0 \text{ V}; \ T_{amb} = 25 \text{ °C}$	$\begin{array}{c} \text{emitter-base cut-off} \\ \text{current} \\ \\ \text{collector-emitter cut-off} \\ \text{current} \\ \\ \text{DC current gain} \\ \\ \text{DC current gain} \\ \\ \text{V}_{CE} = -45 \text{ V}; \text{ V}_{BE} = 0 \text{ V}; \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{V}_{CE} = -10 \text{ V}; \text{ I}_{C} = -150 \text{ mA}; \text{ pulsed}; \text{ t}_{p} \leq \\ 300 \text{ µs}; \delta \leq 0.02; \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{V}_{CE} = -10 \text{ V}; \text{ I}_{C} = -500 \text{ mA}; \text{ pulsed}; \text{ t}_{p} \leq \\ 300 \text{ µs}; \delta \leq 0.02; \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{V}_{CE} = -10 \text{ V}; \text{ I}_{C} = -500 \text{ mA}; \text{ pulsed}; \text{ t}_{p} \leq \\ 300 \text{ µs}; \delta \leq 0.02; \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{I}_{C} = -500 \text{ mA}; \text{ I}_{B} = -0.5 \text{ mA}; \\ \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{I}_{C} = 500 \text{ mA}; \text{ I}_{B} = -0.5 \text{ mA}; \\ \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{transition frequency} \\ \\ \text{V}_{CE} = -5 \text{ V}; \text{ I}_{C} = -500 \text{ mA}; \text{ f} = 100 \text{ MHz}; \\ \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{transition frequency} \\ \\ \text{V}_{CE} = -5 \text{ V}; \text{ I}_{C} = -500 \text{ mA}; \text{ f} = 100 \text{ MHz}; \\ \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{transition frequency} \\ \\ \text{V}_{CE} = -5 \text{ V}; \text{ I}_{C} = -500 \text{ mA}; \text{ f} = 100 \text{ MHz}; \\ \text{ T}_{amb} = 25 \text{ °C} \\ \\ \text{turn-on time} \\ \\ \\ \text{IB}_{Bon} = -0.5 \text{ mA}; \text{ IB}_{Boff} = 0.5 \text{ mA}; \text{ I}_{Con} = \\ - 500 \text{ mA}; \text{ I}_{Con} = \\ - 500 \text{ mA}; \text{ T}_{A} = -25 \text{ °C} \\ \\ \text{T}_{A} = -25 \text{ °C} \\ \\ \text{T}_{A}$

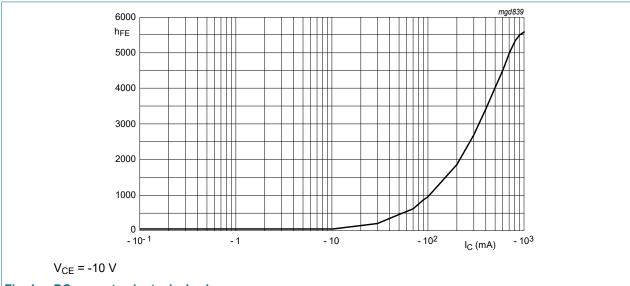
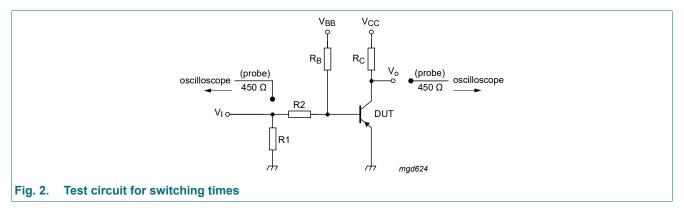


Fig. 1. DC current gain; typical values

**PNP Darlington transistor** 

## 11. Test information

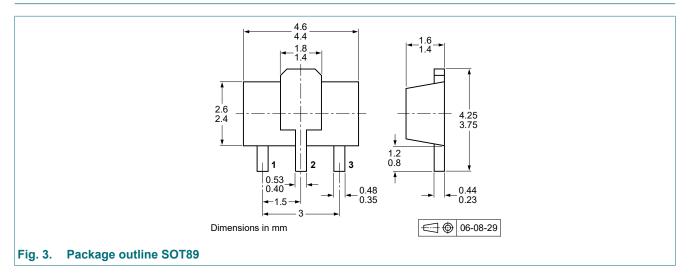


 $V_i$  = -10 V; T = 200 μs; tp = 6 μs;  $t_r$  =  $t_f$  ≤ 3 ns R1 = 56 Ω; R2 = 10 kΩ; R<sub>B</sub> = 10 kΩ; R<sub>C</sub> = 18 Ω  $V_{BB}$  = 1.8 V;  $V_{CC}$  = -10.7 V Oscilloscope: input impedance  $Z_i$  = 50 Ω

### **Quality information**

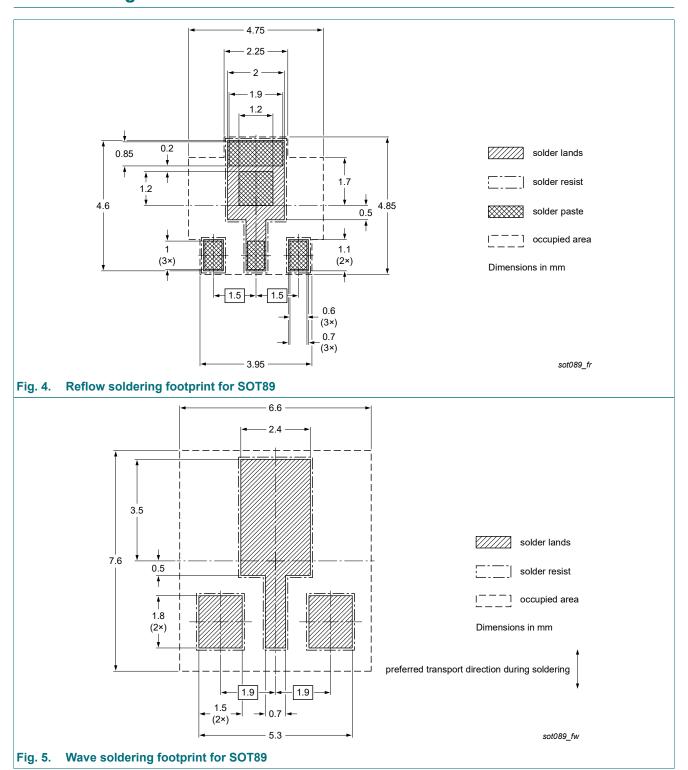
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



### **PNP Darlington transistor**

# 13. Soldering



## **PNP** Darlington transistor

# 14. Revision history

#### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BST60-Q v.1	20231027	Product data sheet	-	-

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### **PNP Darlington transistor**

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 27 October 2023

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