

40V Low-Power LDO Regulator

GENERATION DESCRIPTION

The LW65XX is a 40V low-power high accuracy LDO regulator. The $2.5\mu A$ power consumption makes it ideal for most HV power-saving systems. The maximum operating voltage can be as high as 40V. The output accuracy is as excellent as $\pm 1.0\%$.

The other features include low dropout voltage, current limiting protection and thermal shutdown protection.

The LW65XX is available in SOT23-3L, SOT23-5L and SOT89-3 packages.

APPLICATIONS

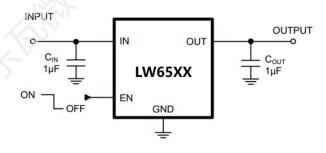
- ➤ Battery Supplied Systems
- > Telecom Systems
- ➤ Audio & Video Devices

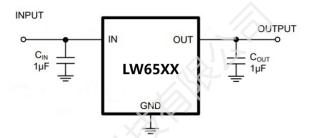
FEATURES

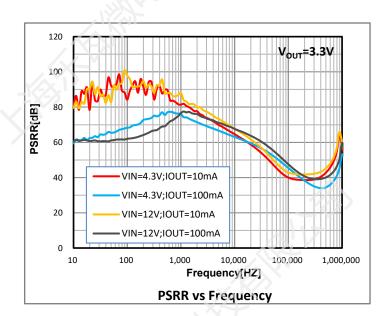
- Maximum Operating Voltage: 40V
- > Output Voltage: 1.5V~5.0V, step=100mV
- ➤ Output Accuracy: ±1.0%
- Low Power Consumption: 2.5μA
- > <0.2μA Standby Current
- Current Limiting, Thermal Shutdown
- Available in SOT23-3L, SOT23-5L and SOT89-3 Packages

■ TYPICAL APPLICATION CIRCUIT

■ TYPICAL PERFORMANCE CHARACTERISTCS





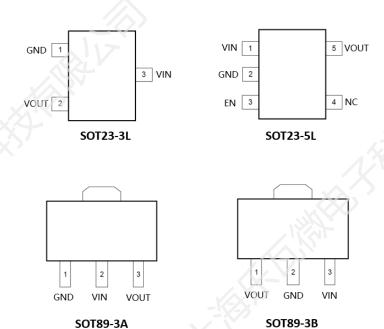


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■ PIN DESCRIPTION

	PIN No			CYAMPON	PEGGDIPEETON
SOT23-3L	SOT23-5L	SOT89-3A	SOT89-3B	SYMBOL	DESCRIPTTION
1	2	1	2	GND	Ground
2	5	3	1	VOUT	Output
3	1	2	3	VIN	Power Supply Input
-	3	-	5	EN	Chip Enable
-	4	-	-/ \	NC	Not Connected

■ PIN ASSIGNMENT



■ MARK INFORMATION:

SOT23-3L, SOT23-5L, SOT89-3

XX: VOLTAGE

YY: DATE CODE

LW65XX YYYYY



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ABSOLUTE MAXIMUM RATINGS (1)

(T_A =25°C, unless otherwise specified.)

Symbol	Item	Rating	Unit	
$V_{ m IN}$	Supply Voltage		-0.3~44	V
V_{EN}	EN pin to GND Voltage	(X-V	-0.3~44	V
V _{OUT}	VOUT pin to GND Voltage	×12	-0.3~6.0	V
V _(ESD)	ESD Susceptibility, HBM ⁽²⁾	7	±2000	V
D	Manimum Parray Disaination(3)	SOT23-3/5L	0.5	W
P_{D}	Maximum Power Dissipation ⁽³⁾	SOT89-3	1.0	W
D	I	SOT23-3/5L	250	°C/W
$R_{\theta JA}$	Junction-to-ambient Thermal Resistance ⁽³⁾	SOT89-3	125	°C/W
$T_{\rm J}$	Junction Temperature Range		-40~150	°C
T_{STG}	Storage Temperature Range		-40~150	°C
T _{SOLDER}	Lead Temperature (Soldering)		260°C, 10s	

Note:

- 1. Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability
- 2. per ANSI/ESDA/JEDEC JS-001
- 3. Device mounted on FR-4 PCB

■ RECOMMANDED OPERATING RANGE:

SYMBOL	ITEM	VALUE	UNIT
$ m V_{IN}$	VIN Supply Voltage	2.5~40	V
V_{EN}	EN Pin Voltage	0~40	V
V_{OUT}	V _{OUT} Pin Voltage	1.5~5.0	V
I_{OUT}	Output Current	0~150	mA
T_{J}	Junction Temperature Range	-40~125	$^{\circ}\mathbb{C}$

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■ ELECTRICAL CHARACTERISTICS

($V_{IN}=V_{OUT}+1V$, $V_{OUT}=3.3V$, $C_{IN}=C_{OUT}=1uF$, $T_A=25\,^{\circ}C$, unless otherwise specified.)

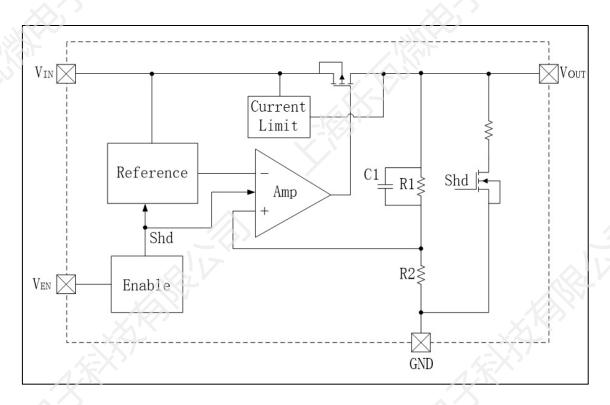
Symbol	Parameter	Test Condit	tions	MIN	TYP	MAX	Units
V _{IN}	Input Voltage		, ((-))	2.5		40	V
V _{OUT}	Output Accuracy	I _{OUT} = 1mA		-1.0		+1.0	%
I_{LIM}	Current Limit	V_{IN} =4.3V, V_{OUT} =3.3V	(2)	150	210		mA
		$V_{IN} = V_{EN} = V_{OUT} + 1V$	V _{OUT} <1.8V		3.0	4.0	
I_Q	Quiescent Current	No Load	$V_{OUT} \geqslant 1.8V$		2.5	3.5	μΑ
I_{SHD}	Shutdown Current	V _{IN} =12V, V _{EN} =0V			0.1	0.2	μΑ
		I _{OUT} =100mA, V _{OUT} =1.8V			700		
V_{DROP}	Dropout Voltage(1)	$I_{OUT} = 100 \text{mA}, V_{OUT} = 3.3 \text{V}$			450		mV
		I _{OUT} =100mA, V _{OUT} =5.0V			360		
S _{LINE}	Line Regulation	V _{IN} = V _{OUT} +1V to 40V, I _{OUT} =1mA			0.02	0.05	%/V
S_{LOAD}	Load Regulation	1mA≤I _{OUT} ≤150mA			0.002	0.005	%/mA
I _{SHORT}	Short Current	V _{OUT} =0V			15		mA
V_{ENH}	EN High Voltage	V =2.5V to 40V I	—1 ··· A	1.5	X		V
V_{ENL}	EN Low Voltage	V_{IN} =2.5V to 40V, I_{OUT}	-IIIIA		\X	0.5	V
		V -5 0V	f=217Hz		86		
PSRR	Power Supply	V_{IN} =5.0V, C_{IN} =None,	f=1KHz		81		dB
PSRR	Rejection Ratio	I _{OUT} =10mA	f=10KHz		64		ав
		IOUT-TOINA	f=100KHz		40		
T_{SD}	Overheat Protection	Temperature rising			165		$^{\circ}\!\mathbb{C}$
$\triangle T_{SD}$	TSD Hysteresis	Temperature falling			15		$^{\circ}$ C
R _{DSCHG}	R _{ON} of V _{OUT} Discharge MOSFET	V_{IN} = 12V, V_{OUT} =5.0V	, V _{EN} =0V		130		Ω

NOTES:

1. The dropout voltage is defined as V_{IN} - $V_{\text{OUT}},$ when $V_{\text{OUT}}\!=\!95\%*V_{\text{OUT}(\text{NOM})}$



■ SIMPLIFIED BLOCK DIAGRAM



■ DETAIL OPERATION DESCRIPTION

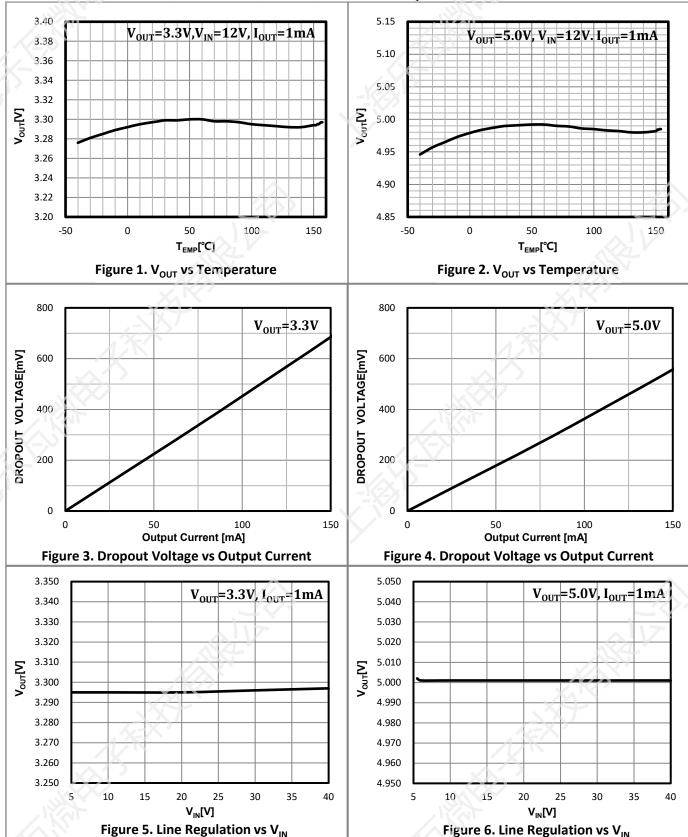
The LW65XX is a low power consumption low drop-out voltage regulator. It consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error correction circuit, and is compatible with low ESR ceramic capacitors. The current limiter's fold-back circuit operates as a short circuit protection as well as the output current limiter.

Current Limiting and Short-Circuit Protection

The current limit circuitry prevents damage to the MOSFET switch and the hub downstream port but can deliver load current up to the current limit threshold through the switch. When a heavy load or short circuit is applied to an enabled switch, a large transient current may flow until the current limit circuitry responds. Once this current limit threshold is exceeded the device enters constant current mode until the thermal shutdown occurs or the fault is removed.

■ TYPICAL OPERATING CHARACTERISTICS:

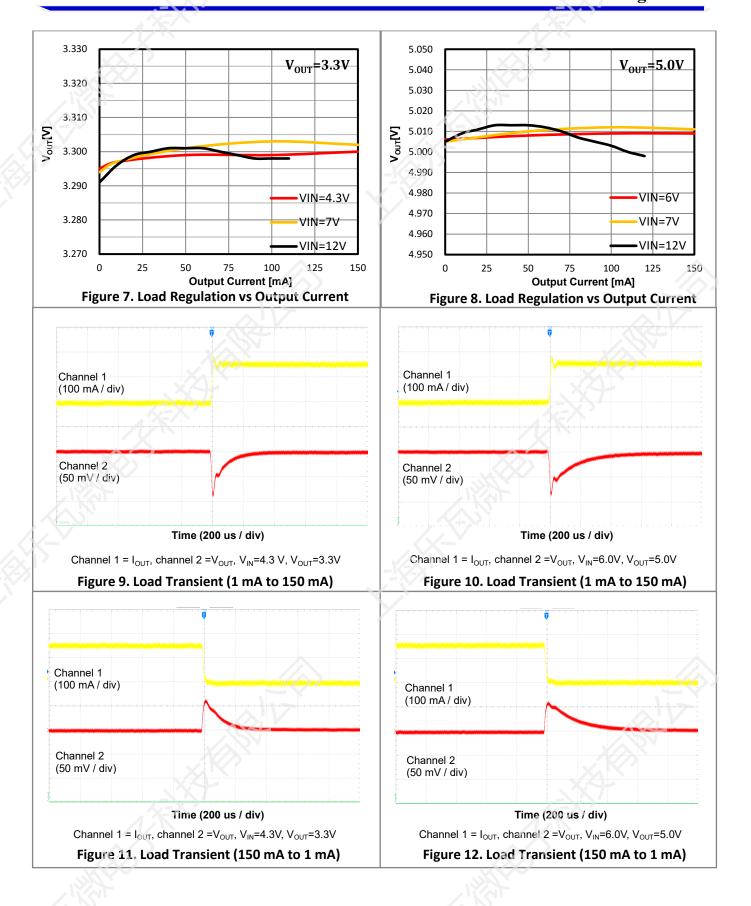
(Tested under $V_{IN}=V_{OUT}+1V$, $C_{IN}=C_{OUT}=1uF$, $T_A=25$ °C, unless otherwise specified)



上海乐瓦微电子科技有限公司 www.lewa-micro.com LW65XX Series Rev. 1.0 Nov.2023

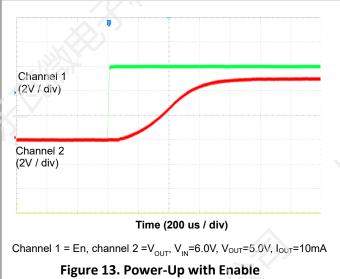


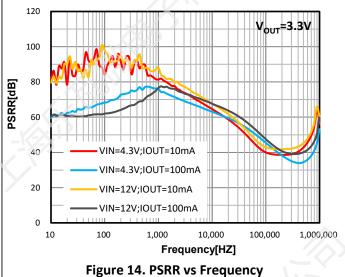
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■ ORDER INFORMATION

LW65123456

Designator	Item	Symbol	Description
12	Output Voltage	18~50	e.g.2.8V→①=2,②=8
3456		A23D	SOT23-3L
	D 1	A23E	SOT23-5L
	Packages	A89C	SOT89-3A
		B89C	SOT89-3B

Part #	Output Voltage	Package	Shipping
LW6518A23D	1.8V		117
LW6525A23D	2.5V		
LW6530A23D	3.0V		3/12/
LW6533A23D	3.3V	GOT22 21	2000 D /T 0 D 1
LW6536A23D	3.6V	SOT23-3L	3000 Pcs / Tape & Reel
LW6540A23D	4.0V		135
LW6542A23D	4.2V		
LW6550A23D	5.0V		
LW6518A23E	1.8V		39
LW6525A23E	2.5V		12.
LW6530A23E	3.0V		
LW6533A23E	3.3V	COT22 51	2000 Bee / Tene 9 Beel
LW6536A23E	3.6V	SOT23-5L	3000 Pcs / Tape & Reel
LW6540A23E	4.0V		
LW6542A23E	4.2V		
LW6550A23E	5.0V		
LW6518A89C	1.8V		//
LW6525A89C	2.5V		,<
LW6530A89C	3.0V		
LW6533A89C	3.3V	COT00 24	1000 B /T 0 B 1
LW6536A89C	3.6V	SOT89-3A	1000 Pcs / Tape & Reel
LW6540A89C	4.0V		XXX
LW6542A89C	4.2V		4EX
LW6550A89C	5.0V		



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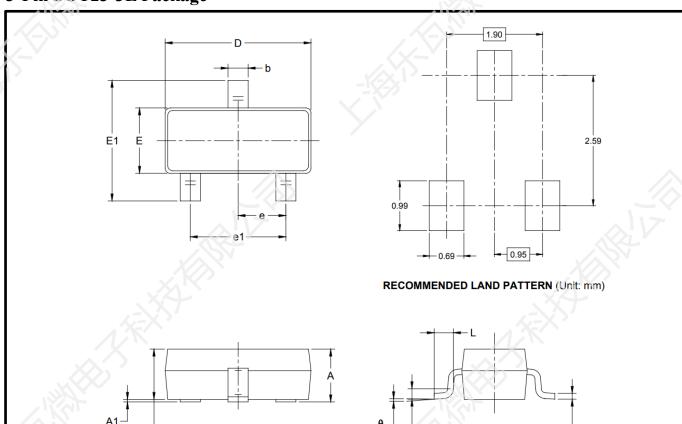
	T				
Part #	Output Voltage	Package	Shipping		
LW6518B89C	1.8V				
LW6525B89C	2.5V				
LW6530B89C	3.0V		->0		
LW6533B89C	3.3V		1000 B /T 0 B 1		
LW6536B89C	3.6V	SOT89-3B	1000 Pcs / Tape & Reel		
LW6540B89C	4.0V	V-1/2			
LW6542B89C	4.2V				
LW6550B89C	5.0V				

If customers have special output voltage requirements, please contact us.



PACKAGE OUTLINE

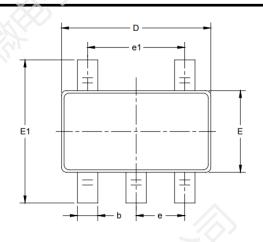
3-Pin SOT23-3L Package

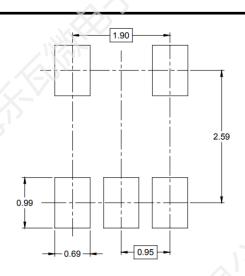


Symbol	Dimensions In Millimeters		Dimensions In Inches	
, , , , , , , , , , , , , , , , , , , ,	MIN	MAX	MIN	MAX
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 BSC		0.037	BSC
e1	1.900	BSC	0.075	BSC
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°_

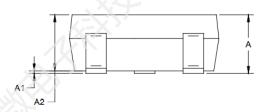


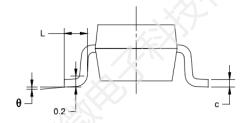
5-Pin SOT23-5L Package





RECOMMENDED LAND PATTERN (Unit: mm)

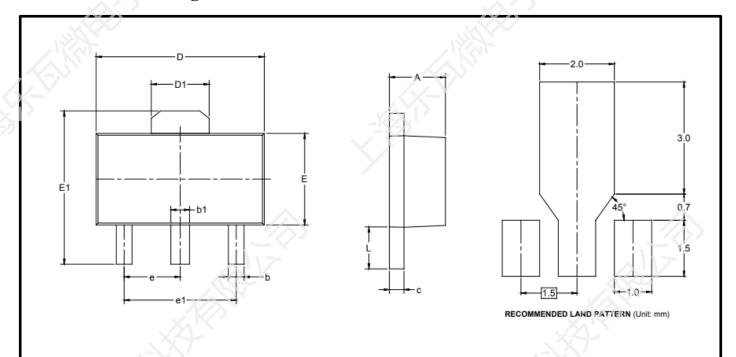




Symbol	Dimensions In Millimeters		Dimensions In Inches	
, I	MIN	MAX	MIN	MAX
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950	BSC	0.037	BSC
e1	1.900	BSC	0.075	BSC
·X L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



3-Pin SOT89-3 Package



Symbol		nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.020	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550 REF		0.061	REF	
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP		0.060 TYP		
e1	3.000) TYP	0.118 TYP		
L	0.900	1.200	0.035	0.047	



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■ REVISION HISTORY

Revision	Date	Descriptions	
Rev 0.1	Dec.2022	Initial Version	
Rev 0.2	Jun.2023	Update Electrical Characteristics	
Rev 1.0	Nov.2023	Formal Version Release	



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■ **DISCLAIMER:**

The information in this document is believed to be accurate and reliable. However, no responsibility is assumed by LW-Micro for its use. All operating parameters must be designed, validated and tested to ensure they meet the requirements of your application. LW-Micro reserves the right to make any specification and/or circuitry changes without prior notification. Before starting a brand-new project, please contact LW-Micro Sales to get the most recent relevant information.

Mailing Address: Room 301, Building 2, No.1690 CaiLun Road, China (Shanghai) Pilot Free Trade Zone Shanghai Lewa Micro-electronics Technology Co., Ltd