# MSKSEMI 美森科













ESD

TV/S

TSS

MOV

GDT

PLED

AO4832-MS

Product specification





## **Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	10mΩ	10A

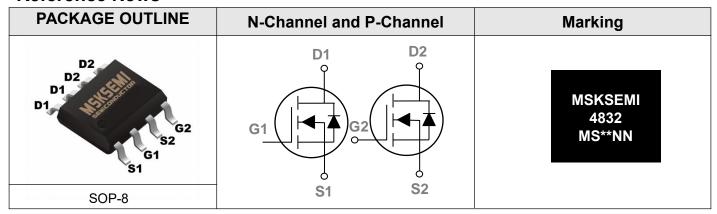
#### **Features**

- 30V,10A, RDS(ON) =  $10m\Omega$  @VGS = 10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

## **Applications**

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

#### **Reference News**



## Absolute Maximum Ratings (T<sub>A</sub>=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units
Vos	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
	Drain Current - Continuous (Tc=25°C)	10	Α
lb	Drain Current - Continuous (Tc=100℃)	6.3	Α
Ірм	Drain Current - Pulsed <sup>1</sup>	40	Α
Po	Power Dissipation (Tc=25°C)	2.1	W
PD	Power Dissipation - Derate above 25℃	0.017	W/℃
Тѕтс	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	${\mathbb C}$

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		60	°C/W



## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	Vgs=0V , Ip=250uA	30			V
△BVDSS/△TJ	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I⊳=1mA		0.04		V/°C
	Drain-Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
IDSS	Diam-Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
Igss	Gate-Source Leakage Current	Vgs= ±20V , Vps=0V			±100	nA

#### On Characteristics

RDS(ON)	RDS(ON) Static Drain-Source On-Resistance <sup>3</sup>	Vgs=10V , ID=8A		10	13	mΩ
TADS(ON)	Statio Brain Source on Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =4A		15	20	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	\/cc=\/pc	1.2	1.6	2.5	V
$^{\triangle}V$ GS(th)	V <sub>GS(th)</sub> Temperature Coefficient	──Vgs=Vds , ld =250uA		-4		mV/°C
gfs	Forward Transconductance	VDS=10V , ID=3A		6		S

## **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>3,4</sup>		 7.4	
Qgs	Gate-Source Charge <sup>3,4</sup>	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A	 2.3	 nC
Qgd	Gate-Drain Charge <sup>3, 4</sup>		 3	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3,4</sup>		 3.8	
Tr	Rise Time <sup>3, 4</sup>	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V ,	 10	 
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>	R <sub>G</sub> =6Ω I <sub>D</sub> =1A	 22	 ns
Tf	Fall Time <sup>3, 4</sup>		 6.6	
Ciss	Input Capacitance		 620	
Coss	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz	 85	 pF
Crss	Reverse Transfer Capacitance		 60	
Rg	Gate resistance	Vgs=0V, Vps=0V, f=1MHz	 2.8	 Ω

## **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			10	Α
lsм	Pulsed Source Current <sup>3</sup>	Vo VB OV , I oros current			20	Α
VsD	Diode Forward Voltage <sup>3</sup>	Vgs=0V , Is=1A , TJ=25°C			1.2	V

#### Note:

<sup>1.</sup>Repetitive Rating: Pulsed width limited by maximum junction temperature.

 $<sup>2.</sup>V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =11A., $R_{G}$ =25 $\Omega$ , $Starting T_{J}$ =25 $^{\circ}C$ .

<sup>3.</sup>The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.

<sup>4.</sup> Essentially independent of operating temperature.



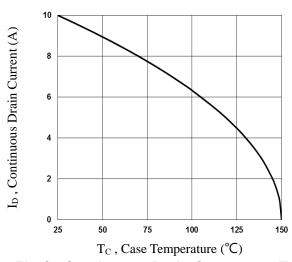


Fig.1 Continuous Drain Current vs. Tc

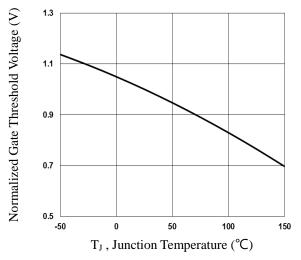


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

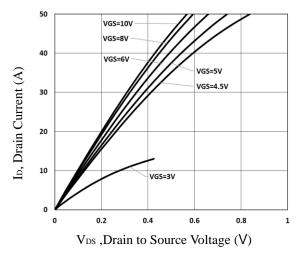


Fig.5 Typical Output Characteristics

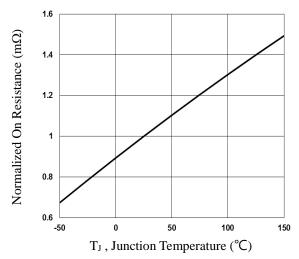


Fig.2 Normalized RDSON vs. T<sub>J</sub>

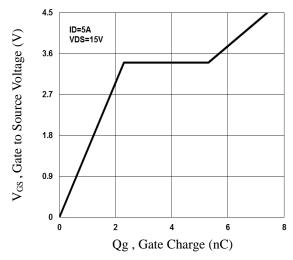


Fig.4 Gate Charge Waveform

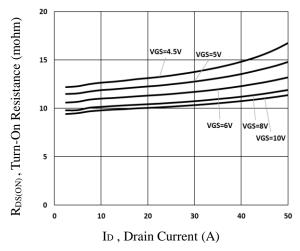


Fig.6 Turn-On Resistance vs. ID



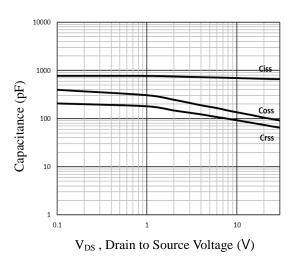


Fig.7 Capacitance Characteristics

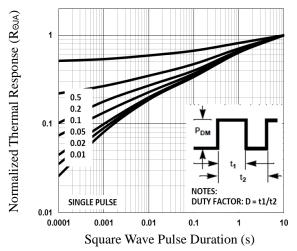


Fig.9 Normalized Transient Response

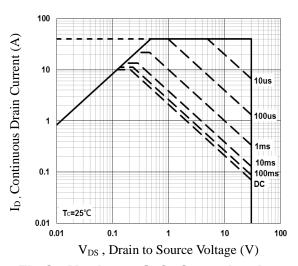


Fig.8 Maximum Safe Operation Area

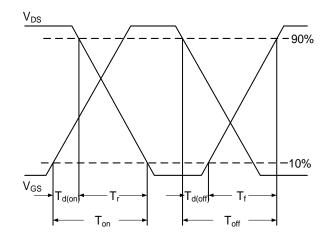
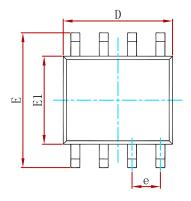
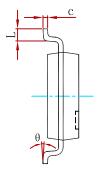


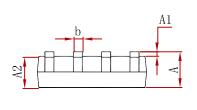
Fig.10 Switching Time Waveform



## **PACKAGEMECHANICALDATA**

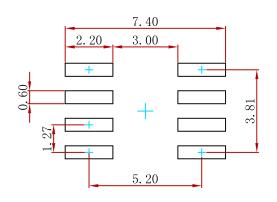






Council of	DimensionsInMillimeters		Dimension	nsInInches
Symbol	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0. 250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0. 250	0.007	0.010
D	4.800	5. 000	0. 189	0. 197
e	1.270 (	1.270 (BSC)		(BSC)
E	5.800	6. 200	0. 228	0. 244
E1	3.800	4. 000	0. 150	0. 157
L	0.400	1. 270	0.016	0.050
θ	0°	8°	0°	8°

# **Suggested Pad Layout**



#### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
- 3. The pad layout is for reference purposes only.

## **REELSPECIFICATION**

P/N	PKG	QTY
AO4832-MS	SOP-8	3000



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