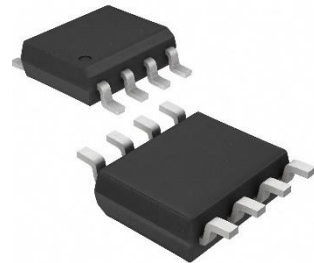


HX8485-S High speed bus transceiver

The HX8485-S is a high-speed RS485/RS-422 transceiver circuit that operates on a +5V power supply with half-duplex functionality. It features one driver and one receiver, achieving transmission speeds of up to 10Mbps. The circuit includes $\pm 15\text{KV}$ ESD protection to safeguard the chip from electrostatic discharge. Both the driver and receiver have enable pins (DE and RE). When these pins are disabled, the output enters a high-resistance state. Additionally, the HX8485-S incorporates a fail-safe circuit that ensures correct output in open or short circuits at the receiver input. The receiver's input impedance is 1/8 unit load, allowing for a maximum of 256 transceivers on the same bus.



SOP-8

Characteristic

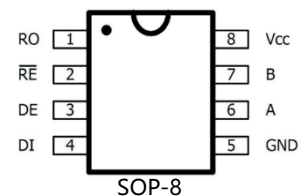
- Tri state output.
- Electrostatic Discharge (ESD): A/B $\pm 15\text{KV}$, compliant with Human Body Mode (HBM) standards.
- The bus allows up to 256 transceivers to be connected.
- Maximum data speed of 10 Mbps
- Adopting SOP8 encapsulation.

Application

- Industrial control
- RS485/RS422
- interface Industrial motor drive
- Automated HVAC system

Chip Pin Description

ID	NUME	Function
1	RO	Receiver data output
2	$\overline{\text{RE}}$	The receiver output is effective at low levels and high when high.
3	DE	SET: High valid when DE low, output high
4	DI	Drive data input
5	GND	Ground
6	A	Driver data output, receiver data input
7	B	Driver data output, receiver data input
8	Vcc	power supply



Driver Truth Table

Input	Enable	Output	
DI	DE	A	B
H	H	H	L
L	H	L	H
X	L	Z	Z

Receiver Truth Table

Input			Output
$\overline{\text{RE}}$	DE	A-B	RO
L	X	$\geq -50\text{mV}$	H
L	X	$\leq -200\text{mV}$	L
L	X	open circuit	H
L	X	short circuit	H
H	H	X	Z
H	L	X	Z

Dc Electrical Parameter							
Limiting Parameter							
SYMBOL	PARAMETER NAME	M I N	MAX	UNIT			
VCC	Supply voltage		+6.0	V			
DE, RE	Control input voltage	-0.5	+6.0	V			
DI	Drive input voltage	-0.5	+6.0	V			
A,B	Drive output voltage/receive input voltage	-7.0	+12.0	V			
RO	Receiving output voltage	-0.3	VCC+0.3	V			
TSTG	Storage temperature range	-55	+150	°C			
TOP	Operating temperature range	-40	+85	°C			
TMOP	Maximum operating temperature range	-55	+125	°C			
P _D	SOP-8 (+70°C or above)		470	mW			
T _L	Solder temperature (10 seconds)		+300	°C			
DC characteristics (unless otherwise specified, Vcc=5V ± 5%, TA=25 °C) 2							
ARGUMENT	SYMBOL	Test condition		MIN	TYP	MAX	UNIT
Working voltage range	V _{CC}			4.5		5.5	V
Driver differential output (no load)	VOD1	-		-		5	V
Driver differential output (with load)	VOD2	R=54ΩorR=27Ω Figure 1		1.5		-	V
AVDV_DO_Driver1	ΔV _{OD}			-		0.2	V
Driver common mode output voltage	VOC			1		3	V
ΔVCOM_DRV1	ΔV _{OC}					0.2	V
Input high voltage	V _{IH}	DE,RE,DI		2			V
Input undervoltage	V _{IL}	DE,RE,DI				0.8	V
Input current	I _{IIN1}	DE,RE,DI				±2	uA
Input current (A, B)	I _{IIN2}	DE=0V,V _{CC} =5V	V _{IN} =5V		40	90	uA
			V _{IN} =0V		60	100	
Receiver differential input threshold voltage	V _{TH}	-7V ≤ V _{CM} ≤ +12V		-200		-50	mV
Receiver input delay	ΔV _{TH}				25		mV
Receiver output high level	V _{OH}	I _O =-8mA		4			V
Receiver output low level	V _{OL}	I _O =8mA				0.4	V
RT-HI-OC	I _{OZR}	0.4V ≤ V _O ≤ 2.4V				1	uA
Receiver input impedance	R _{IN}	-7V ≤ V _{CM} ≤ +12V		96			kΩ
No load operating current	I _{CC}	no-load	DE = V _{CC}		480	600	uA
			RE=DI=GND or V _C	DE=GND		450	600
Receiver output short-circuit current	I _{OSR}	c 0V ≤ V _{RO} ≤ V _{CC}				95	mA
ESD protection		Between A/B, human body mode		±8	±15		kV
Switch characteristics not specified otherwise Vcc=5V ± 5%, TA=25°C							
Parameter	Symbol	Test Conditions		MIN	TYP	MAX	UNIT
Drive input to output	t _{DPLH}	R _{DIFF} =50Ω C _{L1} =C _{L2} =100Pf Figure3, 5			34	60	nS
	t _{DPHL}				34	60	nS
Driver output offset t _{DPLH} – t _{DPHL}	t _{DSKEW}				-2.5	±10	nS
Drive up and down time	t _{DR}				10	25	nS
	t _{DF}				10	25	nS
Driver enables high output	t _{DZH}	C _L 00pF Figure4,6 S2 close				150	nS
Driver enables output to be low	t _{DZL}	C _L =100pF Figure4,6 S1 close				150	nS
Drive from low to off	t _{DLZ}	C _L =15pF Figure4,6 S1 close				100	nS
Drive from high to off	t _{DHZ}	C _L =15pF Figure4,6 S2 close				100	nS
Receiver input to output	t _{RPLH}	V _{ID} ≥ 2.0V;V _{ID} Rising and falling				150	nS
	t _{RPHL}					150	nS
Differential receiver offset	t _{RSKEW}	time ≤ 15nS Figure 7,9			0	±10	nS
The receiver stays on until output is low	t _{RZL}	C _L =100pF Figure 2,8			20	50	nS
Enable the receiver to output high	t _{RZH}	C _L =100pF Figure 2,8			20	50	nS
Receiver from low to off	t _{RLZ}	C _L =100pF Figure 2,8			20	50	nS
Receiver from high to off	t _{RHZ}	C _L =100pF Figure 2,8			20	50	nS

Driver output short-circuit current	IOD	Short circuit current between A/B		-	100	mA
Maximum data speed	fMAX		10	-	-	Mbps

Notes :
 1 ΔVOD and ΔVOC respectively represent the changes in VOD and VOC when DI changes.
 2 When the current flows into the device, it is positive, and when it flows out of the device, it is negative; Unless otherwise specified, all voltages are referenced to ground.

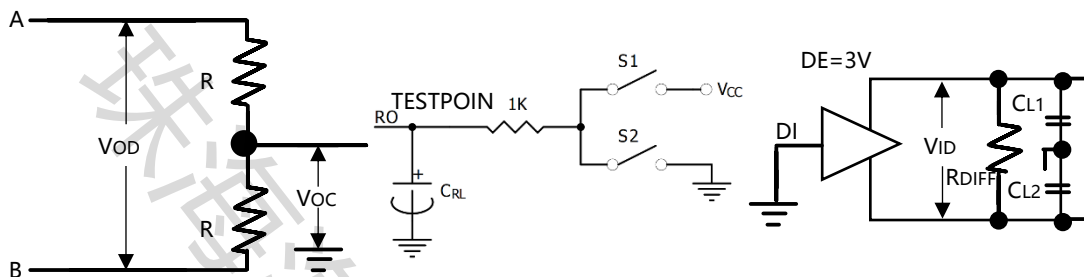


Figure 1 DC characteristic test load of driver

Figure 3 Driver switch characteristic test circuit

Figure 2 Receiver Enable/Off Switch Characteristics Test Load

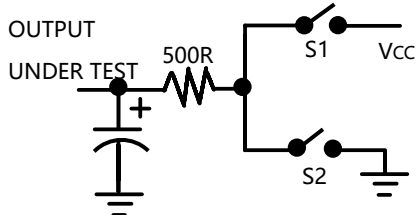


Figure 4 Driver Enable/Off Switch Characteristics Test Load

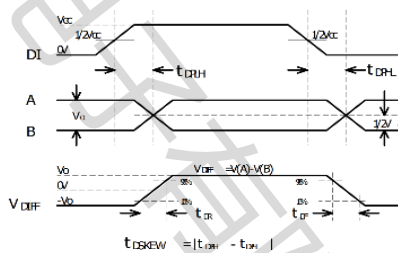


Figure 5 Drive transmission delay

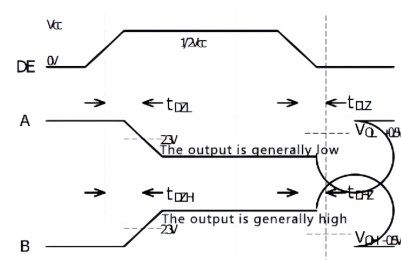


Figure 6 Driver Enable/Off Timing

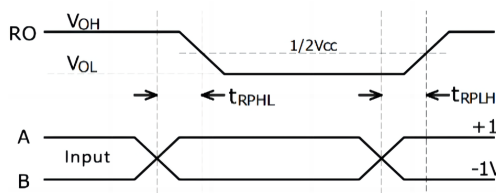


Figure 7 Receiver transmission delay

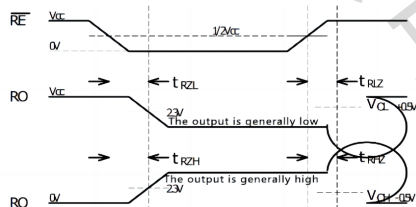


Figure 8 Receiver Enable/Off Timing

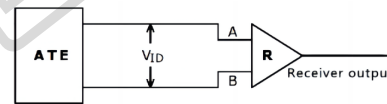
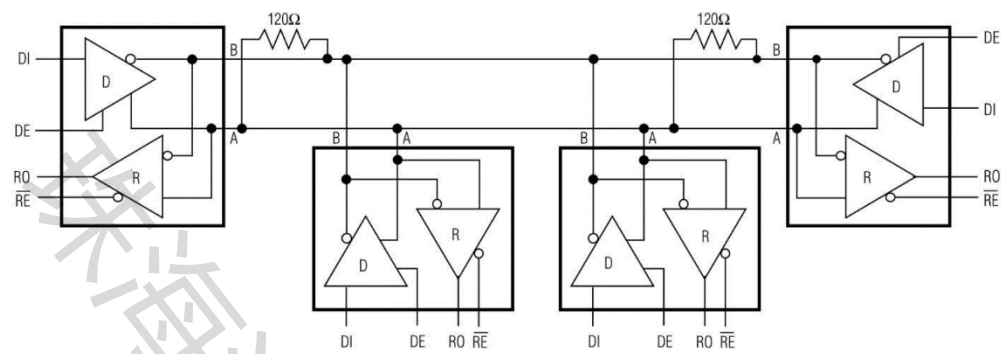


Figure 9 Receiver Transmission Delay Test

Typical Application



Packaging and packaging

SOP8 (Package Outline Dimensions)

