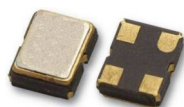




PLETRONICS *PRONTO*™ QM33L Series

CMOS Programmable Clock Oscillator



QM33L
2.0 x 2.5 x 0.81 mm
LCC Ceramic Package

Features

- Pletronics' QM33L Series is a programmable quartz crystal controlled precision square wave oscillator
- CMOS Output (will interface with TTL devices)
- Enable/Disable Function includes low standby power
- Low Jitter
- 1.8V, 2.5V, or 3.3V nominal Supply Voltage
- 1-200 MHz Frequency Range (1-125MHz at 1.8V)
- Fundamental crystals

Applications

Driving A/Ds, D/As, FPGAs
Digital Video
Ethernet, GbE
Medical
Storage Area Networking
COTS
Broad Band Access
SONET/ SDH/ DWDM
Test & Measurement

Electrical Characteristics

Parameter	Min	Typ	Max	Unit	Condition
Frequency Range ²	1	-	200	MHz	Consult factory for other options (1.8V frequency range 1-125MHz)
Frequency Stability vs. Temperature ² ± 20 = 20 , ± 25 = 44 , ± 50 = 45	±20	-	±50	ppm	For all supply voltages, load changes, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures
Operating Temperature Range ²	-10 -20 -40	- - -	+70 +70 +85	°C	Standard range Extended range C option Extended range E option
Supply Voltage ^{1,2} V _{CC}	1.8	-	3.3	Volts	± 5%, See Part Number options on page 2
Supply Current I _{CC}	-	22	35	mA	@15pF Load @ 110 MHz
Output Waveform	CMOS				
Duty Cycle	45	-	55	%	See Load Circuit
Output V _{HIGH}	90	-	-	%V _{CC}	
Output V _{LOW}	-	-	10	%V _{CC}	
Output T _{RISE} and T _{FALL}	-	-	2	ns	
Startup Time	-	-	5	ms	Time for output to reach specified frequency
V _{DISABLE}	-	-	30	%	Of V _{CC} applied to Pad 1
V _{ENABLE}	70	-			
Enable Time	-	-	100	ns	Time for output to reach a logic state
Disable Time	-	-	100	ns	Time for output to reach a high Z state
Aging	-	-	±3	ppm	First year
Standby Current	-	20	35	mA	Pad 1 low, device disabled @ 110 MHz
Jitter	-	1.0	-	ps	12 kHz to 20 MHz @ 110 MHz
Storage Temperature Range	-55	-	+125	°C	

Notes: Specifications with Pad 1 E/D open circuit

¹ Place an appropriate power supply bypass capacitor next to device for correct operation

² Specified by part number



PLETRONICS *PRONTO*TM QM33L Series

CMOS Programmable Clock Oscillator

Part Number

Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V _{CC}	Frequency in MHz
QM33	45	L	E	V	- 125.0M
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20 = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	X = 1.8V ±5% W = 2.5V ±5% V = 3.3V ±5%	1 - 200 MHz (1.8V: 1-125MHz)

Device Marking

PRONTO

- YMxxx

PRONTO = Pletronics Model
YM = Date Code, Year Month (see below)
xxx = internal factory codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking.
External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YM (Year Month)

Code	9	0	1	2	3	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2019	2020	2021	2022	2023	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Package Labeling

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 16mm tape, 8mm pitch.

P/N Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

P/N:	
QM3345LEV-125.0M	
Customer P/N:	
12345678	
Qty:	
1000	
D/C:	
9DW	
MSL: 1	

RoHS Compliant

2nd LvL Interconnect

Category=e4

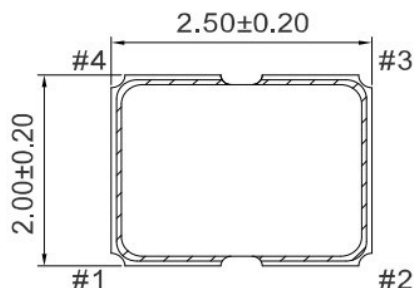
Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS 3 (2015/863) and WEEE 2 (2012/19/EU) directives.

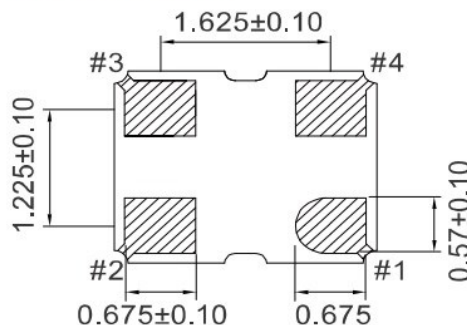
Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.022 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D
Second Level Interconnect code: e4

Mechanical Dimensions (mm)

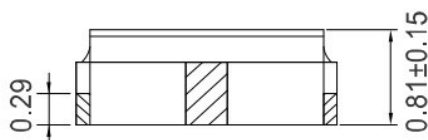
[TOP VIEW]



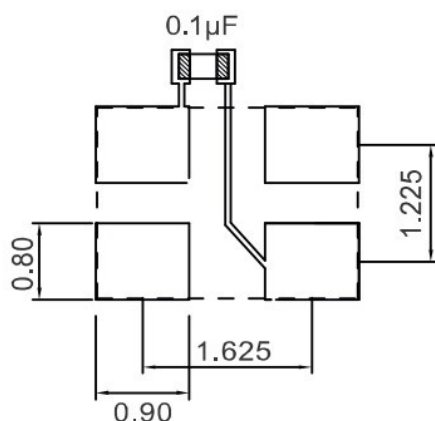
[BOTTOM VIEW]



[SIDE VIEW]



Pin#	Function
1	Tri-State
2	GND
3	Output
4	VDD



Pad Layout mm shown

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vdd and GND pads.

(Not to Scale)

Contacts (pads): Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

For Optimum Jitter Performance, Pletronics recommends:

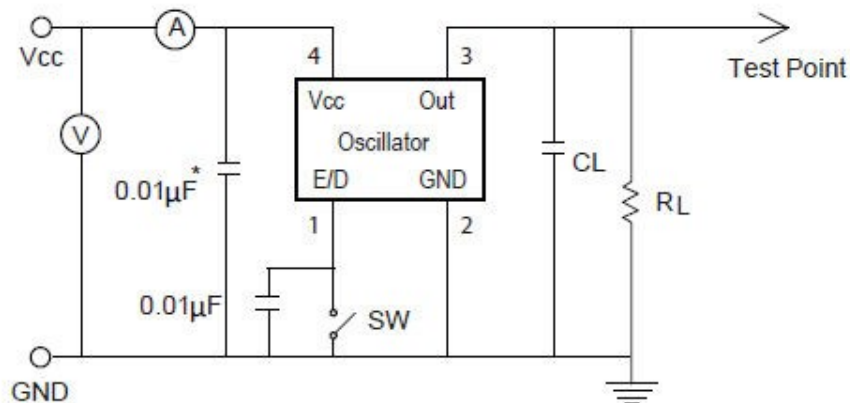
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



PLETRONICS *PRONTO*TM QM33L Series

CMOS Programmable Clock Oscillator

Electrical Test / Load Circuit

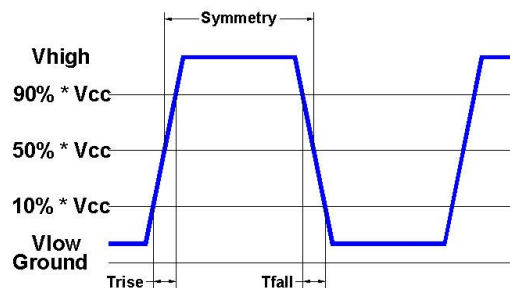


Notes:

RL: 5 Kohm minimum

CL: Includes the input capacitance of oscilloscope

* 0.01µF external by-pass filter is recommended



Environmental / ESD Ratings

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	JESD22-B104
Vibration	JESD22-B103
Solderability	IPC J-STD-002
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Charged Device Model	500V	JESD 22-C101
Machine Model	200V	JESD22-A115

Thermal Characteristics:

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

Absolute Maximum Ratings

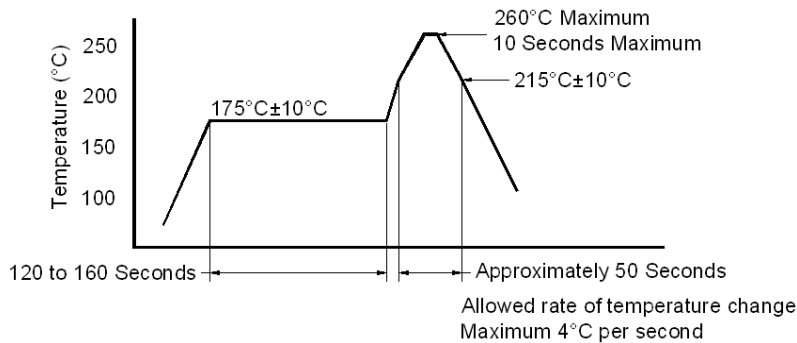
Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +7.0V
V _i Input Voltage	-0.5V to V _{CC} + 0.5V
V _o Output Voltage	-0.5V to V _{CC} + 0.5V



PLETRONICS *PRONTO*TM QM33L Series

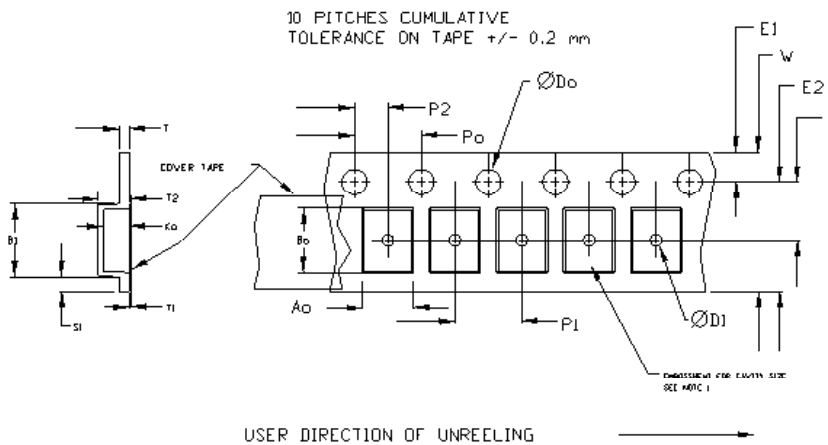
CMOS Programmable Clock Oscillator

Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel



Tape Constant Dimensions Table 1

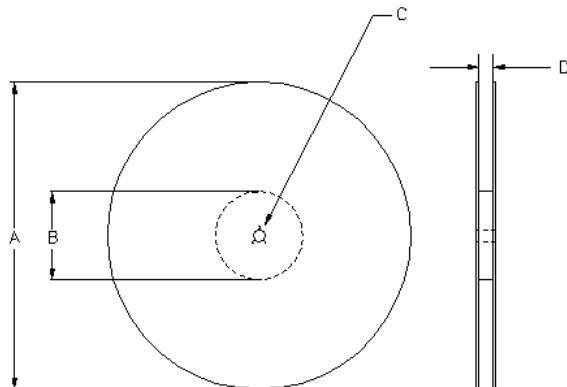
Tape Size	Do	D1 min	E1	Po	P2	S1 min	T max	T1 max
8mm	1.5	1.0	1.75	4.0	2.0	0.6	0.6	0.1
12mm		1.5			±0.05			
16mm	+0.1 -0.0	1.5	±0.1	±0.1	2.0	0.6	0.6	0.1
24mm		1.5			±0.1			

Tape Variable Dimensions Table 2

Tape Size	B1 max	E2 min	F	P1	T2 max	W max	Ao, Bo & Ko
8mm	4.55	6.25	3.5 ±0.05	4.0 ±0.1	1.55	8.3	Note 1

Dimensions in mm Drawing Not to scale

Note 1: Embossed cavity to conform to EIA-481-B



Reel Dimensions (may vary) Table 3

Reel Size	A		B		C	D
	Inches	mm	Inches	mm	mm	mm
7	7.0	177.8	2.50	63.5	13.0 +0.5 -0.2	Tape size +0.4 +2.0 -0.0
10	10.0	254.0	4.00	101.6		
13	13.0	330.2	3.75	95.3		



PLETRONICS *PRONTO*™ QM33L Series

CMOS Programmable Clock Oscillator

Important Notice

Pletronics Incorporated (PLE) reserves the right to make corrections, improvements, modifications and other changes to this product at anytime. PLE reserves the right to discontinue any product or service without notice. Customers are responsible for obtaining the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to PLE's terms and conditions of sale supplied at the time of order acknowledgment.

PLE warrants performance of this product to the specifications applicable at the time of sale in accordance with PLE's limited warranty. Testing and other quality control techniques are used to the extent PLE deems necessary to support this warranty. Except where mandated by specific contractual documents, testing of all parameters of each product is not necessarily performed.

PLE assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using PLE components. To minimize the risks associated with the customer products and applications, customers should provide adequate design and operating safeguards.

PLE products are not designed, intended, authorized or warranted to be suitable for use in life support applications, weapons, weapon systems or space applications, devices or systems or other critical applications that may involve potential risks of death, personal injury or severe property or environmental damage. Inclusion of PLE products in such applications is understood to be fully at the risk of the customer. Use of PLE products in such applications requires the written approval of an appropriate PLE officer. Questions concerning potential risk applications should be directed to PLE.

PLE does not warrant or represent that any license, either express or implied, is granted under any PLE patent right, copyright, artwork or other intellectual property right relating to any combination, machine or process which PLE product or services are used. Information published by PLE regarding third-party products or services does not constitute a license from PLE to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from PLE under the patents or other intellectual property of PLE.

Reproduction of information in PLE data sheets or web site is permissible only if the reproduction is without alteration and is accompanied by associated warranties, conditions, limitations and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. PLE is not responsible or liable for such altered documents.

Resale of PLE products or services with statements different from or beyond the parameters stated by PLE for that product or service voids all express and implied warranties for the associated PLE product or service and is an unfair or deceptive business practice. PLE is not responsible for any such statements.

Contacting Pletronics Inc.

Pletronics, Inc.
19013 36th Ave. West
Lynnwood, WA 98036-5761
U.S.A.

Tel: 425.776.1880
Fax: 425.776.2760
email: ple-sales@pletronics.com

URL: www.pletronics.com