

Features

- Low RDS(ON)
- Fast switching
- Green Device Available

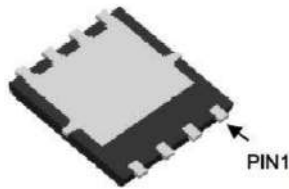
Application

- MB / VGA / Vcore
- POL Applications

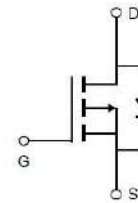
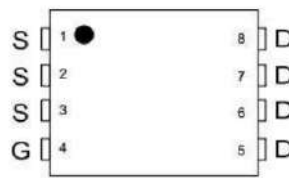
Product Summary



V _{DSS}	-30	V
R _{DS(ON), Typ @ V_{GS} = -10V}	10	mΩ
I _D	-30	A



PDFN3.3*3.3-8



P-MOSFET

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Symbol	Parameter		Max.	Units
V _{DSS}	Drain-Source Voltage		-30	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Continuous Drain Current	T _C = 25°C	-30	A
		T _C = 100°C	-7	A
I _{DM}	Pulsed Drain Current ^{note1}		-90	A
P _D	Power Dissipation	T _A = 25°C	3.7	W
R _{θJA}	Thermal Resistance, Junction to Ambient		33.8	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C

Thermal Characteristics

Symbol	Parameter		Rating	Unit
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	4.6	°C/W
R _{θJA} ^③	Thermal Resistance-Junction to Ambient	Steady State	62	°C/W

Note ① : Max. current is limited by bonding wire

Note ② : UIS tested and pulse width are limited by maximum junction temperature 150°C

Note ③ : Surface Mounted on 1in² FR-4 board with 1oz.

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -30V, V _{GS} = 0V,	-	-	-1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-1.5	-2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance note2	V _{GS} =-10V, I _D =-10A	-	10	14	mΩ
		V _{GS} =-4.5V, I _D =-5A	-	14.4	20	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	-	1757	-	pF
C _{oss}	Output Capacitance		-	371	-	pF
C _{rss}	Reverse Transfer Capacitance		-	315	-	pF
Q _g	Total Gate Charge	V _{DS} = -15V, I _D = -9.1A, V _{GS} = -10V	-	30	-	nC
Q _{gs}	Gate-Source Charge		-	5.3	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	7.6	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = -15V, I _D = -6A, V _{GS} =-10V, R _{GEN} =2.5Ω	-	14	-	ns
t _r	Turn-on Rise Time		-	20	-	ns
t _{d(off)}	Turn-off Delay Time		-	95	-	ns
t _f	Turn-off Fall Time		-	65	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-30	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-90	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = -10A	-	-0.8	-1.2	V

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

 2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

Typical Performance Characteristics

Figure1: Output Characteristics

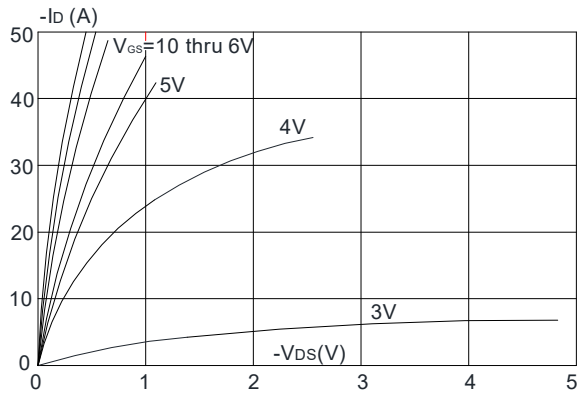


Figure 2: Typical Transfer Characteristics

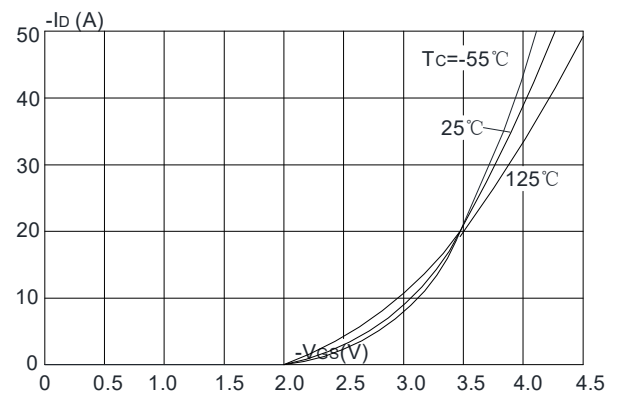


Figure 3: On-resistance vs. Drain Current

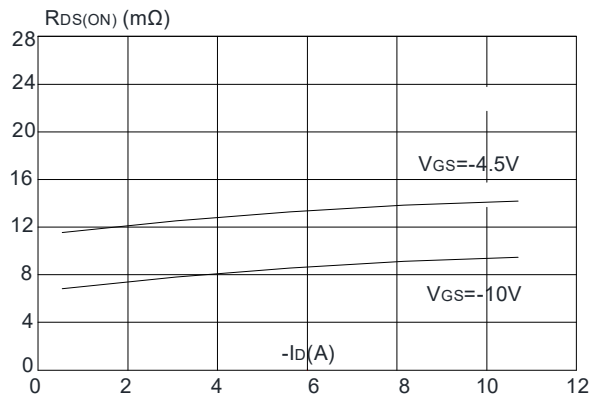


Figure 4: Body Diode Characteristics

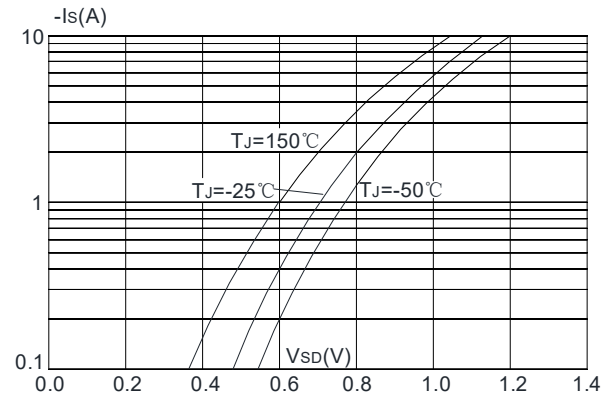


Figure 5: Gate Charge Characteristics

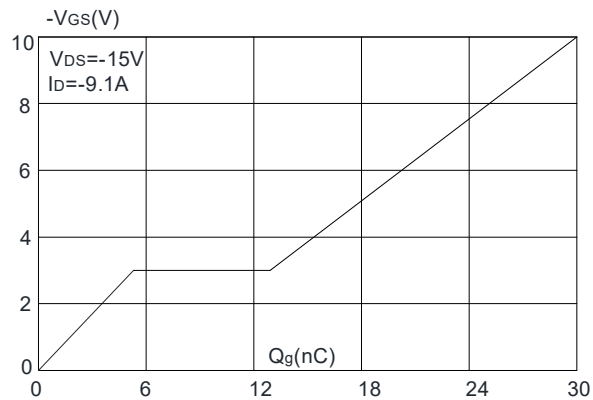


Figure 6: Capacitance Characteristics

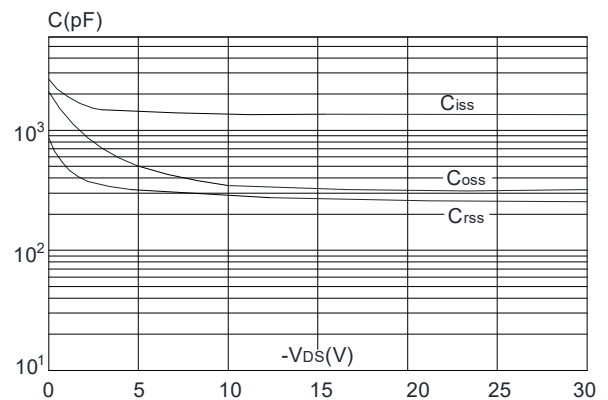




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

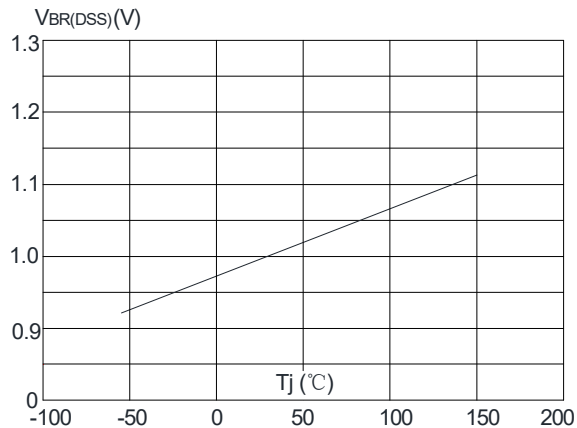


Figure 8: Normalized on Resistance vs. Junction Temperature

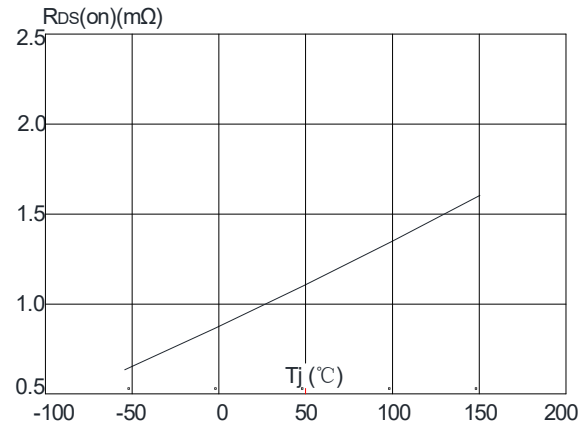


Figure 9: Maximum Safe Operating Area

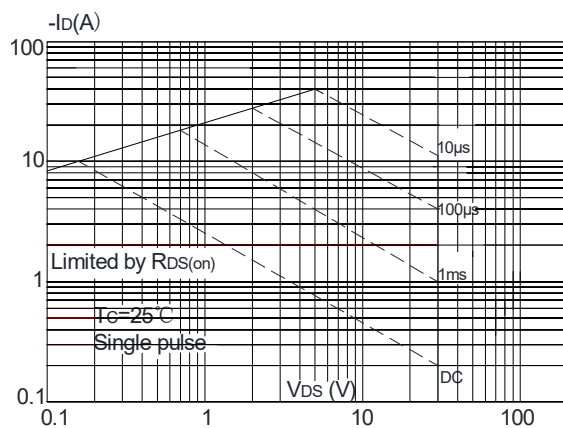


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

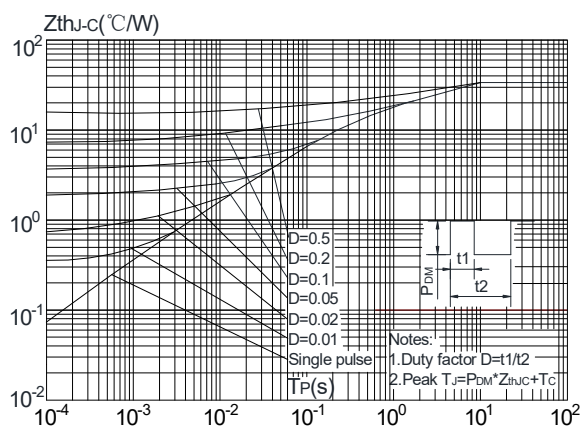
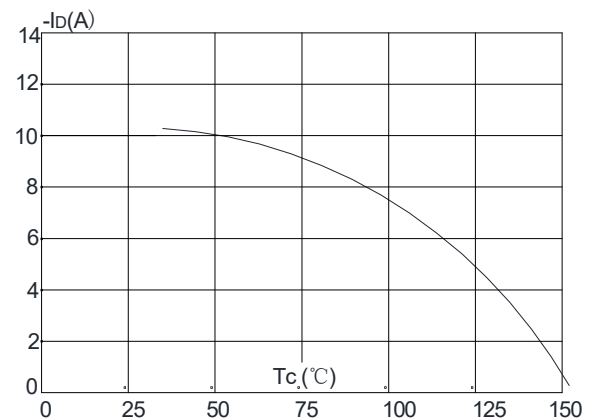
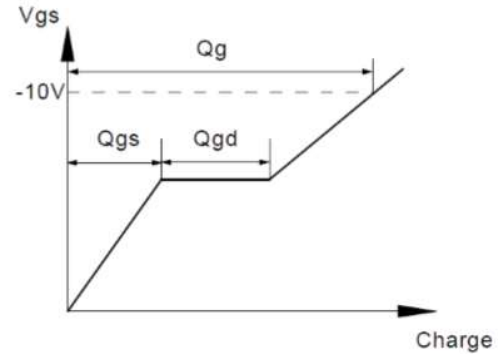
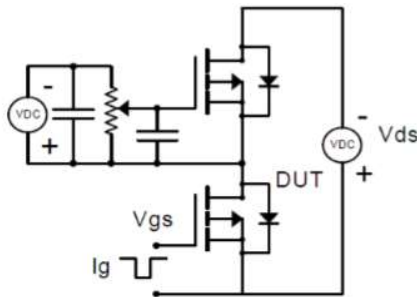


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

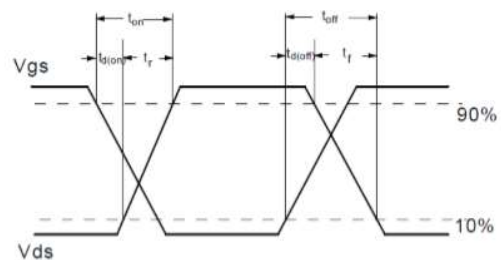
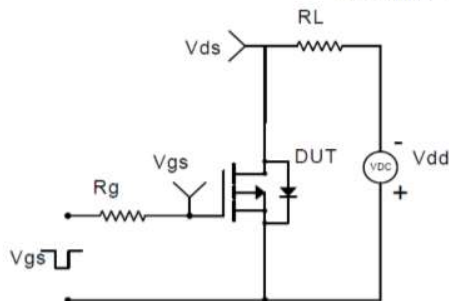




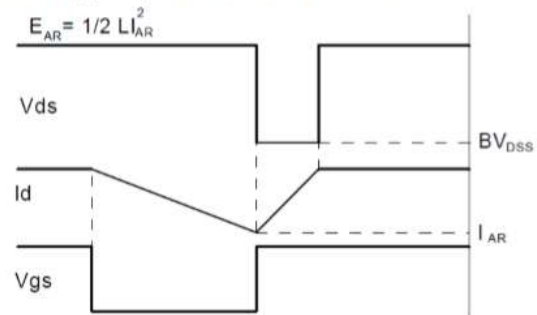
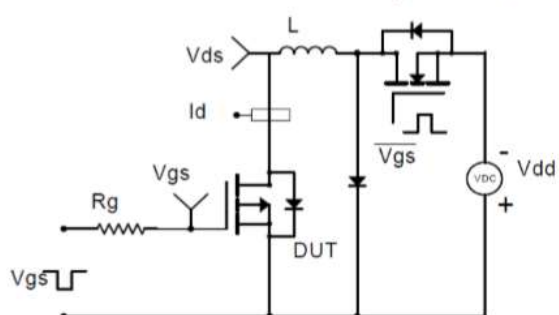
Gate Charge Test Circuit & Waveform



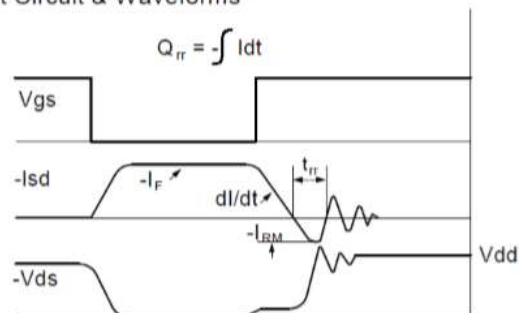
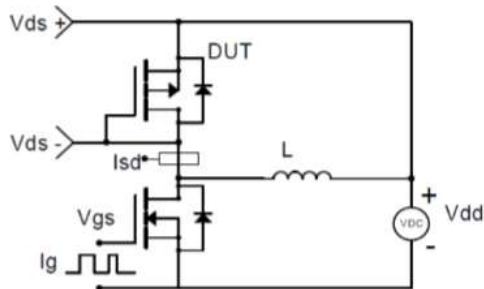
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

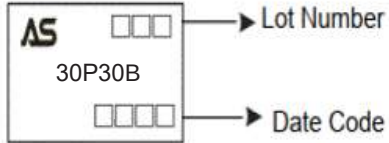


Diode Recovery Test Circuit & Waveforms

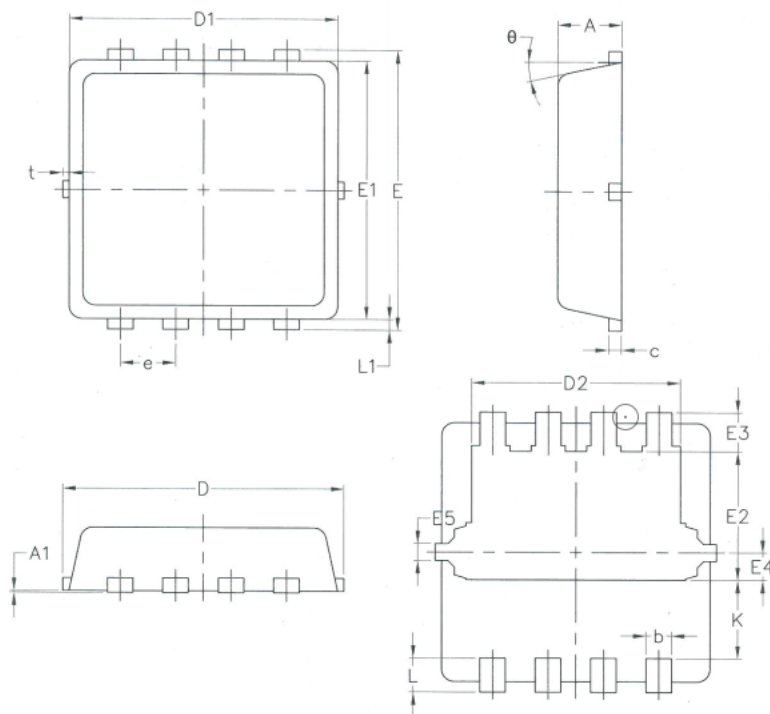


Ordering and Marking Information

Device	Marking	Package	Packaging	Quantity
ASDM30P30BE-R	30P30B	PDFN3.3*3.3-8	Tape&Reel	5000/Reel

PACKAGE	MARKING
PDFN3.3*3.3-8	 The marking diagram shows a rectangular package with the following markings: 'AS' logo, '30P30B', a four-digit 'Lot Number', and a four-digit 'Date Code'.

PDFN3.3*3.3-8 PACKAGE INFORMATION



SYMBOL	COMMON		
	MM		
	MIN	NOM	MAX
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.32	1.52	1.72
E3	0.28	0.46	0.65
E4	0.18	0.33	0.48
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.78	0.93	1.13
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
θ	10°	12°	14°

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