

VOLTAGE RANGE CURRENT 50 to 1000 Volts 2.0 Ampere

RoHS

#### Features

- Axial lead type devices for through hole design
- Oj chip junction
- · Fast switching for high efficiency
- Low leakage current
- High forward surge capability
- High reliability
- High temperature soldering guaranteed
   260°C/10 seconds,0.375"(9.5mm)lead length at 5 lbs(2.3kg) tension

#### Mechanical Data

- Case: Transfer molded plastic
- Epoxy: UL94V-0 rate flame retardant
- Polarity: Color band denotes cathode end
- Lead: Plated axial lead, solderable per MIL-STD-202E method 208C
- Mounting position: Any
- Weight: 0.012ounce, 0.39 grams

# 0.140 (3.6) 0.104(2.6) DIA. 0.300(7.6) 0.230(5.8) 1.0 (25.4) MIN. 1.0 (25.4) MIN.

Dimensions in inches and (millimeters)

## Maximum Ratings and Electrical Characteristics

- Ratings at 25°C ambient temperature unless otherwise specified
- Single Phase, half wave, 60Hz, resistive or inductive load
- For capacitive load derate current by 20%

TYPE NUMBER		SYMBO LS	FR 201	FR 202	FR 203	FR 204	FR 205	FR 206	FR 207	UNITS
Maximum Repetitive Peak Reverse Voltage		$V_{RRM}$	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage		$V_{\scriptscriptstyle RMS}$	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage		$V_{DC}$	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at T₄=75°C		I <sub>(AV)</sub>	2.0				Amps			
Peak Forward Surge Current 8.3mS single half sine-wave superimposed on rated load (JEDEC method)		I <sub>FSM</sub>	60				Amps			
Maximum Instantaneous Forward Voltage at 2.0A		V <sub>F</sub>	1.3				Volts			
Maximum DC Reverse Current at Rated DC	T <sub>A</sub> = 25°C		5.0							
Blocking Voltage	T <sub>A</sub> = 125℃	I <sub>R</sub>	100						+ μΑ	
Maximum Reverse Recovery Time <sup>(NOTEL)</sup>		$T_{RR}$	150 250 500		00	nS				
Typical Junction Capacitance (NOTE 2)		C <sub>J</sub>	40			pF				
Typical Thermal Resistance (NOTE 3)		$R_{\theta JA}$	50			°C/W				
Operating and Storage Temperature Range		$T_{\rm J}, T_{\rm STG}$	-55 to +150				°C			

#### Notes:

- 1. Reverse Recovery Test Conditions:If=0.5A,Ir=1.0A,Irr=0.25A.
- 2. Measured at 1.0MHz and applied reverse voltage of 4.0 Volts.
- 3. Thermal Resistance from Junction to Ambient with 0.375" (9.5mm) lead length, PCB mounted.



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## Ratings and Characteristic Curves (T<sub>A</sub>=25°C unless otherwise noted)

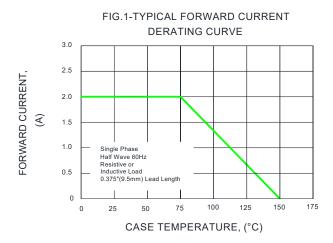


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

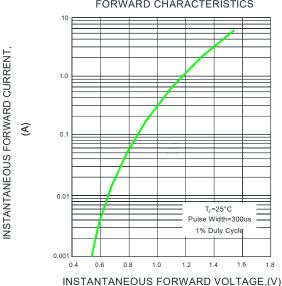


FIG.5-TYPICAL JUNCTION CAPACITANCE

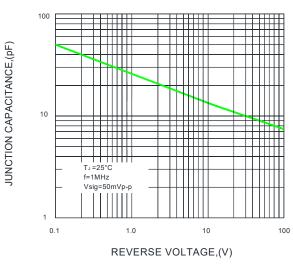


FIG.2-MAXIMUM NON-REPETITIVE PEAK
FORWARD SURGE CURRENT

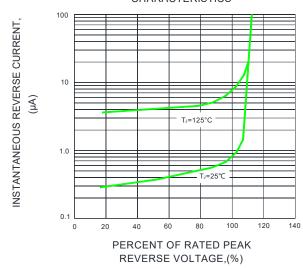
70

60

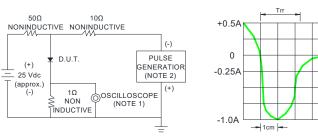
8.3ms Single Half Sine-Wave
(JEDEC Method) T<sub>j</sub> = T<sub>jmax</sub>

FIG.4-TYPICAL REVERSE CHARACTERISTICS

NUMBER OF CYCLES AT 60 Hz



F1G.6-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



NOTES : 1.Rise Time=7ns max. Input Impedance= 1 magohm. 22pF

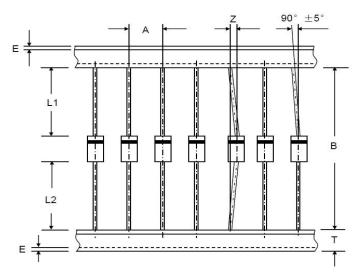
2.Rise time=10ns max. Source Impedance=
50 ohms

SET TIME BASE FOR 50/100ns/cm



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## Axial Lead Taping Specifications for Rectifiers



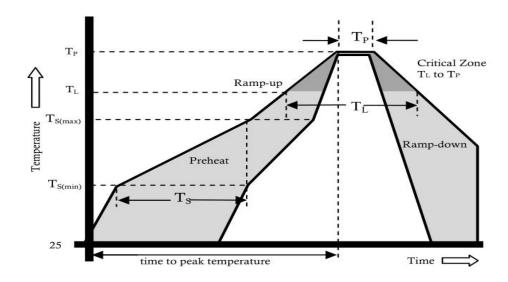
Component Outline	Component Pitch A	Inner Tap	e Pitch B	Cumulative	
Component Outline	±0.5mm	+0.5mm -0.4mm		Tolerance	
DO-204AC(DO-15)	5.0mm	52.4mm	26.0mm	2.0mm/20pitch	

ltem	Symbol	Specifications(mm)	Specifications(inch)
Component alignment	Z	1.2 max	0.048 max
Tape width	Т	6.0±0.4	0.236±0.016
Exposed adhesive	Е	0.8 max	0.032 max
Body eccentricity	IL1-L2I	1.0 max	0.040 max



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## Reflow Profile



Reflow Condition		Pb-Free Assembly		
	Temperature Min.	+150°C		
Pre Heat	Temperature Max.	+200°C		
	Time(Min to Max)	60-180 secs.		
Average ramp up rate(Liquidus Temp(T <sub>L</sub> ) to peak)		3°C/sec. Max.		
T₅(max) to T∟ - Ramp-up Rate		3°C/sec. Max.		
Defless	Temperature $(T_L)$ (Liquidus)	+217°C		
Reflow	Temperature $(T_L)$	60-150 secs.		
Peak Temp (T <sub>P</sub> )		+(260+0/-5 )°C		
Time within 5°C of actual Peak Temp (T♭)		25 secs.		
Ramp-down Rate		6°C/sec. Max.		
Time 25°C to peak Temp (T₂)		8 min. Max.		
Do not exceed		+260°C		



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