

TSA90N20MC

200V N-Channel MOSFET

General Description

The TSA90N20MC uses advanced trench technology and design to provide excellent RDS(ON). These devices are well suited for high efficient switching mode power supplies and active power factor correction.

Product Summary

90A, 200V, Max.RDS(on)=25mΩ
@ VGS =10V

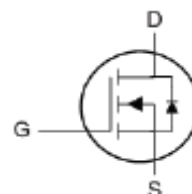
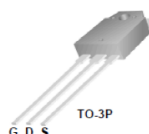
Applications

Uninterruptible power supplies
DC/DC converter
DC/AC inverter

Features

Fast switching
100% avalanche tested
Improve dv/dt capability
RoHS Compliant

TO-3P Pin Configuration



Absolute Maximum Ratings

Tc=25°C unless otherwise specified

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	200	V
VGS	Gate-Source Voltage	±20	V
ID@TC=25°C	Continuous Drain Current	90	A
ID@TC=100°C	Continuous Drain Current	55	A
IDM	Pulsed Drain Current ¹	270	A
EAS	Single Pulse Avalanche Energy ²	1000	mJ
PD@TC=25°C	Total Power Dissipation	350	W
TSTG	Storage Temperature Range	-55 to 175	°C
TJ	Operating and Storage Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
RθJC	Thermal Resistance Junction-ambient	---	0.4	°C/W
RθJA	Thermal Resistance Junction-case	---	40	°C/W

Electrical Characteristics(TJ=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	200	--	--	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=30A$	--	--	25	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	2	--	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=160V$, $V_{GS}=0V$	--	--	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	--	--	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=20V$, $I_D=3A$	--	13	--	S
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1MHz$	--	6000	--	pF
C_{oss}	Output Capacitance		--	280	--	pF
C_{rss}	Reverse Transfer Capacitance		--	25	--	pF
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=100V$, $I_D=60A$ $R_L=1.66\Omega$ $R_G=1\Omega$ $V_{GS}=10V$	--	15	--	ns
t_r	Rise Time		--	130	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	30	--	ns
t_f	Fall Time		--	90	--	ns
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1MHz$	--	2	--	Ω
Q_g	Total Gate Charge	$I_D=60A$ $V_{DD}=100V$ $V_{GS}=10V$	--	60	--	nC
Q_{gs}	Gate-Source Charge		--	18	--	nC
Q_{gd}	Gate-Drain Charge		--	17	--	nC

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	--	--	90	A
I_{SM}	Pulsed Source Current		--	--	270	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=1A$, $T_J=25^\circ C$	--	--	1.5	V

Note :

- 1.Repetitive rating; pulse width limited by maximum junction temperature.
- 2.The test condition is $V_{DD}=50V$, $V_{GS}=10V$, $L=1mH$, $I_{AS}=45A$.

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