



SLM6550

2A Synchronous Buck Li-Ion Battery Charger

Description

The SLM6550 is a 2A single cell Li-ion battery charger applied for 5V adapters. It utilizes 1.2MHz synchronous buck converter topology to reduce power dissipation and therefore reaches a high efficiency up to 90%.

The SLM6550 includes complete charge termination circuitry, automatic recharge, and a $\pm 1\%$ 4.35V float voltage. It also has other features include output short-circuit protection, battery temperature monitor, overheating protection, and no blocking diode is required.

The SLM6550 is available in SOP8/DFN8 package with heat sink. Its few external component count makes the SLM6550 a high-efficient battery charger ideally suited for portable applications.

Features

- 1.2MHz Fixed Switching Frequency
- High Efficiency up to 90%
- 2.5A Maximum Charge Current
- 6.3V input overvoltage protection
- No External MOSFET or Blocking Diode Required
- Preset 4.35V Charge Voltage with $\pm 1\%$ Accuracy
- Automatic Recharge
- Charge State Pairs of Output, No Battery and Fault Status Display
- C/10 Charge Termination
- 140uA Supply Current in Shutdown
- 2.9V Trickle Charge
- Soft-Start Limits Inrush Current
- Battery Temperature Monitoring
- Short-Circuit Protection
- Available in 8-Pin SOP/DFN Package

Absolute Maximum

Ratings

- V_{IN} : -0.3V~6.5V
- BAT: -0.3V~7V
- LX: -0.3V~7V
- VS: -0.3V~7V
- NCHRG: -0.3V~8V
- NSTDBY: -0.3V~8V
- TS: -0.3V~8V
- BAT Short-Circuit Duration: Continuous
- Maximum Junction Temperature: 145°C
- Operating Temperature Rang: -40°C~85°C
- Storage Temperature Range: -65°C~125°C
- Lead Temperature (Soldering, 10 sec) 260°C

Applications

- Cellular Telephones
- MP3, MP4 Players
- GPS, Digital Cameras
- Electronic Dictionaries
- Portable Devices, Chargers

Complete Charge Cycle

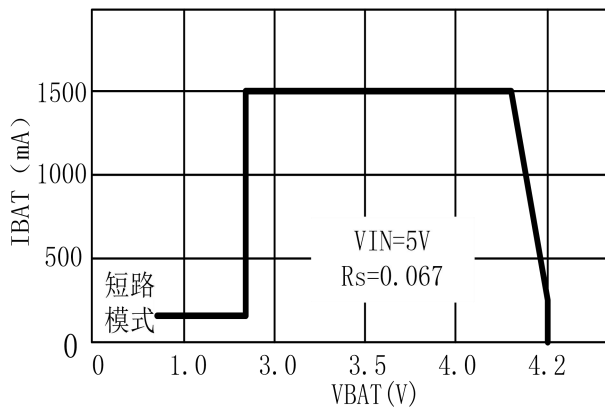


Figure 1

Typical Application

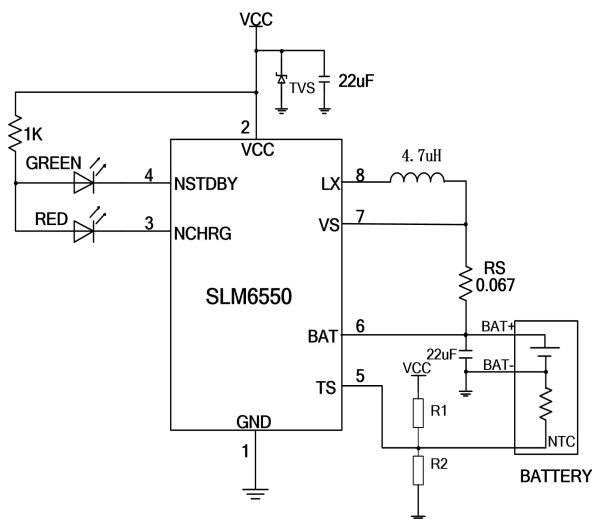


Figure 2

Application Tips

Effective heat dissipation is the key to ensure the chip to long-term maintain high charge current.

In order to maximize the charge current, PC board layout design should be optimized to provide IC within SOP8/DFN package effective heat dissipation.

The thermal path for the heat generated by IC is from the die to lead frame, and finally to the PC board copper through the bottom heat sink. As the heat sink of IC, the copper pads of PC board should be as wide as possible, and extends out to other larger copper areas to dissipate heat into ambient environment

Another effective way to improve the heat dissipation ability of charger is to placing via to the internal or back layer of PC board, as figure 3 illustrates, place a 2.5*6.5mm pad as the heat sink of SLM6550, and then place 4 cooling holes with 1.2mm diameter and 1.6mm hole spacing on the pad. Solder should be injected into the cooling holes from the back layer of PC board to ensure that the bottom heat sink of SLM6550 is effectively connected to the cooling pad.

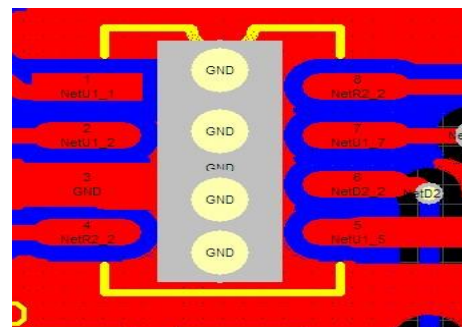


Figure 3

Other heat sources not related to the IC should also be considered when designing PC board layout, as they might influence the overall temperature rise and the maximum charge current.

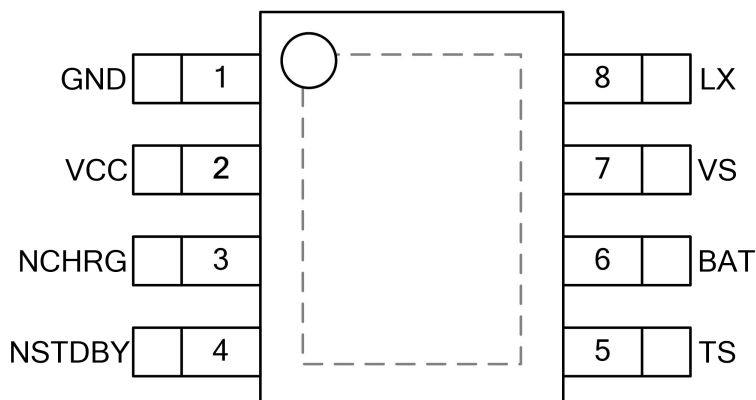


Figure 4. SLM6550 Package

Pin	Symbol	Function
1	GND	Ground
2	VCC	Positive input supply voltage
3	NCHRG	Open-Drain charge status output
4	NSTDBY	Charge terminated status output
5	TS	Chip enable and battery temperature sense
6	BAT	Battery connection Pin
7	VS	Charge current sense
8	LX	Switching

Pin Assignment

GND(Pin 1): Ground.

Vcc(Pin 2): Positive input supply voltage. It provides power to the internal circuit. When V_{CC} drops to within 30mV of the BAT pin voltage, the SLM6550 enters low power mode, dropping I_{BAT} to less than 2 μ A.

NCHRG (Pin 3) : Open-Drain charge status output. When the battery is being charged, the NCHRG pin is pulled low by an internal switch to indicate the charge. Otherwise, NCHRG pin is in high impedance state.



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NSTDBY(Pin 4): Charge terminated status output. NSTDBY is pulled low by an internal switch to indicate the termination of battery charge. Otherwise NSTDBY pin is in high impedance state.

TS(Pin 5): Chip enable and battery temperature sense input. Connecting TS pin to NTC sensor's output in Lithium ion battery pack. If TEMP pin's voltage is below 45% or above 80% of supply voltage V_{CC} , this means that battery's temperature is too low or too high, charging is suspended. The temperature sense function can be disabled by connecting it to VCC pin. Grounding TS pin will make the chip disable and terminate the charge.

BAT(Pin 6): Battery connection Pin. Connect the positive terminal of the battery to this pin. Dropping BAT pin's current to less than $2\mu A$ when IC in disable mode or in sleep mode. BAT pin provides charge current to the battery and provides regulation voltage of 4.35V.

VS(Pin 7): Charge current sense pin

LX(Pin 8): External inductor connecting pin

Characteristics

($T_A=25^{\circ}C$, $V_{IN}=5V$, unless otherwise specified)

Symbol	Parameter	Condition	Min	Typ.	Max	Unit
V_{IN}	Input supply voltage		3.8	5	6.0	V
I_{CC}	Input supply current	Charge mode		250	500	μA
		Standby mode (charge ends)		140	280	μA
		Shutdown mode (R_S not connected, $V_{IN}<V_{BAT}$ or $V_{IN}<V_{UV}$)		140	280	μA
V_{FLOAL}	Regulated output voltage	$0^{\circ}C \leq T_A \leq 85^{\circ}C$	4.307	4.35	4.393	V
I_{BAT}	BAT pin current (Test condition of current mode is $V_{BAT}=3.8V$)	$R_S=0.1\Omega$, current mode	900	1000	1100	mA
		$R_S=0.05\Omega$, current mode	1800	2000	2200	mA
		Standby mode, $V_{BAT}=4.35V$	0	-3.0	-5	μA
		Shutdown mode (R_{PROG} not connected)		± 1	± 2	μA
I_{TRIKL}	Trickle charge current	$V_{BAT}<V_{TRIKL}$, $R_{PROG}=0.05\Omega$	180	200	220	mA
V_{TRIKL}	Trickle charge threshold voltage	$R_S=0.05\Omega$, V_{BAT} rising	2.8	2.9	3.0	V



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V _{TRHYS}	Trickle charge hysteresis voltage	R _S =0.05Ω	60	80	100	mV
V _{UV}	V _{CC} under voltage lockout threshold	V _{IN} from low to high	3.6	3.7	3.9	V
V _{UVHYS}	V _{CC} under voltage lockout hysteresis		150	200	300	mV
V _{ASD}	V _{CC} -V _{BAT} lockout threshold voltage	V _{IN} from low to high	120	200	300	mV
		V _{IN} from low to high	20	45	80	mV
I _{TERM}	C/10 termination current threshold	R _S =0.1Ω		100		mA
		R _S =0.05Ω		200		mA
V _{NCHRG}	NCHRG Pin output low voltage	I _{NCHRG} =5mA		0.3	0.6	V
V _{NSTDBY}	NSTDBY Pin output low voltage	I _{NSTDBY} =5mA		0.3	0.6	V
V _{TS_H}	The voltage at TS increase			80	82	%V _{IN}
V _{TS_L}	The voltage at TS decrease		43	45		%V _{IN}
ΔV _{RECHRG}	Recharge battery threshold voltage	V _{FLOAT} -V _{RECHRG}	50	100	150	mV
FREQ	Switching frequency		1.3	1.5	1.7	MHz
T _{LIM}	Thermal protection temperature			145		°C
RPFET	The on-resistance of P-FET			150		mΩ
RNFET	The on-resistance of N-FET			120		mΩ
t _{SS}	Soft-start time	I _{BAT} =0 to I _{BAT} =1200V/R _{PROG}		20		us
t _{RECHRG}	Recharge comparator filter time	V _{BAT} from high to low	0.8	1.8	4	ms
t _{TERM}	Termination comparator filter time	I _{BAT} below I _{CHRG} /10	0.8	1.8	4	ms



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Principle

The SLM6550 is a 2A single cell Li-ion battery charger applied for 5V wall adapters. It utilizes 1.2MHz synchronous buck converter topology to reduce the power dissipation. It provides a 2A maximum charge current, and the charge current can be programmed by a external resister.

The SLM6550 include two Open-Drain charge status Pins: Charge status indicator NCHRG and battery full status NSTDBY. The internal thermal regulation circuit reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 145°C. This feature protects the SLM6550 from excessive temperature, and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the SLM6550 or the external components.

The charge cycle begins when the voltage at the VCC pin rises above the threshold voltage. The CHRG pin outputs a logic low to indicate that the charge cycle is on going. If the battery voltage is below 2.9V, the charger goes into the fast charge constant-current mode, and the charge current is set by R_s . When the battery approaches the regulation voltage 4.35V, the charge current begins to decrease as the SLM6550 enters the constant-voltage mode. When the current drops to charge termination threshold, the charge cycle is terminated, and NCHRG pin turns into a high impedance state and NSTDBY pin outputs a logic low level.

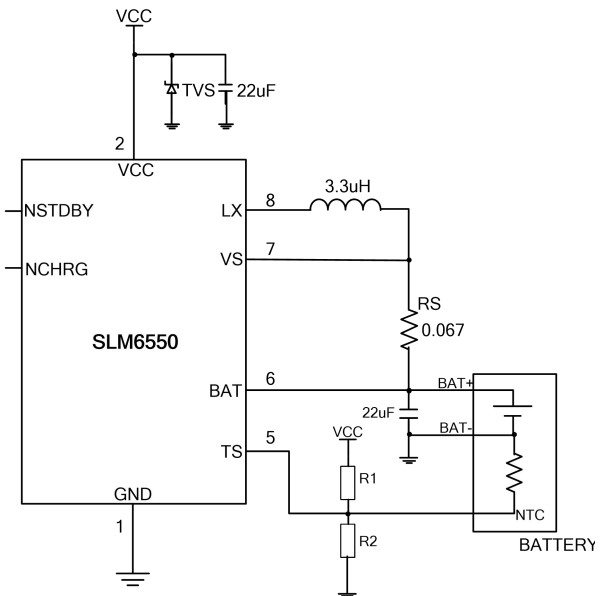
The charge termination threshold is 10% of the current in constant current mode. The charge cycle can also be automatically restarted if

the voltage of BAT pin falls below the recharge threshold.

The on-chip reference voltage, error amplifier and the resistor divider provide regulation voltage with 1% accuracy which can meet the requirement of lithium-ion and lithium polymer batteries. When the input voltage is not connected, or below V_{BAT} , the charger goes into a sleep mode, dropping battery drain current to less than 3μA. This greatly reduces the current drain on the battery and increases the standby time. The charger can be shutdown by forcing the TS pin to GND.

Programming Charging Current

The charge current is programmed by the resistor connected between VS pin and LX pin (figure 4).



The program resistor and the charge current are calculated using the following formula:

$$R_s(\text{ohm}) = 0.1V / I_{BAT}(\text{A})$$

R_S (Ω)	I_{BAT} (mA)
1	100
0.2	500
0.1	1000
0.067	1500
0.05	2000

Table 1: R_S and I_{BAT}

Charge Termination

A charge cycle is terminated when the charge current falls to 1/10 the programmed value after the final float voltage is reached. This condition is detected by using an internal filtered comparator to monitor the voltage of R_S . When the R_S voltage falls below 10mV for longer than t_{TERM} (typically 1.8mS), charging is terminated. The charge current is latched off and the SLM6550 enters standby mode, where the input supply current drops to 140 μ A (Note: C/10 termination is disabled in trickle charging and thermal limiting modes).

When charging, transient loads on the BAT pin can cause the R_S voltage to fall below 10mV for short periods of time before the DC charge current has dropped to 1/10th the programmed value. The 1.8mS filter time (t_{TERM}) on the termination comparator ensures that the transient loads do not result in premature charge cycle termination. Once the average charge current drops below 1/10th the programmed value, the SLM6550 terminated the charge cycle and ceases to provide any current through the BAT pin. In this state all loads on the BAT pin must be supplied by the battery.

The SLM6550 constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the 4.05V recharge threshold (V_{RECHRG}), another charge cycle begins and current is once again supplied to the battery.

Charge Status Indicator

SLM6550 has two open-drain status indicator output NCHRG and NSTDBY. NCHRG is pull-down when the SLM6550 in a charge cycle. In other status NCHRG in high impedance, NCHRG and NSTDBY are all in high impedance when the battery out of the normal temperature.

Represent in failure state, when TS pin in typical connecting and the charger with no battery: red LED and green LED all don't light.

The battery temperature sense function is disabled by connecting TS pin to VCC. In this case, if the battery is not connected to charger, NCHRG pin outputs a PWM level to indicate no battery. If BAT pin connects a 10 μ F capacitor, the frequency of flicker about 1-4S, if status indicator is not required, it should be connected to GND. (Table 2)

Charge status	Red LED NCHRG	Green LED NSTDBY
charging	Light	Dark
Battery full	Dark	Light
Under-voltage, battery's temperature too high or too low, battery not connected (TS used)	Dark	Dark
BAT pin connects to 10uF capacitor, battery not connected, TS=VCC	Green LED lights, red LED flickers, F=1-4S	

Table 2: Charge Status Indicator

Thermal Limiting

An internal thermal feedback loop reduces the programmed charge current if the die temperature attempts to rise above a preset value of 140°C. The feature protects the SLM6550 from overheating and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the SLM6550.

Battery Temperature

Sense

To prevent the damage caused by the very high or very low temperature done to the battery, the SLM6550 continuously senses battery temperature by measuring the voltage at TS pin determined by the voltage divider circuit and the battery's internal NTC sensor as shown in Figure 2.

The SLM6550 compares the voltage at TEMP pin (TS) against its internal V_{TS_L} and V_{TS_H} thresholds to determine if charging is allowed. In SLM6550, V_{TS_L} is fixed at $(45\% \times V_{IN})$, while V_{TS_H} is fixed at $(80\% \times V_{IN})$. If $V_{TS} < V_{TS_L}$ or $V_{TS} > V_{TS_H}$, it indicates that the battery temperature is too high or too low and the charge cycle is suspended. The battery temperature sense function can be disabled by connecting TS pin to VCC.

The values of R1 and R2 in figure 2 can be determined according to the assumed temperature monitor range and thermal resistor's values. Following is an example: Assume temperature monitor range is $T_L \sim T_H$, the thermal resistor in battery has negative temperature coefficient (NTC), R_{TL} is the resistance at T_L , R_{TH} is the resistance at T_H , so $R_{TL} > R_{TH}$, then at temperature T_L , the voltage at TEMP pin is:

$$V_{TEMP_L} = \frac{R_2 \parallel R_{TL}}{R_1 + (R_2 \parallel R_{TL})} \times V_{IN}$$

At temperature T_H , the voltage at TS pin is:

$$V_{TEMP_H} = \frac{R_2 \parallel R_{TH}}{R_1 + (R_2 \parallel R_{TH})} \times V_{IN}$$

From:

$$V_{TEMP_L} = V_{TS_L} = K_2 \times V_{IN} (K_2 = 0.8)$$

$$V_{TEMP_H} = V_{TS_H} = K_1 \times V_{IN} (K_1 = 0.45)$$

derive:

$$R_1 = \frac{R_{TL} R_{TH} (K_2 - K_1)}{(R_{TL} - R_{TH}) K_1 K_2}$$

$$R_2 = \frac{R_{TL} R_{TH} (K_2 - K_1)}{R_{TL} (K_1 - K_1 K_2) - R_{TH} (K_2 - K_1 K_2)}$$

For positive temperature coefficient thermal resistor in battery, we have $R_{TH} > R_{TL}$ and we can calculate:

$$R_1 = \frac{R_{TL} R_{TH} (K_2 - K_1)}{(R_{TH} - R_{TL}) K_1 K_2}$$

$$R_2 = \frac{R_{TL} R_{TH} (K_2 - K_1)}{R_{TH} (K_1 - K_1 K_2) - R_{TL} (K_2 - K_1 K_2)}$$

It is obvious that temperature monitor range is independent of power supply voltage V_{IN} and it only depends on R1, R2, R_{TL} and R_{TH} . The values of R_{TH} and R_{TL} can be found in related battery handbook or deduced from testing data.

In actual applications, if only one terminal temperature is concerned (protecting overheating), only R1 is needed.



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Manual Termination

At any time of the charging cycle will put the SLM6550 into disable mode through pulling TS pin to GND. This made the battery drain current to less than 2μA. To restart the charge cycle, set TS pin in high level or connect a programming resistor.

Over-current and Short-Circuit Protection

SLM6550 includes several varieties of protection, In order to avoid damaging the SLM6550, the peak current through the chip is limited at 3.5A. When the voltage of BAT pin is lower than 1.2V, the SLM6550 will enter a short-circuit protection mode, and the current through the chip will be limited at 10% of the peak current which is about 350mA.

Under Voltage Lockout

An internal under voltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VCC rises above the under voltage lockout threshold. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until VCC rises 120mV above the battery voltage.

Auto Restart

Once charge is terminated, SLM6550 immediately use a 1.8ms filter time ($t_{RECHARGE}$) comparator to monitor the voltage on BAT pin. If this voltage drops below the 4.05V recharge threshold (about between 80% and 90% of battery capacity), another charge cycle begins. This ensured the battery maintained (or approach) to a charge full status and avoid the requirement of restarting the periodic charging cycle. In the recharge cycle, NCHRG pin enters a pulled down status.

White LED Driver

The SLM6550 can directly drive white LED if 4 dry batteries is in serial as the input (6V). As the white LED turn-on voltage is approximate 3.6V, and works in the constant current stage. SLM6550 is able to supply stable DC current for single white LED or multiple white LED in parallel, and provides a 4.35V over-voltage protection. The SLM6550 can drive the 0.5W-7W white LED, and the driving current is programmed by the value of RS.

Input and Output Capacitor

Although a variety of types of capacitor can be used, power capacitor with high quality is recommended. Cautions are needed when using ceramic capacitor. Some ceramic capacitor may cause high EMI; therefore, under certain conditions high transient voltage may be caused to damage the chip. It is advised to use 10μF tantalum capacitor, if you want to use electrolytic capacitors, a 0.1μF bypass capacitor is required, and place it as close as the chip.

Inductor Selection

In order to guarantee the stability of the system, make sure the system works under CCM mode when in pre-charge and constant-current charge.

According to the inductor current formula:

$$\Delta I = \frac{1}{L \times F} \left(\frac{V_{IN} - V_{BAT}}{V_{IN}} \right) \times V_{BAT}$$

Where ΔI is the inductor ripple current, F is the switching frequency.

It is advised to take ΔI as 1/10 of the programmed charge current to ensure the system works under CCM mode in both pre-charge and constant current charge. Then based on the value of V_{in} , the value of inductor can be calculated.

The value of the inductor should be between 2.2uH and 10uH. 4.7uH is recommended.

The rated current of the inductor must be higher than the programmed charge current, and lower resistance is advised.

Heat Dissipation

In order to maximize the charge current, PC board layout design should be optimized to provide IC within SOP8/DFN package effective heat dissipation.

The thermal path for the heat generated by IC is from the die to lead frame, and finally to the PC board copper through the bottom heat sink. As the heat sink of IC, the copper pads of PC board should be as wide as possible, and extends out to other larger copper areas to dissipate heat into ambient environment

Another effective way to improve the heat dissipation ability of charger is to placing via to the internal or back layer of PC board, as figure 3 illustrates, place a 2.5*6.5mm pad as the heat sink of SLM6550, and then place 4 cooling holes with 1.2mm diameter and 1.6mm hole spacing on the pad. Solder should be injected into the cooling holes from the back layer of PC board to ensure that the bottom heat sink of the SLM6550 is completed connected to the cooling pad.

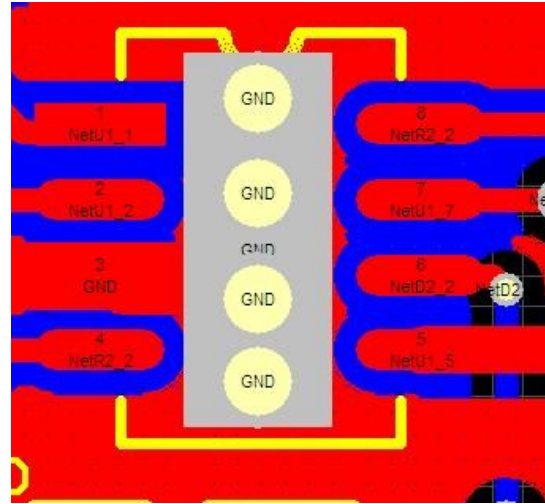
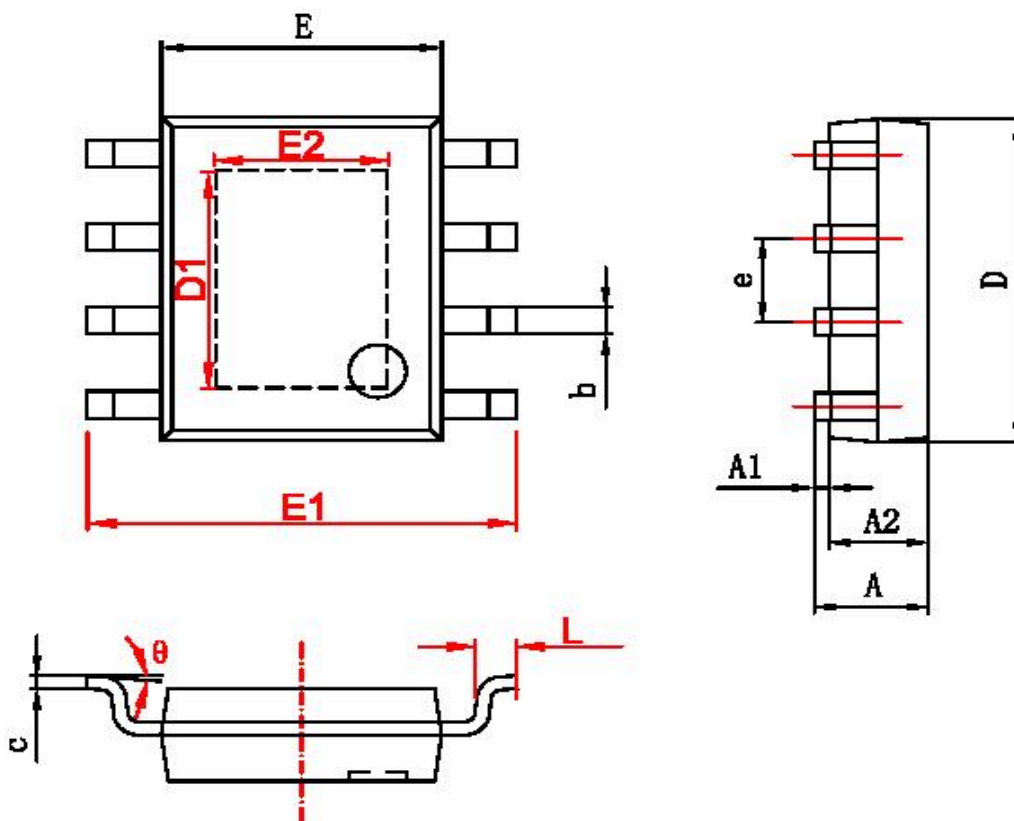


Figure 5

Other heat sources not related to the IC should also be considered when designing PC board layout, as they might influence the overall temperature rise and the maximum charge current.

8-Pin SOP Package (Unit mm)



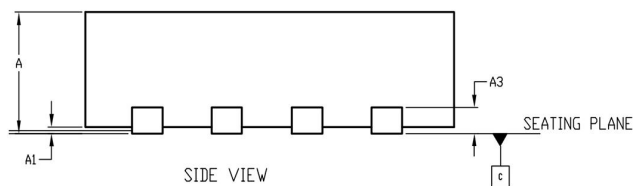
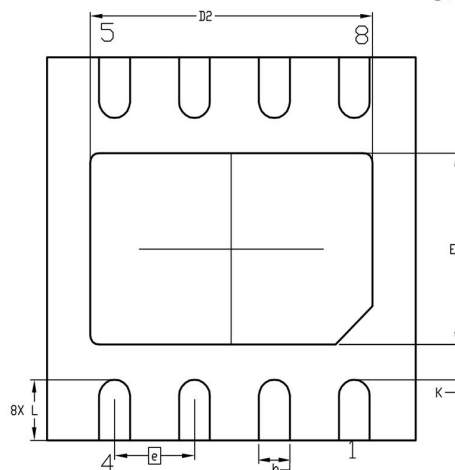
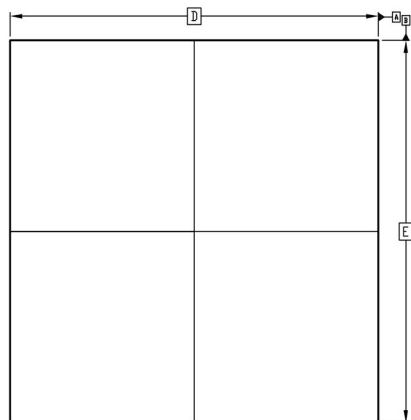
字符	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.050	0.150	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
D1	3.202	3.402	0.126	0.134
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
E2	2.313	2.513	0.091	0.099
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

8-Pin DFN Package (Unit mm)

DFN3X3 -8L

PACKAGE OUTLINE DIMENSIONS

Unit: mm



DIM SYMBOL	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0	0.02	0.05
A3	—	0.20 REF	—
b	0.20	0.25	0.30
D	3.00BSC		
E	3.00BSC		
D2	2.25	2.30	2.35
E2	1.45	1.50	1.55
e	0.65BSC		
L	0.425	0.475	0.525
K	0.20		