

# N-Channel 40-V (D-S) MOSFET

### Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize R<sub>DS(ON)</sub>, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### **Features**

- $R_{DS(ON)} = 1.7 \text{m}\Omega$  @  $V_{GS} = 10V$
- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- AEC-Q101 Qualification Available

#### **Typical Applications**

- Power Management in Desktop Computer
- High Frequency Switching
- Synchronous rectifier applications

Package type: PDFN 5X6

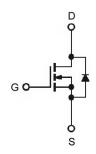
#### **Packing & Order Information**

3,000/Reel

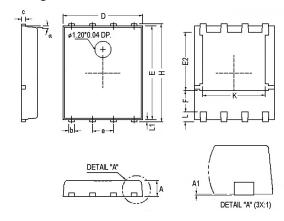


RoHS Compliant

### **Graphic Symbol**

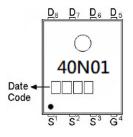


#### **Package Dimension**



REF.	Millimeter		REF.	Millimeter			
NEF.	Min.	Nom.	Max.	NEF.	Min.	Nom.	Max.
Α	0.85	1.00	1.15	Е	5.70	-	5.90
A1	0.00	-	0.10	е	-	1.27	-
b	0.30	-	0.51	Н	5.90	-	6.20
С	0.20	-	0.30	L	-	0.60	-
D	4.80	-	5.00	L1	0.06	-	0.20
F	1	.10 Ref.		α	0°	-	12°
E2	3	3.50 Ref.		K	3.70	3.90	4.10

#### Marking





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### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
V <sub>DS</sub>	Drain-Source Voltage	40	V		
V <sub>GS</sub>	Gate-Source Voltage	±20	V		
lo	Continuous Drain Current¹ (T <sub>C</sub> =25°C)	180	Α		
	Continuous Drain Current¹ (T <sub>C</sub> =100°C)	125	Α		
I <sub>DM</sub>	Pulsed Drain Current <sup>1,2</sup>	700	Α		
I <sub>AS</sub>	Single Pulse Avalanche Current, L =0.1mH³	80	Α		
E <sub>AS</sub>	Single Pulse Avalanche Energy, L =0.1mH³	320	mJ		
P <sub>D</sub>	Power Dissipation <sup>4</sup> (T <sub>C</sub> =25°C)	98	W		
TJ/Tstg	Operating Junction and Storage Temperature	-55 to 150	°C		

Thermal Resistance Ratings						
Symbol	Parameter	Maximum	Units			
$R_{\theta JA}$	Maximum Junction-to-Ambient <sup>1</sup>	45	°C/W			
Rejc	Maximum Junction-to-Case <sup>1</sup>	1.4	°C/W			

Electrical Characteristics (T」=25°C unless otherwise specified)						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V <sub>GS (th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.6	2.2	V
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
Igss	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	-	-	1 5	μA
RDS (on)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =50A V <sub>GS</sub> =4.5V, I <sub>D</sub> =50A	-	1.4 2.1	1.7 2.6	mΩ
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =25V, L =0.1mH, I <sub>AS</sub> =60A	180	_	_	mJ
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	-	-	1.2	V
ls	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	-	-	180	
Іѕм	Pulsed Source Current <sup>2,6</sup>		-	-	400	Α



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Dynamic						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Qg	Total Gate Charge <sup>2</sup>	V <sub>DS</sub> =15V		45		
Qgs	Gate-Source Charge	I <sub>D</sub> =20A		12		nC
Q <sub>gd</sub>	Gate-Drain Charge	V <sub>GS</sub> =10V		18.5		
t <sub>d(on)</sub>	Turn-On Delay Time <sup>2</sup>	V <sub>DS</sub> =15V		18.5		
tr	Rise Time	I <sub>D</sub> =20A		9		
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GS</sub> =10V		58.5		ns
tf	Fall Time	R <sub>G</sub> =3.3Ω		32		
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =20V		3972		
Coss	Output Capacitance	V <sub>GS</sub> =0V		1119		pF
C <sub>RSS</sub>	Reverse Transfer Capacitance	f =1.0MHz		82		1
Rg	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f =1.0MHz		1.0		Ω

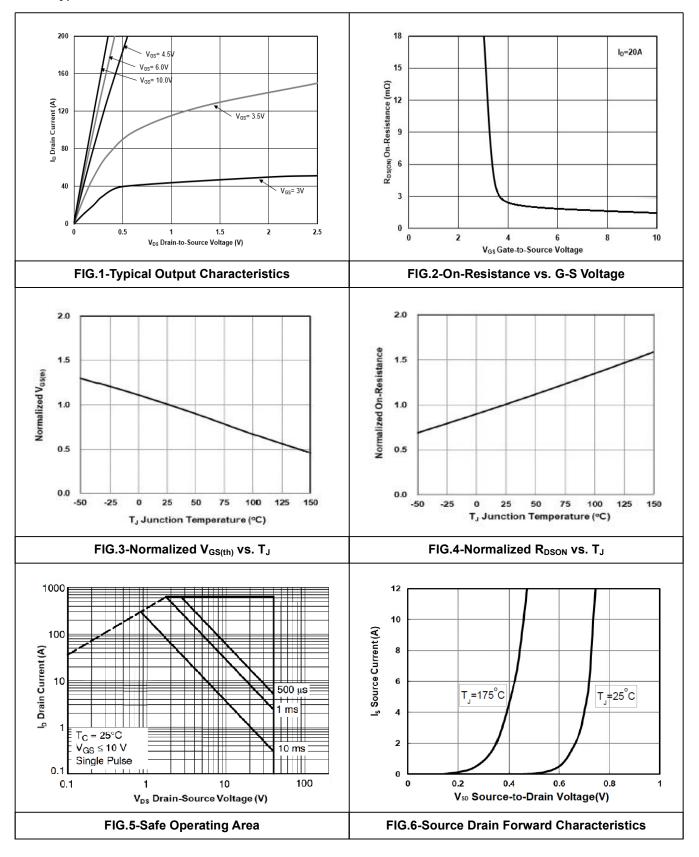
#### **Notes**

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width  $\leq$  300us, duty cycle  $\leq$  2%.
- 3. The EAS data shows maximum rating. The test condition is  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =80A.
- 5. The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.



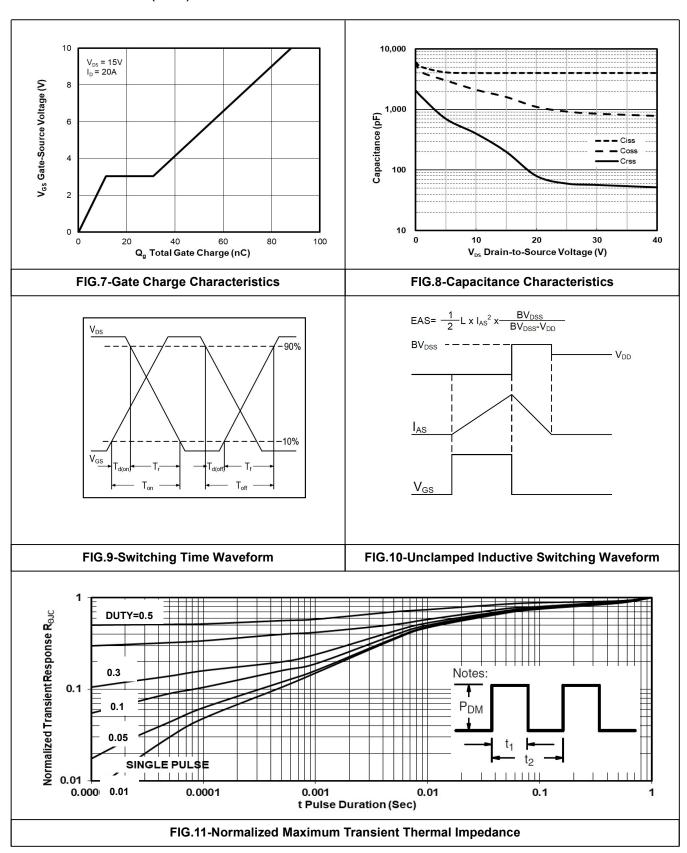
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### • Typical Electrical Characteristics





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