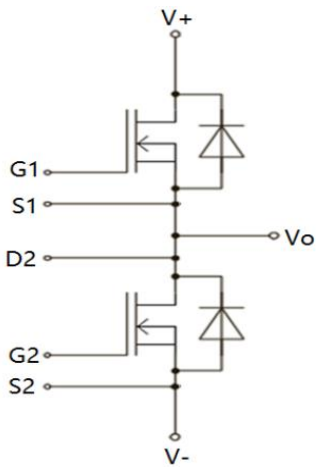
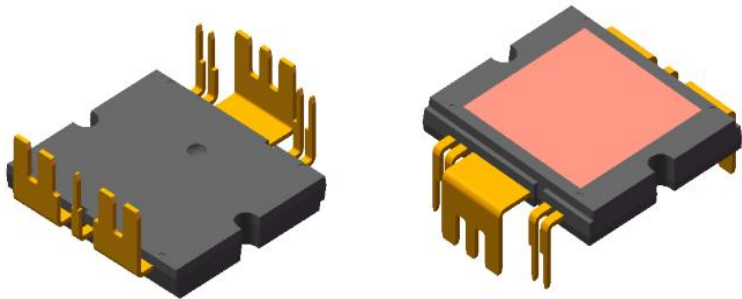


SLC500MM15SLT2



150V N沟道MOSFET，
500A半桥功率模块。

150V N-Channel MOSFET，
500A Half-Bridge Power Module.



$V_{DS}=150V$
 $I_{D\ nom}=500A$
 $R_{DS\ (ON)\ typ}=1.2m\Omega\ (per\ arm)$

特性

- 低导通阻抗
- 高电流密度
- 高可靠性
- 半桥，易并联

Features

- Low Rdson
- High current density
- High Ruggedness
- Halfbridge, Easy paralleling

应用场景

- DC/DC转换器
- UPS系统
- 交流电机控制器
- 太阳能应用

Application scenarios

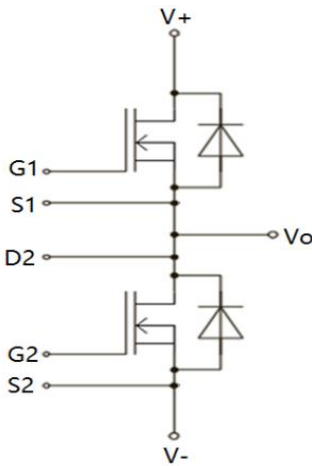
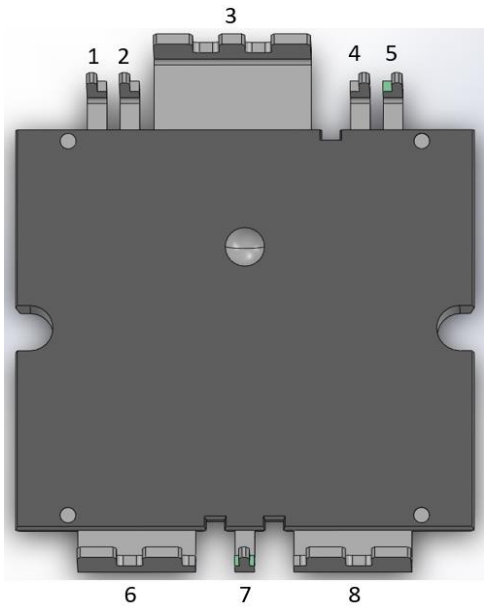
- DC/DC converter
- UPS systems
- AC Motor control
- Solar applications



Type / Ordering Code	Package	Marking	Related Links
SLC500MM15SLT2	T2		

Revision	Date	Subjects (major changes since last revision)
V1.0	2024/6/14	New version release

SLC500MM15SLT2



引脚功能描述/Pin Function Descriptions

Pin No.	Pin Name	Pin Functional Description
1	G1	上桥 MOSFET 栅极 / High Side MOSFET Gate
2	S1	上桥 MOSFET 源极 / High Side MOSFET Source
3	Vo	相线输出 / Phase Output
4	G2	下桥 MOSFET 栅极 / Low Side MOSFET Gate
5	S2	下桥 MOSFET 源极 / Low Side MOSFET Source
6	V+	正电源端子 / Positive Power Terminal
7	D2	下桥 MOSFET 漏极检测 / Low Side MOSFET Drain Sense
8	V-	负电源端子 / Negative Power Terminal

SLC500MM15SLT2



最大额定值 / Maximum Rated Values

$T_{vj} = 25^{\circ}\text{C}$, 除非另有说明/ $T_{vj} = 25^{\circ}\text{C}$, Unless Otherwise Specified

Parameter	symbol	Condition	Rating	Unit
漏源极电压 Drain-source voltage	V_{DS}	$V_{GS}=0, I_D=1\text{mA}, T_j=25^{\circ}\text{C}$	150	V
直流漏极电流 DC drain current	$I_{D \text{ nom}}$	$V_{GS}=15\text{V}, T_c=25^{\circ}\text{C}, R_{thJH}=0.17\text{K/W}$	500	A
		$V_{GS}=15\text{V}, T_c=100^{\circ}\text{C}, R_{thJH}=0.17\text{K/W}$	290	
脉冲漏极电流 Pulsed drain current	$I_{D, \text{ pulse}}$	tp limited by $T_{vj\text{max}}$	1200	A
雪崩能量, 单脉冲 Avalanche energy, single pulse	E_{AS}	$V_{DD}=100\text{V}, L=0.2\text{mH}, R_g=25\Omega, V_{GS}=10\text{V}$	6.5	J
栅源峰值电压 Gate-source voltage	V_{GS}	—	+20/-20	V
总耗散功率 Power dissipation	P_{tot}	$T_{vj\text{MAX}}=150^{\circ}\text{C}, T_c=25^{\circ}\text{C}, R_{thJH}=0.17\text{K/W}$	650	W

电气特征值 / Electrical Characteristic Values (per arm)

$T_{vj} = 25^{\circ}\text{C}$, 除非另有说明/ $T_{vj} = 25^{\circ}\text{C}$, Unless Otherwise Specified (per arm)

静态特性/Static Characteristic

Parameter	symbol	Condition	Value			Unit
			Min	Typ	Max	
漏源击穿电压 Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0, I_D=1\text{mA}, T_j=25^{\circ}\text{C}$	150	155	—	V
栅极阈值电压 Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS} \quad T_j=25^{\circ}\text{C}$	2.6	2.9	3.4	V
		$I_D=250\mu\text{A} \quad T_j=100^{\circ}\text{C}$	—	2.2	—	
		$T_j=125^{\circ}\text{C}$	—	1.9	—	
零栅电压漏极电流 Zero gate voltage drain current	I_{DSS}	$V_{DS}=100\text{V} \quad T_j=25^{\circ}\text{C}$	—	—	5	uA
		$V_{GS}=0\text{V} \quad T_j=100^{\circ}\text{C}$	—	—	30	
		$T_j=125^{\circ}\text{C}$	—	—	100	
栅极漏电流 Gate-source leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}, T_j=25^{\circ}\text{C}$	—	—	± 100	nA
漏源通态电阻 (per arm) Drain-source on-state resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V} \quad T_j=25^{\circ}\text{C}$	—	1.2	1.4	m Ω
		$I_D=100\text{A} \quad T_j=100^{\circ}\text{C}$	—	1.8	—	
		$T_j=125^{\circ}\text{C}$	—	2	—	
内部栅极电阻 Internal gate resistor	R_G	$V_{GS}=0, V_{DS}=0, f=1\text{MHz}$	—	0.76	—	Ω
跨导 Transconductance	g_{fs}	$V_{DS}=40\text{V}, I_D=46\text{A}$	—	276	—	S

动态特性/Dynamic Characteristic

Parameter	symbol	Condition	Value			Unit
			Min	Typ	Max	
输入电容 Input Capacitance	C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}$ $f = 0.1\text{ MHz}$	—	28	—	nF
输出电容 Output capacitance	C_{oss}		—	8.7	—	nF
反向传输电容 Reverse transfer capacitance	C_{rss}		—	0.7	—	pF
开通延迟时间(电感负载) Turn on delay time, inductive load	$t_{d(on)}$	$T_j = 25^\circ\text{C}, V_{DS} = 80\text{ V}, I_D = 50\text{ A},$ $V_{GS} = 18\text{ V}, R_G = 2\Omega$	—	43	—	ns
上升时间(电感负载) Rise time, inductive load	t_r		—	15.6	—	
关断延迟时间(电感负载) Turn off delay time, inductive load	$t_{d(off)}$		—	230	—	
下降时间(电感负载) Fall time, inductive load	t_f			21		
开通损耗 (每脉冲) Turn-on energy loss per pulse	E_{on}			0.01		mJ
关断损耗 (每脉冲) Turn-off energy loss per pulse	E_{off}		—	0.07	—	mJ
总的栅极电荷 Total gate charge	Q_g	$V_{DS} = 40\text{ V}, I_D = 100\text{ A}$ $V_{GS} = 0 \sim 10\text{ V}$		400		nC
栅-源电荷 Gate-Source charge	Q_{gs}			110		
栅-漏电荷 Gate-Drain charge	Q_{gd}			127		

体二极管特性/Body Diode Characteristic

Parameter	symbol	Condition	Value			Unit
			Min	Typ	Max	
二极管正向持续电流 Diode continuous forward current	I_S	$T_j = 25^\circ\text{C}$		500		
二极管正向脉冲电流 Diode pulse forward current	$I_{S, pulse}$	$T_j = 25^\circ\text{C}$		1200		
正向电压 Forward voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 500\text{ A}$	—	1.26	—	V
反向恢复时间 Reverse recovery time	t_{rr}	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$ $I_S = 50\text{ A}$ $dI_F / dt = 2070\text{ A}/\mu\text{s}$	—	57	—	ns
反向恢复电荷 Reverse recovery charge	Q_{rr}		—	170	—	nC

模块特性 / Module Features

Parameter	symbol	Condition	Rating	Unit
结—散热器热阻 Thermal resistance, junction to heatsink(per device)	R_{thJH}	Junction to heat dissipation copper plate	0.17	K/W
绝缘测试电压 Isolation test voltage	V_{ISOL}	RMS, f=50Hz, t=0.3s	2.5	kV
杂散电感, 模块 Stray inductance module	L_{sDS}	$T_j=25^{\circ}\text{C}$	19	nH
重量 Weight	G	-	18	g
模块基板材料 Material of module baseplate			Cu	
内部绝缘 Internal isolation			Al ₂ O ₃	
爬电距离 Creepage distance		端子至散热器 / terminal to heatsink 端子至端子 / terminal to terminal	3.78 1	mm
电气间隙 Clearance		端子至散热器 / terminal to heatsink 端子至端子 / terminal to terminal	2.96 1	mm
相对电痕指数 Comperative tracking index	CTI		TBD	
模块引线电阻, 端子-芯片 Module lead resistance, terminals - chip	$R_{CC'+EE'}$	$T_H = 25^{\circ}\text{C}$, 每个开关 / per switch	TBD	m Ω
在开关状态下温度 Temperature under switching conditions	$T_{vj\ op}$		-40/150	$^{\circ}\text{C}$
储存温度 Storage temperature	T_{stg}		-55/150	$^{\circ}\text{C}$
最高基板工作温度 Maximum baseplate operation temperature	T_{BPmax}		100	$^{\circ}\text{C}$

电气特性图/Electrical characteristics diagrams

Figure 1. On-Region Characteristics

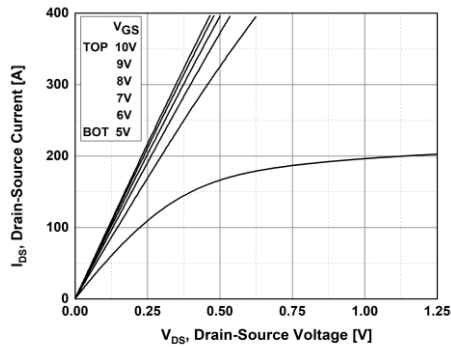


Figure 2. Transfer Characteristics

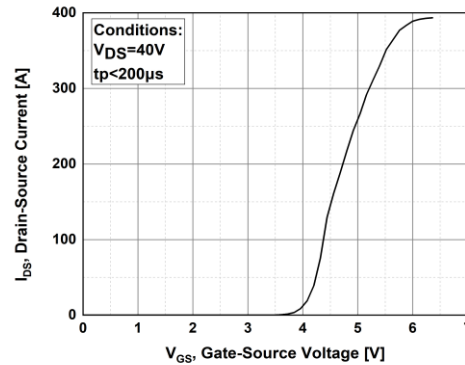


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

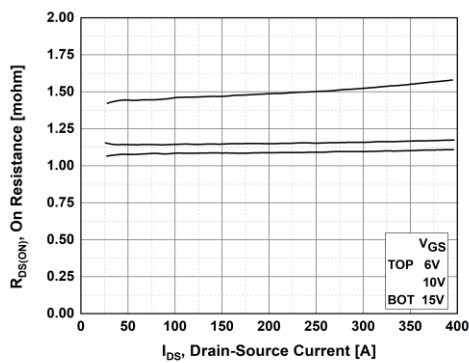


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

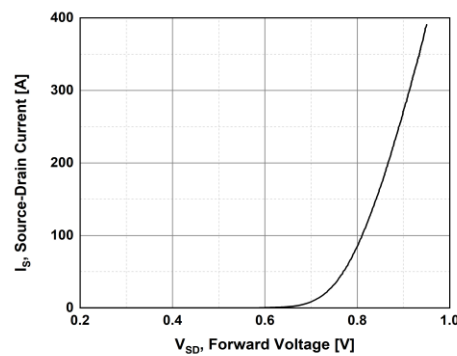


Figure 5. Capacitance Characteristics

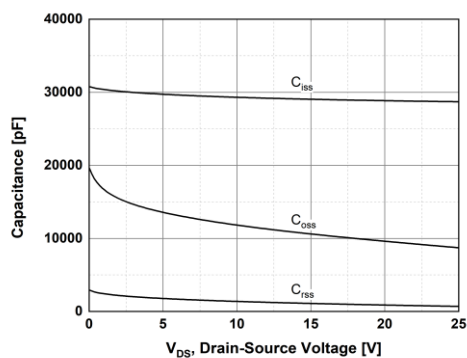


Figure 6. Gate Charge Characteristics

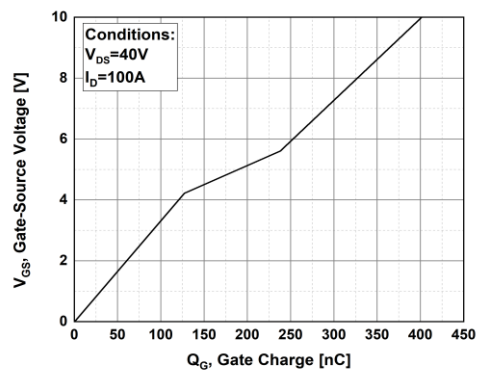


Figure 7. Breakdown Voltage Variation vs Temperature

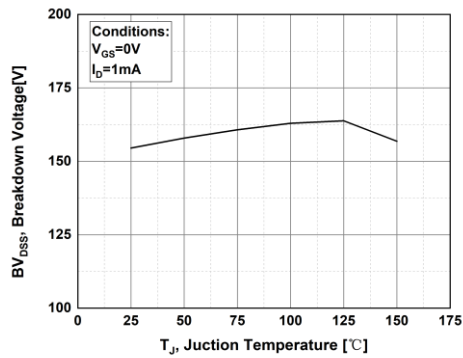


Figure 8. On-Resistance Variation vs Temperature

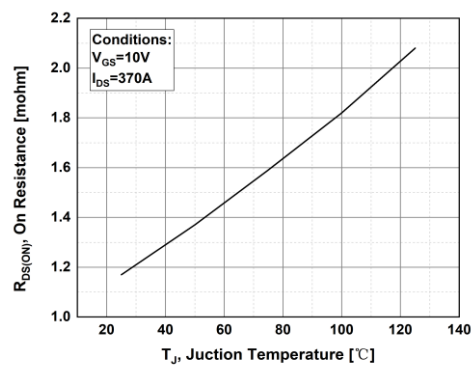


Figure 9. Maximum Power vs Case Temperature

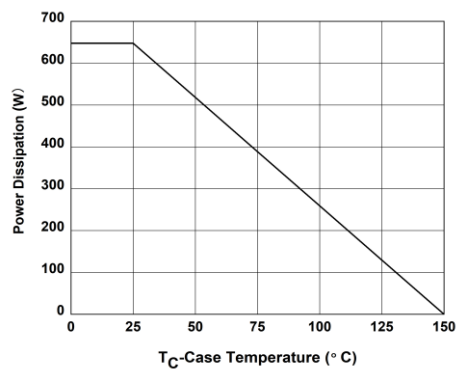


Figure 10. Maximum Drain Current vs Case Temperature

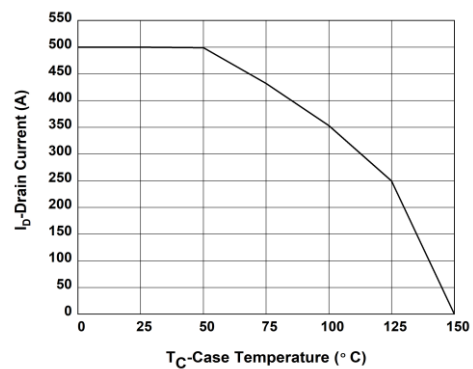


Figure 11. Transconductor Curve

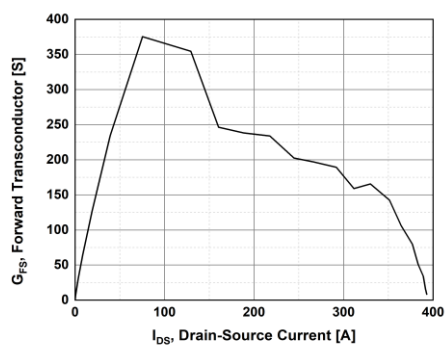
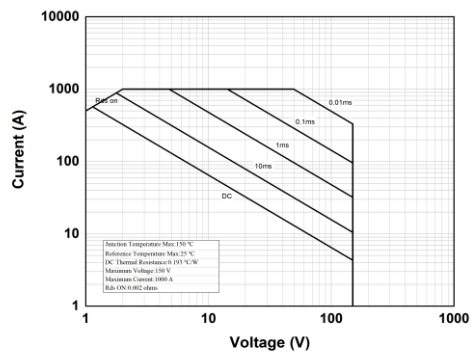


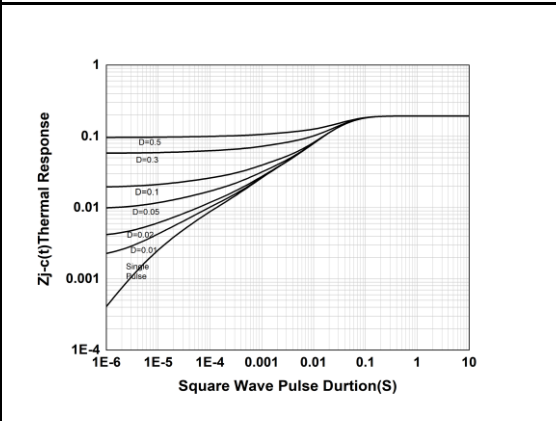
Figure 12. Maximum Safe Operating Area



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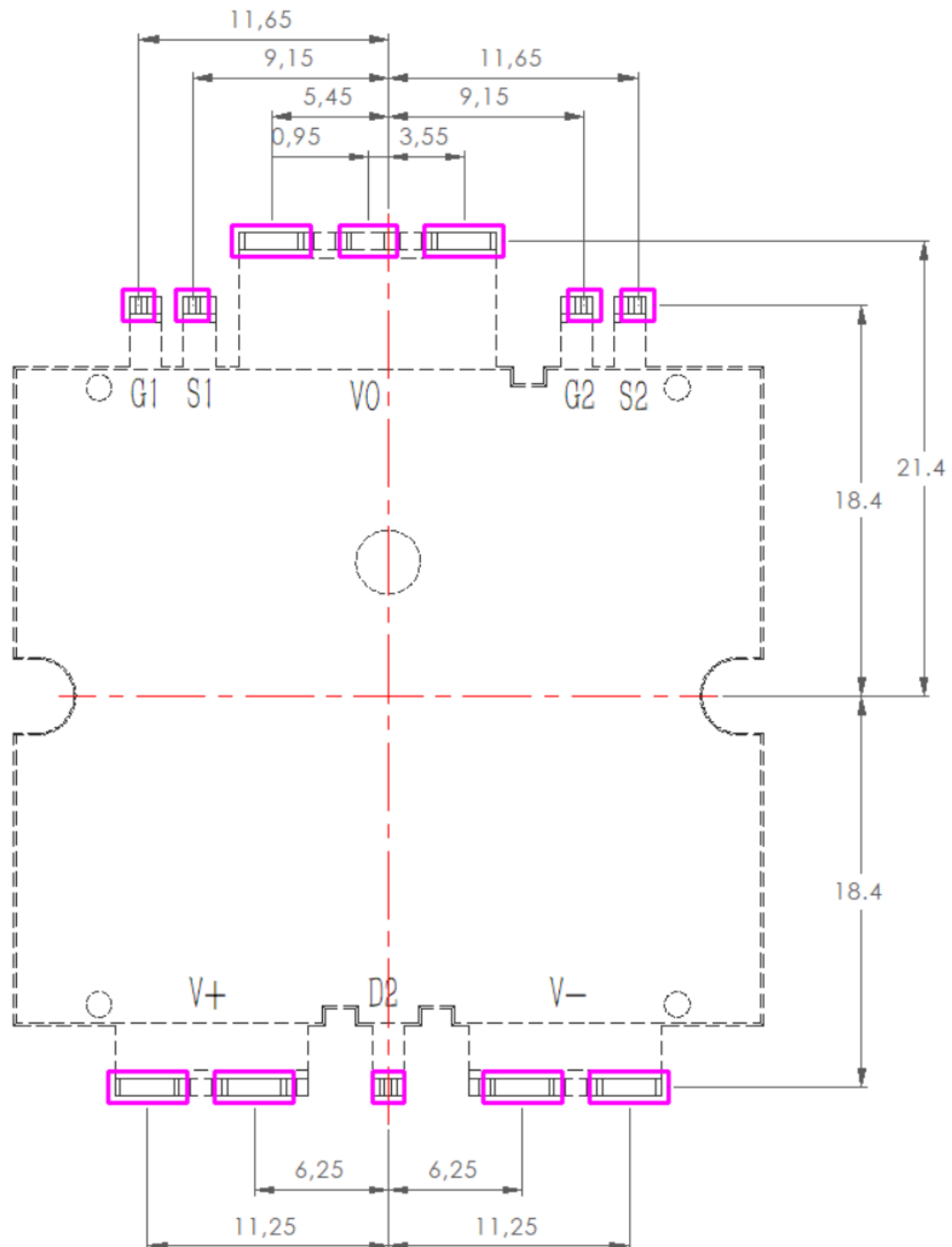
Figure 13. Transient Thermal Response Curve



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Suggested pad layout

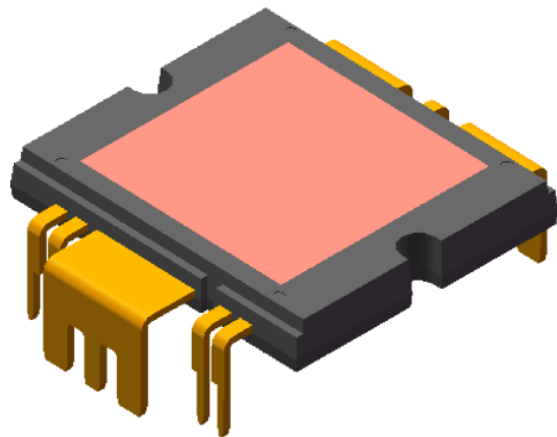
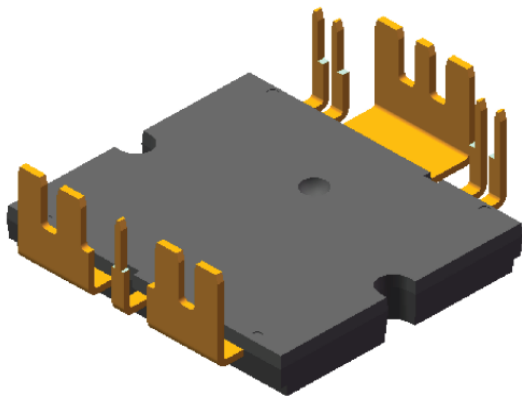


注：所有公差 $\pm 0.3\text{mm}$

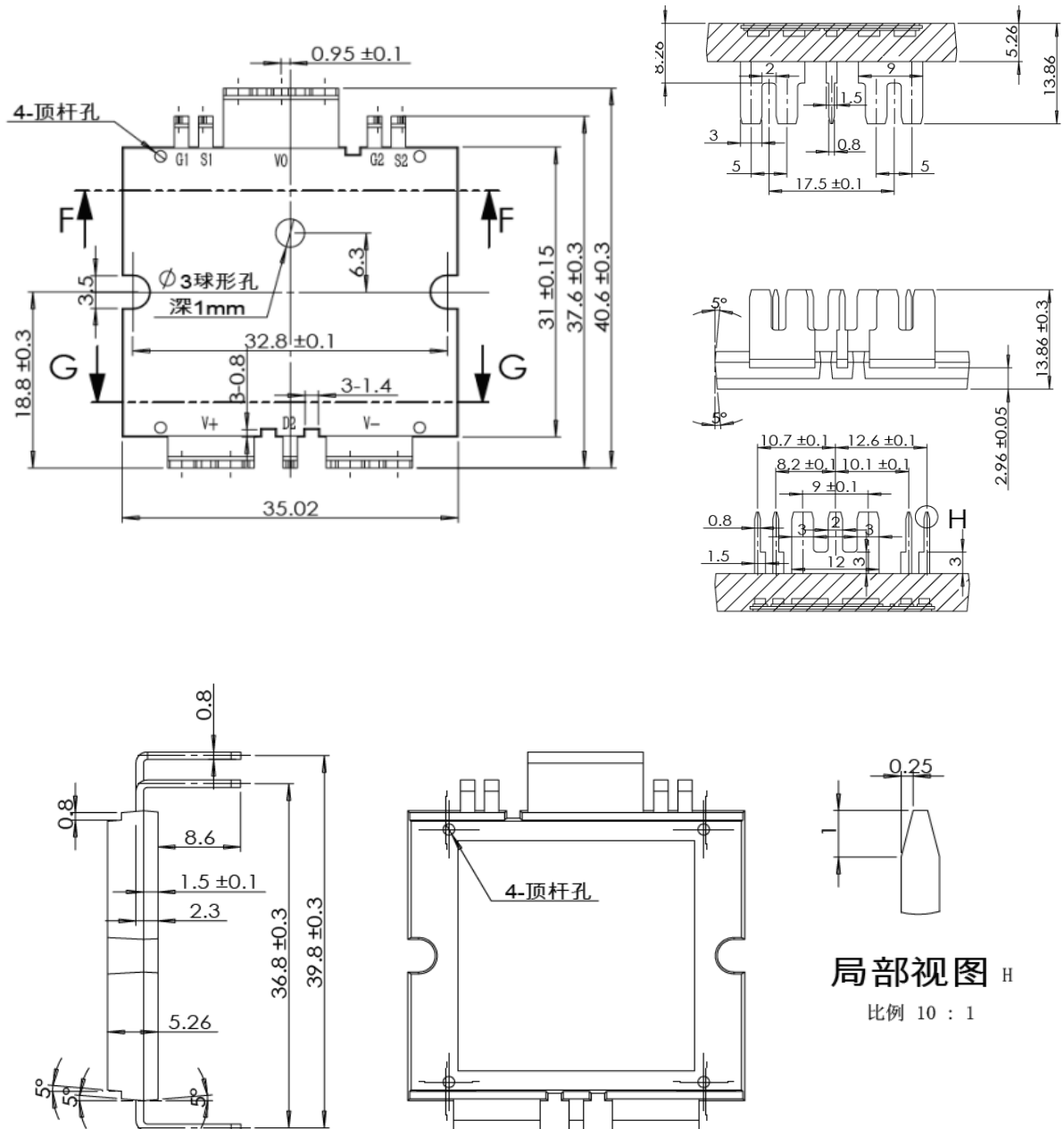
SLC500MM15SLT2



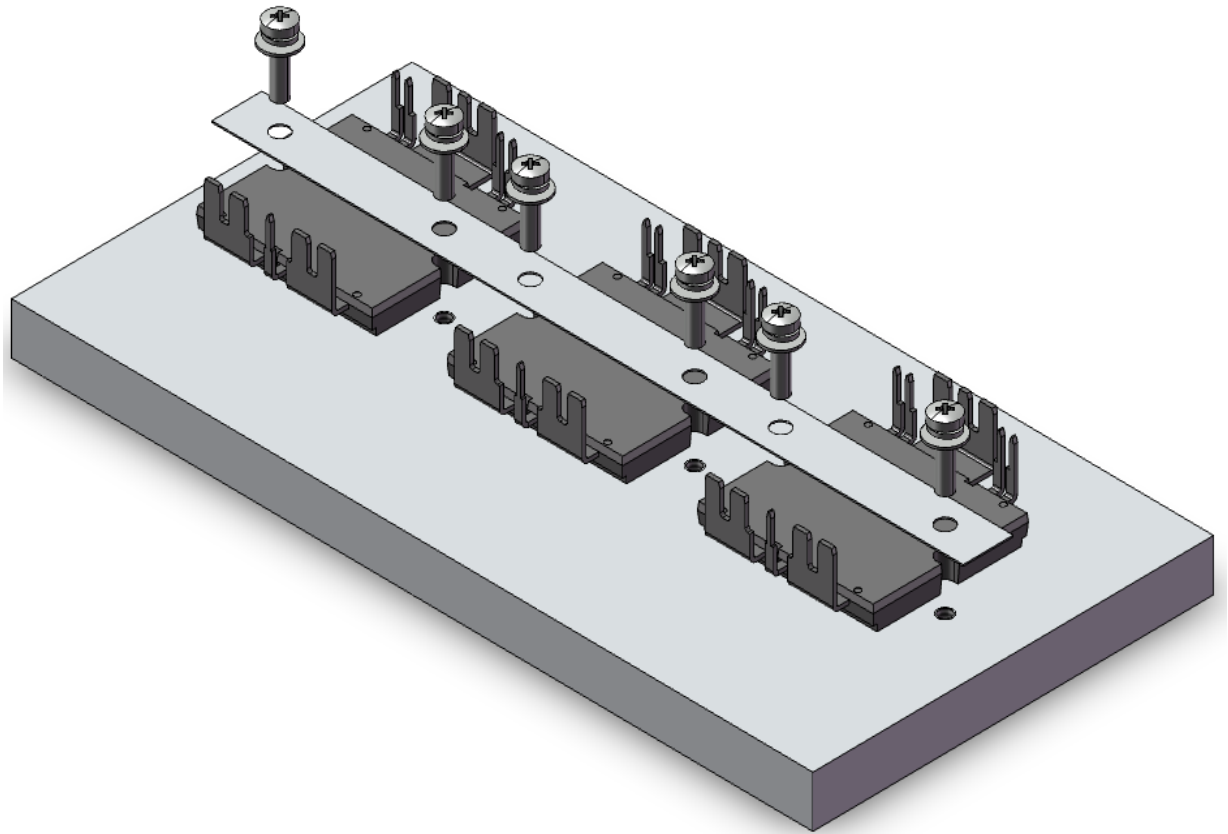
封装外形（单位：mm） / Package Outlines (Unit:mm)



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Installation advice



- 安装说明：**
- 1、请使用推荐的三合一螺栓，压条推荐用0.3mm的304不锈钢。
 - 2、安装时建议用0.3Nm的扭矩预紧，待导热硅脂充分排出后再用0.5-0.8Nm的扭矩锁紧。