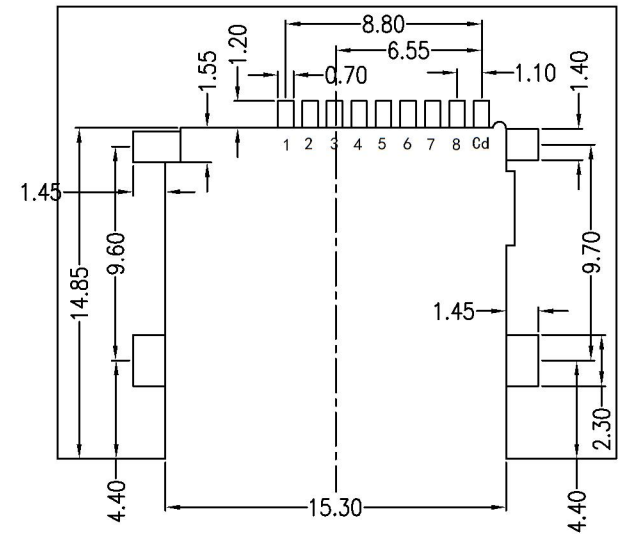
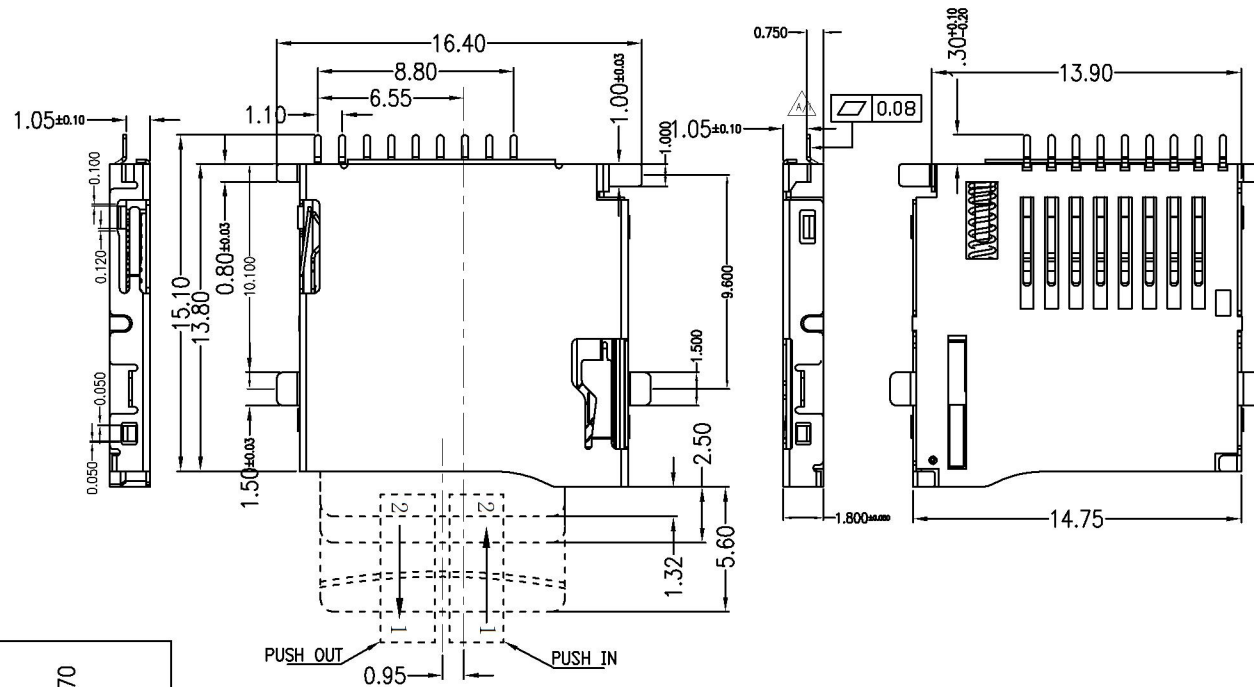
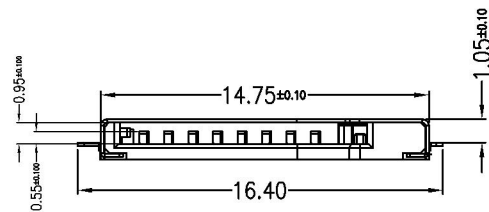
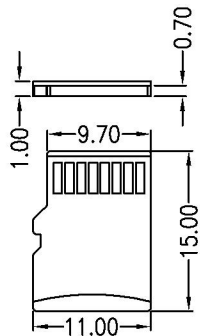


REV.	DESCRIPTION	APPD	DATE
	锡脚加平面度管控尺寸0.08	伍彪	2014-3-30



RECOMMENDED P.C.B HOLE LAYOUT
COMPONENT SIDE VIEW(TOLERANCE: ± 0.05)



PIN NO.	NAME	YTYPE	DESCRIPTION
1	DAT2	I/O/PP	DATE LINE(BIT2)
2	CD/DAT3	I/O/PP	CARD DETECT DATE LIN(BIT3)
3	CMD	PP	COMMAND RESPONSE
4	VDD	S	SUPPLY VOLTAGE
5	CLX	I	CLOCK
6	VSS	S	SUPPLY VOLTAGE GROUND
7	DATO	I/O/PP	DATE LINE(BIT0)
8	DAT1	I/O/PP	DATE LINE(DIT1)

DWG NO: LXP-2000-A		MATERIAL:		深圳市连欣科技有限公司	
LOT NO:		FINISH:			
UNIT: MM	SIZE: A4	COLOR:		TITLE: T-FLASH CRAD CONN	
TOLERANCES: X.X=±0.35 X.XX=±0.25 X.XXX=±0.15 GENERAL ANGLE: X.X=±3.0° X.XX=±2.0°		DR: 杨 凯 0112'13		PART NO: XDFP-0915-0120	
		CHK:		REV: E	
		APP:		SCALE: 1:1	SHEET: 1 OF 1