

NOTES: UNLESS OTHERWISE SPECIFIED

1. FOR GENERAL SERIES SPECIFICATIONS, SEE SDXX-0000.

2. MATERIAL AND FINISHES:

BODY: BRASS, GOLD PLATE
CONTACT: BERYLLIUM COPPER, GOLD PLATE
INSULATOR: PTFE

3. ELECTRICAL SPECIFICATIONS:

IMPEDANCE: 75 OHMS
FREQUENCY RANGE: DC-12GHz
RETURN LOSS: < -20dB @ 6.0GHz (WITH 75 OHM TRACE)
DIELECTRIC WITHSTANDING VOLTAGE: 1000VRMS MIN.
INSULATION RESISTANCE: 10,000 MEGOHMS MIN.

4. DURABILITY: 500 CYCLES

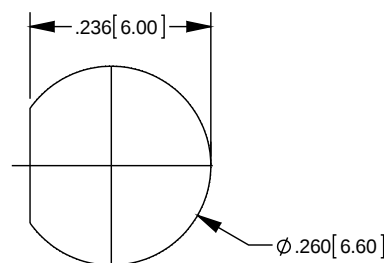
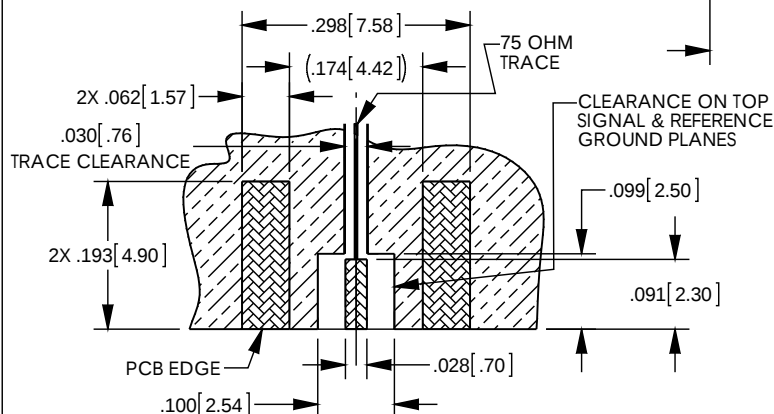
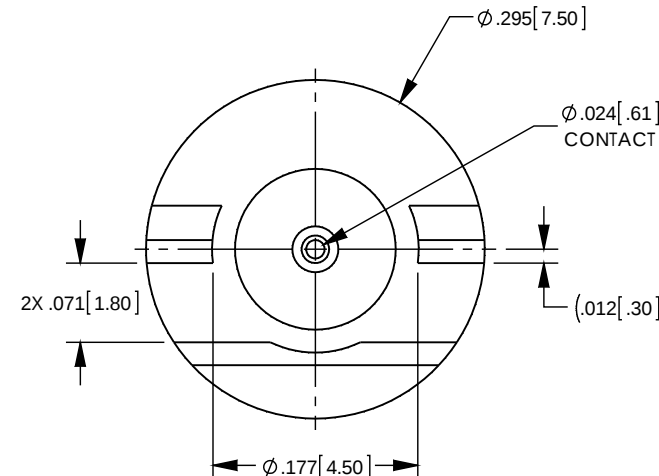
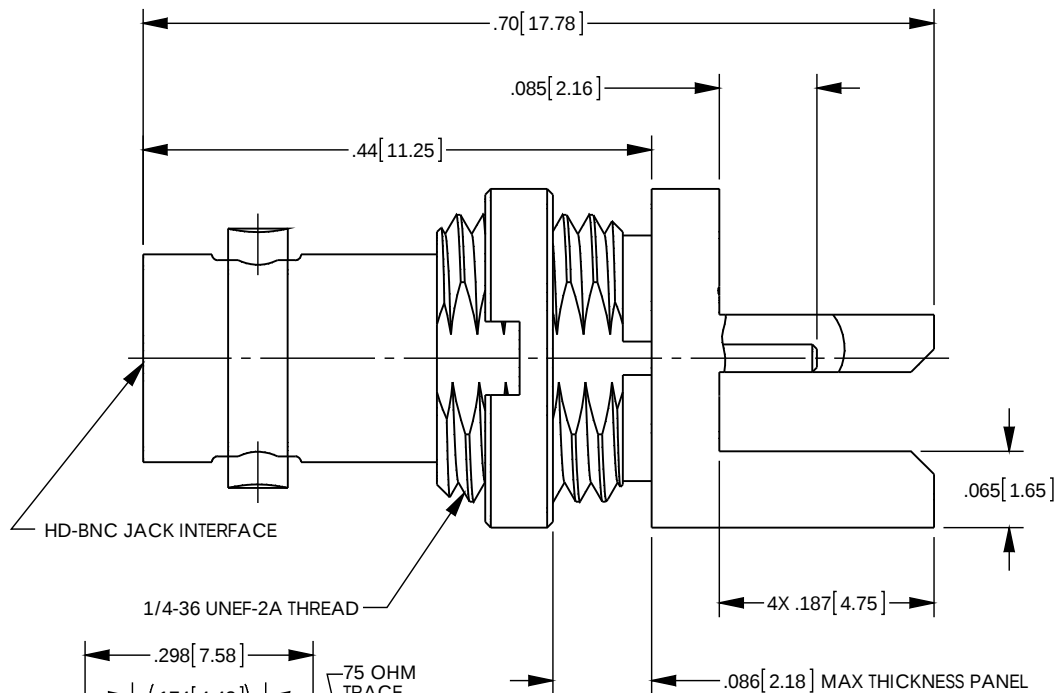
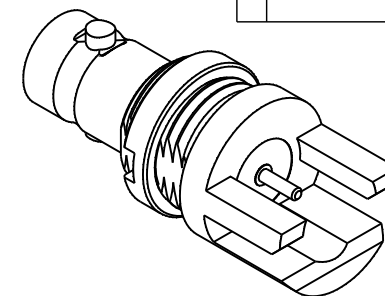
5. ENVIRONMENTAL:

THERMAL SHOCK PER MIL-STD-202 METHOD 107
TEST CONDITIONS B (EXCEPT HIGH TEMP @200° C)
VIBRATION: MIL-STD-202 METHOD 204 TEST CONDITION B
SHOCK: MIL-STD-202 METHOD 213 TEST CONDITION B
CORROSION: MIL-STD-202 METHOD 201
TEST CONDITIONSB 5% SALT SOLUTION

6. PACKAGING: INDIVIDUALLY BAGGED

7. COMPLIANT TO SMPTE 424M (3G-SDI) SPECIFICATION.

REV	DESCRIPTION	BY DATE	APPD
A	ER052721	KPL 04/21/14	MS



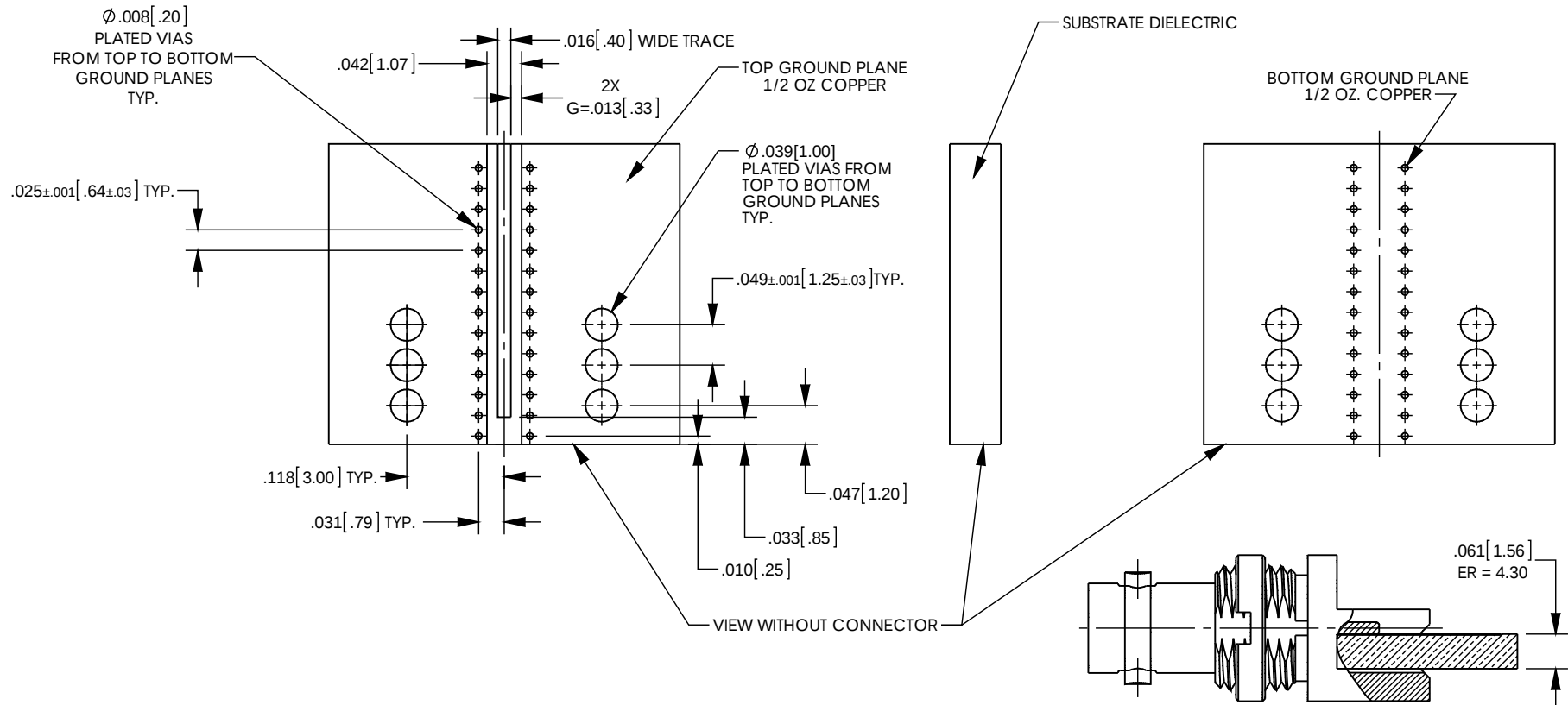
CATALOG P/N: HHD361HDE704

PER ASME Y14.5M-1994 UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES [MM] TOLERANCES: DECIMAL: .XX [X.X] ± .01 [0.3] .XXX [X.XX] ± .005 [0.13] .XXXX [X.XXX] ± .000X ANGLES: ± 2° SCALE: X:X				KINGS[®] by WINCHESTER ELECTRONICS Winchester Electronics Corporation 199 Park Road Ext Suite 104 Middlebury, Connecticut 06762			
CUSTOMER SALES DRAWING				HD-BNC END LAUNCH MOUNT JACK			
DRAWN KPL	DATE 04/21/2014	APPROVED MS	DATE 04/21/2014				
APPROVED XXX	DATE XX/XX/XX	APPROVED XXX	DATE XX/XX/XX				
A				SDHHD361HDE704		SHT 1	OF 3
				REV A			

CONFIGURATION - 1

RECOMMENDED PCB LAUNCH FOR OPTIMAL RF PERFORMANCE

VARIATIONS IN BOARD SUBSTRATE AND TRACE MAY REQUIRE DIFFERENT GEOMETRY



REV	DESCRIPTION	BY	DATE	APPD
01	PRELIMINARY DRAWING ER050491	KPL	11/21/13	MS
02	ADDED HHD TO PART NO. ER051456	KPL	04/21/2014	MS
03	SEE SHT 1 ER051644	KPL	02/10/14	MS
A	SEE SHEET 1 ER052721	KPL	04/21/14	MS

PER ASME Y14.5M-1994 UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES: DECIMAL: .XX ± .XX .XXX ± .XXX .XXXX ± .XXXX ANGLES: ± X° SCALE: X:X		THIRD ANGLE	
FINISH		DRAWN DATE APPROVED DATE	
KPL 04/21/2014 MS 04/21/2014		APPROVED DATE APPROVED DATE	
XXX XX/XX/XX XXX XX/XX/XX		A	

WINCHESTER ELECTRONICS Winchester Electronics Corporation 199 Park Road Ext Suite 104 Middlebury, Connecticut 06762		HD BNC BHD JACK EDGE MOUNT FOR .062 THICK BOARD	
SDHHD361HDE704	SHT 2	OF 3	REV A

RECOMMENDED PCB LAUNCH FOR OPTIMAL RF PERFORMANCE

Technical drawing of a multi-layer printed circuit board (PCB) showing various layers and dimensions.

Top View (Left):

- Plated Vias:** $\phi .008 [.20]$ PLATED VIAS FROM TOP TO BOTTOM GROUND PLANES TYP.
- Dimensions:**
 - $.118 [3.00]$ TYP.
 - $.020 [.51]$ WIDE TRACE
 - $G = .062 [1.58]$ TYP 2X
 - $.025 \pm .001 [.64 \pm .03]$ TYP.
 - $.099 [2.51]$
 - $.055 [1.40]$
 - $.033 [.85]$
 - $.166 [4.21]$
 - $.013 [.33]$ TYP
 - $.032 [.82]$
- Labels:** TOP GROUND PLANE 1/2 OZ COPPER, VIEW WITHOUT CONNECTOR

Bottom View (Right):

- Labels:** MIDDLE GROUND PLANE 1/2 OZ. COPPER, SUBSTRATE DIELECTRIC, BOTTOM GROUND PLANE 1/2 OZ. COPPER
- Section Line:** A-A

Section View (Bottom Right):

- Dimensions:**
 - $.089 [2.26]$
 - $.052 [1.32]$
 - $.045 [1.14]$ ER = 4.30
 - $.024 [.61]$ ER = 4.30
- Section Line:** A-A

Labels: MIDDLE GROUND PLANE

REV

01

DESCRIPTION

PRELIMINARY
DRAWING
ER050491

BY DATE

11/22/13

APPD

MS

REV

02

DESCRIPTION

ADDED HHD TO
PART NO.
ER051456

BY DATE

02/03/14

APPD

MS

REV

03

DESCRIPTION

SEE SHT 1
ER051644

BY DATE

02/10/14

APPD

MS

REV

A

DESCRIPTION

SEE SHT 1
ER052721

BY DATE

04/21/14

APPD

MS

PER ASME Y14.5M-1994
UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES
TOLERANCES:
DECIMAL: .XX ± .XX
.XXX ± .XXX
.XXXX ± .XXXX
ANGLES: ± X° SCALE: 3:1

THIRD ANGLE

FINISH

DRAWN

DATE

APPROVED

DATE

KPL

11/22/13

MS

11/22/13

APPROVED

DATE

APPROVED

DATE

XXX

XX/XX/XX

XXX

XX/XX/XX

HD BNC BHD JACK EDGE MOUNT
FOR .062 THICK BOARD

A

SDHHD361HDE704

SHT

OF

REV

3

3

A

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