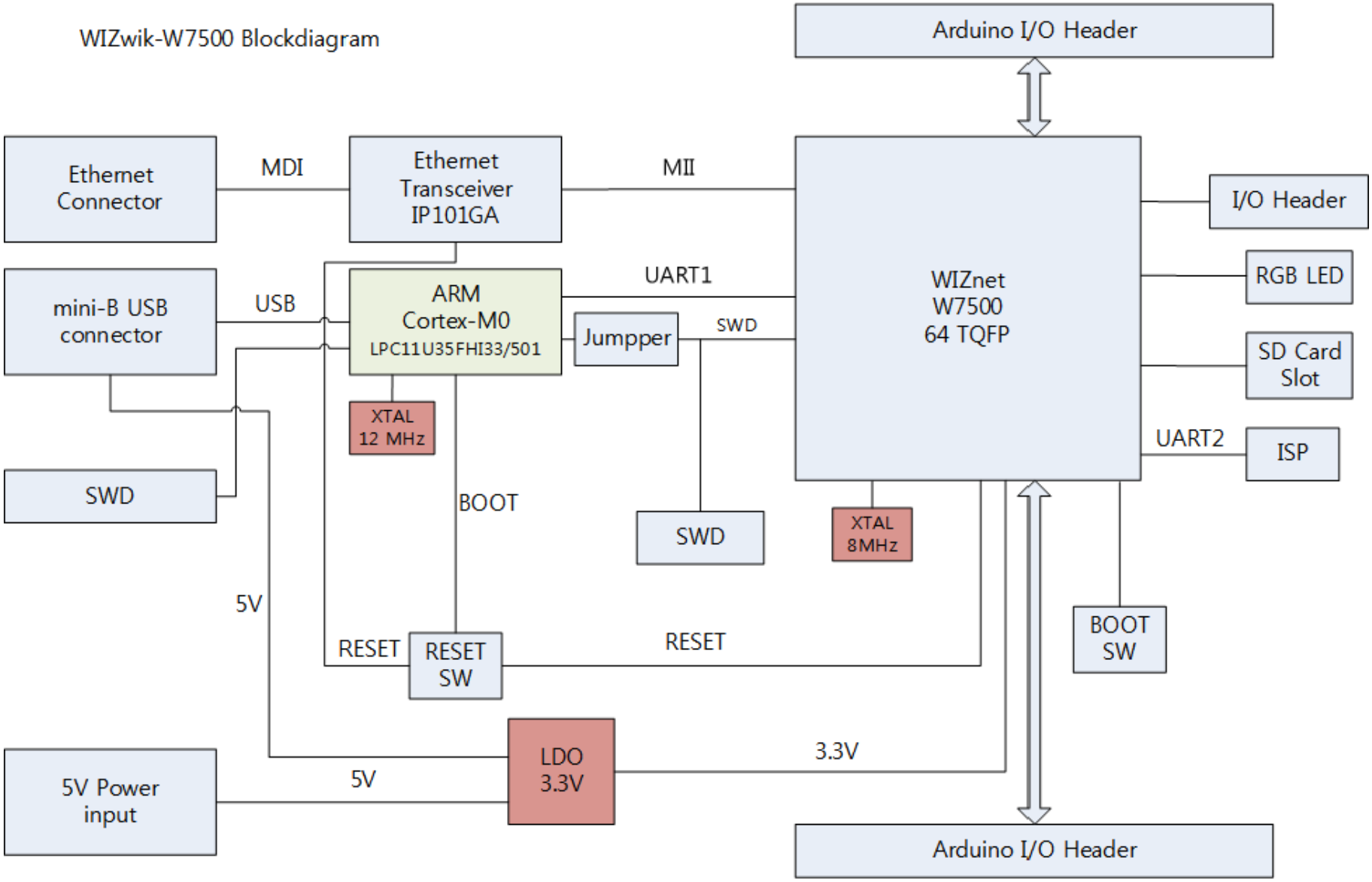

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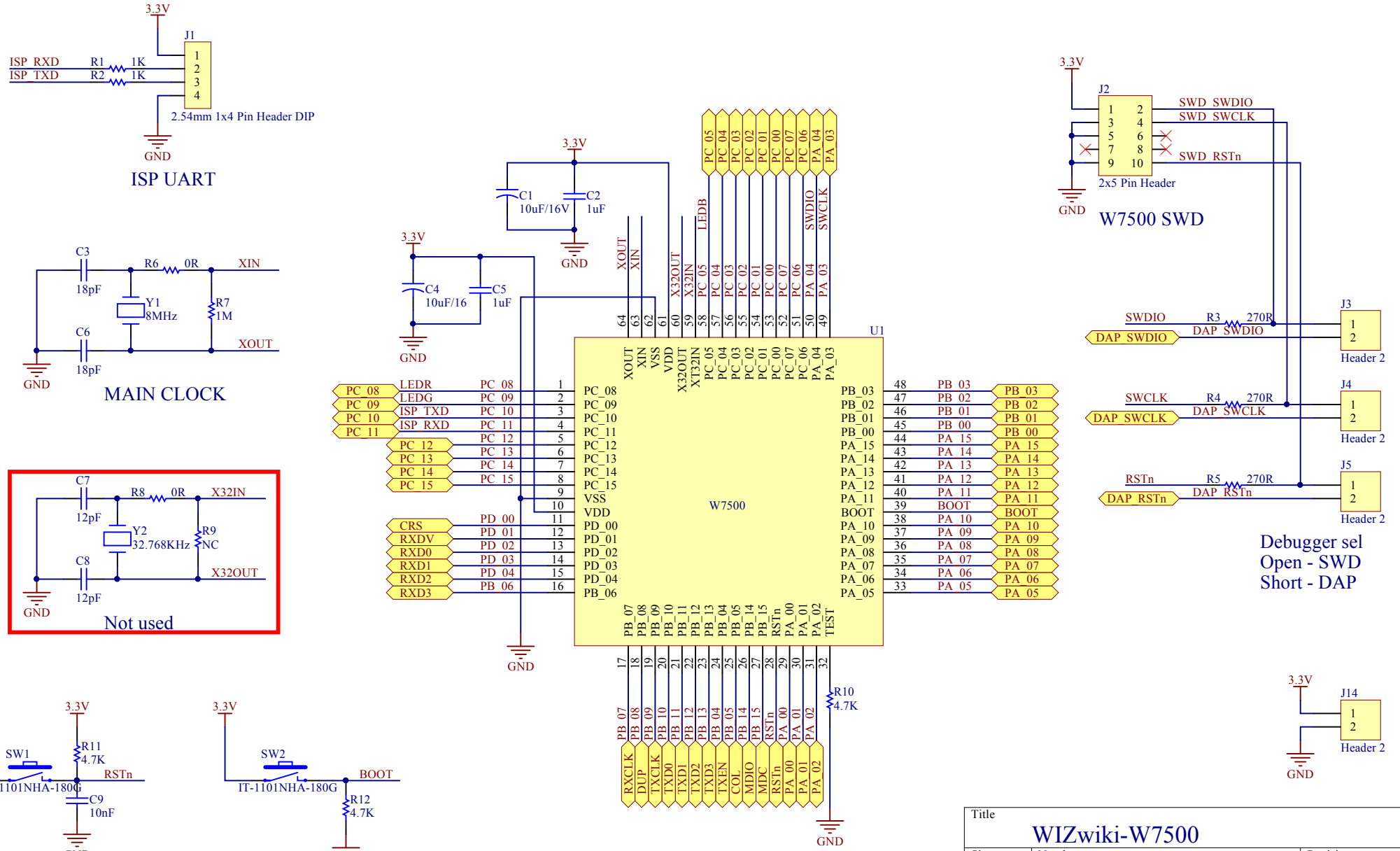

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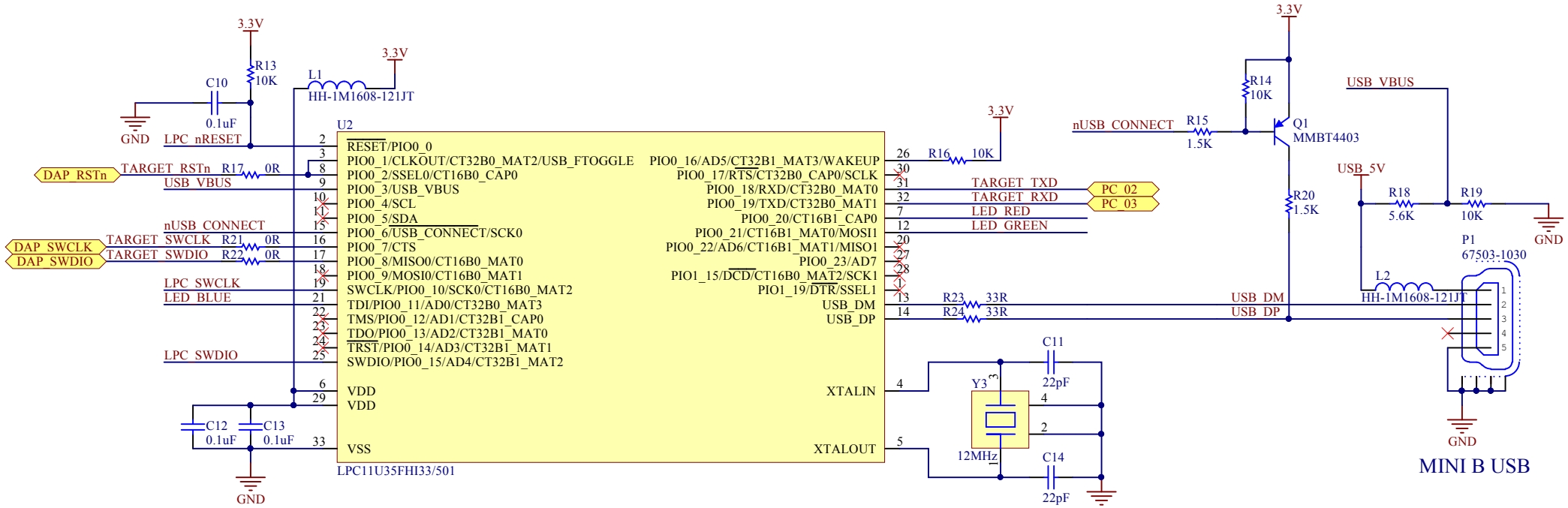

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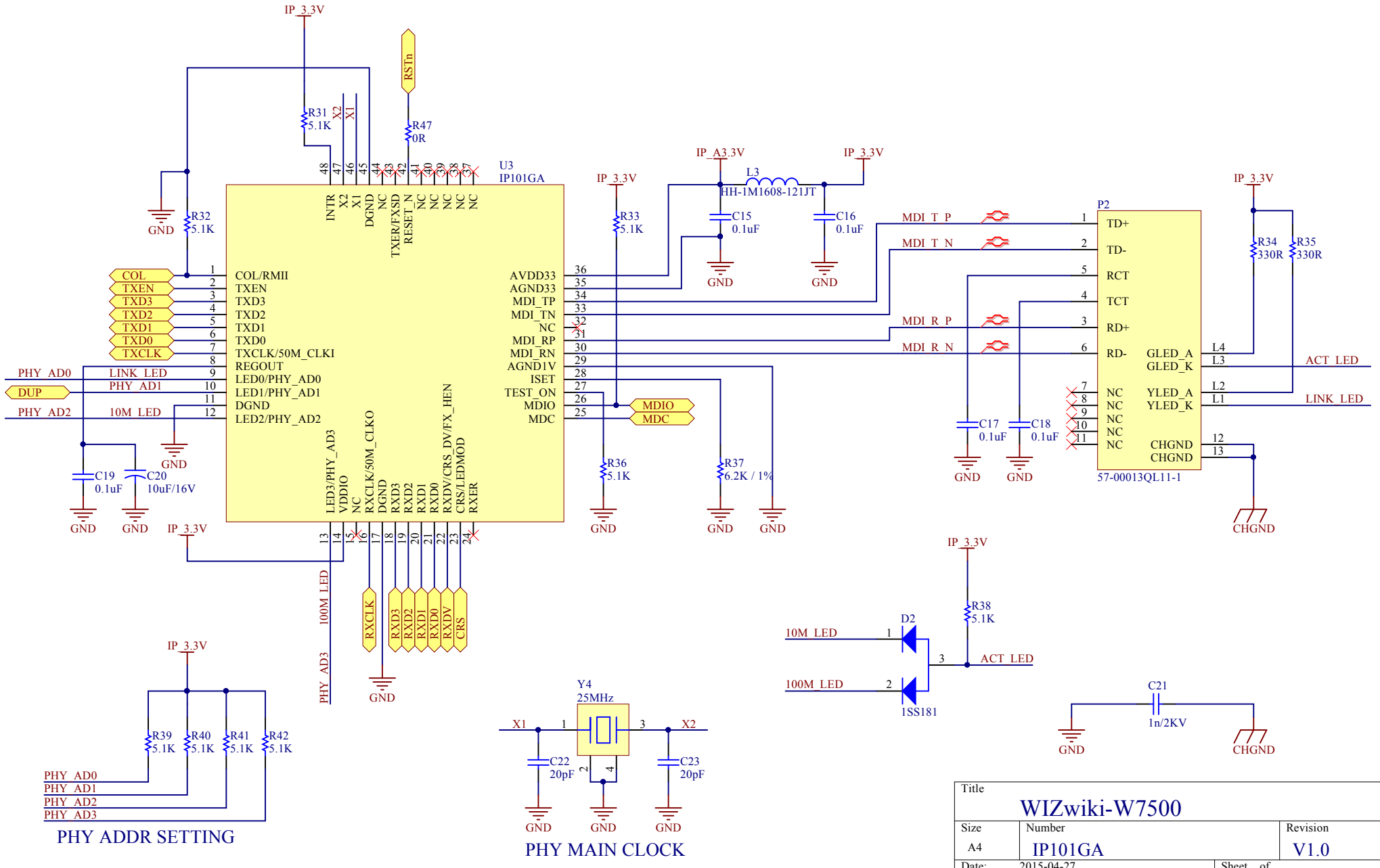
IP_3.3V
RSTin
R31 5.1K
R47 0R
X2
X1
U3
IP101GA
INTR
X2
X1
DGND
NC
TXER/FXSD
RESET_N
R32 5.1K
GND
COL
TXEN
TXD3
TXD2
TXD1
TXD0
TXCLK
COL/RMII
TXEN
TXD3
TXD2
TXD1
TXD0
TXCLK/50M_CLKI
REGOUT
LED0/PHY_AD0
LED1/PHY_AD1
DGND
LED2/PHY_AD2
PHY_AD0
LINK_LED
DUP
PHY_AD1
PHY_AD2
10M_LED
C19 0.1uF
C20 10uF/16V
LED3/PHY_AD3
VDDIO
NC
RXCLK/50M_CLKO
DGND
RXD3
RXD2
RXD1
RXD0
RXDV/CRS_DV/FX_HEN
CRS/LEDMOD
RXER
AVDD33
AGND33
MDI_TP
MDI_TN
NC
MDI_RP
MDI_RN
AGND1V
ISET
TEST_ON
MDIO
MDC
IP_A3.3V
L3
HH-1M1608-121JT
C15 0.1uF
C16 0.1uF
R33 5.1K
R36 5.1K
R37 6.2K / 1%
MDIO
MDC
MDI_T_P
MDI_T_N
MDI_R_P
MDI_R_N
P2
TD+
TD-
RCT
TCT
RD+
RD-
NC
NC
NC
NC
NC
GLED_A
GLED_K
YLED_A
YLED_K
CHGND
CHGND
57-00013QL11-1
C17 0.1uF
C18 0.1uF
R34 330R
R35 330R
L4
L3
L2
L1
ACT_LED
LINK_LED
CHGND
100M_LED
PHY_AD3
RXCLK
RXD3
RXD2
RXD1
RXD0
RXDV
CRS
D2
1SS181
R38 5.1K
ACT_LED
10M_LED
100M_LED
R39 5.1K
R40 5.1K
R41 5.1K
R42 5.1K
PHY_AD0
PHY_AD1
PHY_AD2
PHY_AD3
PHY ADDR SETTING
Y4
25MHz
X1
X2
C22 20pF
C23 20pF
PHY MAIN CLOCK
C21
1n/2KV
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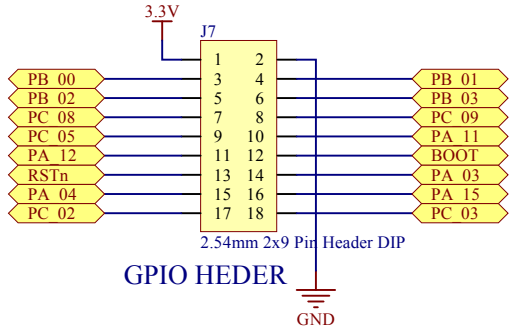
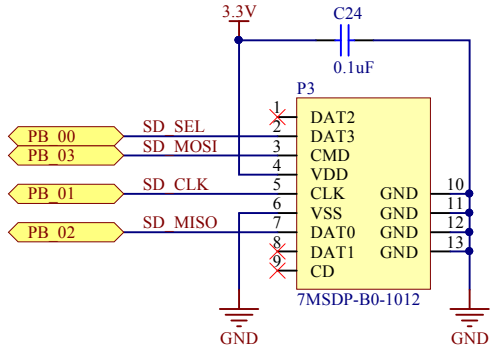


GPIO HEDER



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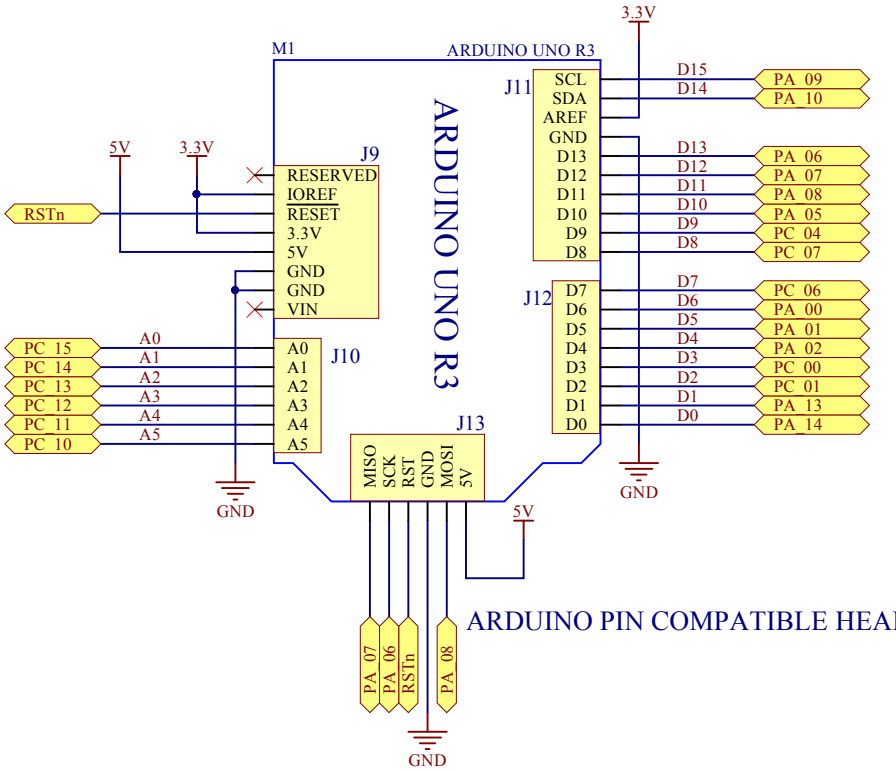
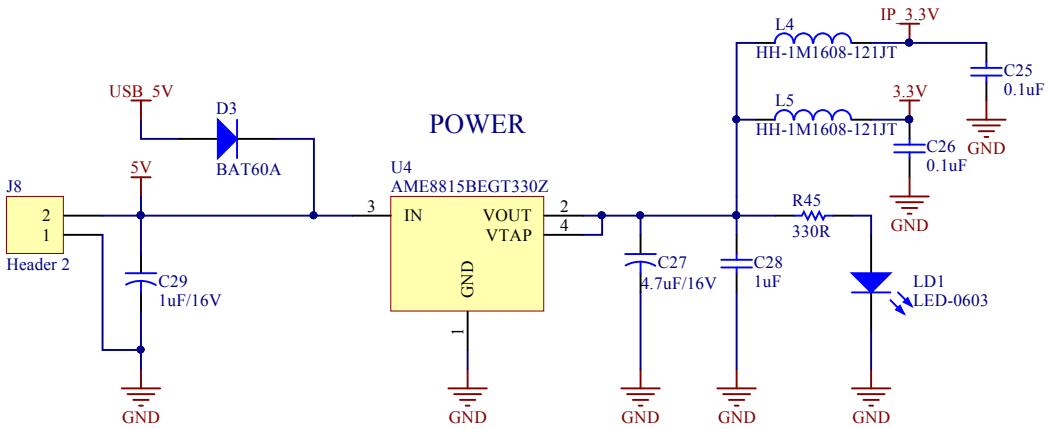
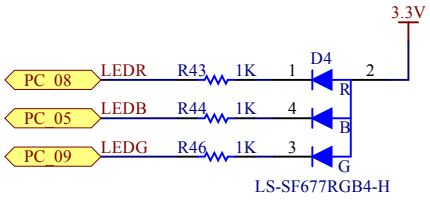
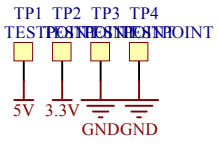


ARDUINO PIN COMPATIBLE HEADER





RGB LED



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