

0.1-3.8GHz SP8T Switch for High Power Applications

FEATURES

- Broadband frequency range: 0.1 to 3.8 GHz
- Low insertion loss: 0.81dB typical @ 3.8 GHz
- High isolation: >20dB @ 3.8 GHz
- Integrated logic
- Small QFN (14-pin, 2.0mm x 2.0 mm) package (MSL1, 260 °C per JEDEC J-STD-020)

APPLICATIONS

- 2G/3G/4G antenna primary and diversity
- Cellular modems, tablets and USB Devices
- Other RF front-end modules

GENERAL DESCRIPTION

The AW13418TQNR is a SP8T switch with low insertion loss and high Isolation. It can be used to support band switching and mode switching in antenna diversity systems for 2G/3G/4G, data cards and tablets.

The symmetrical design of internal ports makes it convenient for PCB routing and adjustment of receiving and transmitting signals. The band/mode switching is realized by the GPIO pins as referenced in the chip block diagram and the control logic.

The AW13418TQNR is provided in a compact 2.0mm x 2.0mm x 0.55mm, 14-pin QFN package.

TYPICAL APPLICATION CIRCUIT

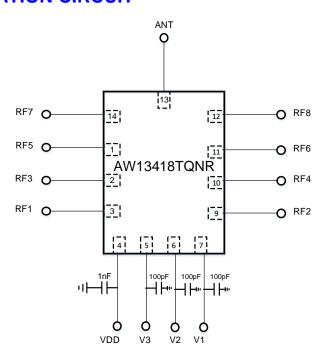


Figure 1 Typical Application Circuit of AW13418TQNR

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PIN CONFIGURATION AND TOP MARK

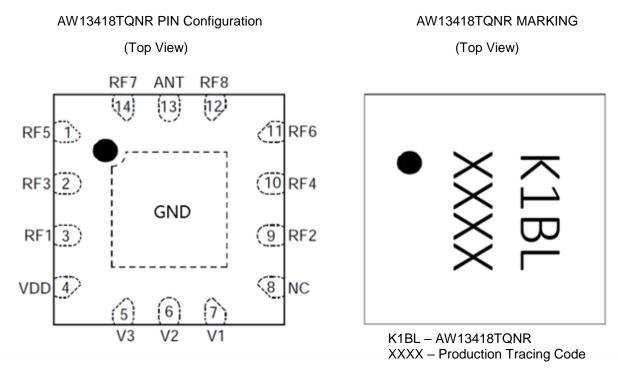


Figure 2 Pin Configuration and Top Mark

PIN DEFINITION

No.	NAME	DESCRIPTION			
1	RF5	RF I/O path 5			
2	RF3	RF I/O path 3			
3	RF1	RF I/O path 1			
4	VDD	DC power supply			
5	V3	DC control voltage 3			
6	V2	OC control voltage 2			
7	V1	DC control voltage 1			
8	NC	Not connected			
9	RF2	RF I/O path 2			
10	RF4	RF I/O path 4			
11	RF6	RF I/O path 6			
12	RF8	RF I/O path 8			
13	ANT	Antenna port			
14	RF7	RF I/O path 7			

Note: Bottom ground paddles must be connected to ground.



FUNCTIONAL BLOCK DIAGRAM

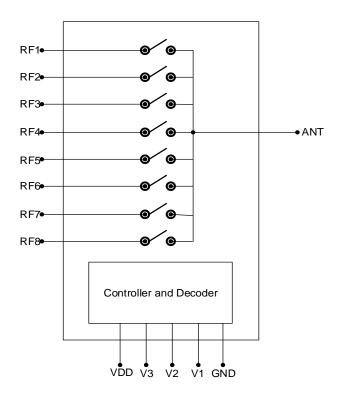
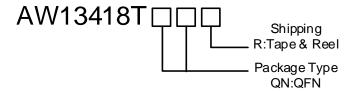


Figure 3 FUNCTIONAL BLOCK DIAGRAM

ORDERING INFORMATION

Part Number	Temperature	Package	Marking	Moisture Sensitivity Level	Environmental Information	Delivery Form
AW13418TQ NR	-40°C∼85°C	QFN 2mmX2m mX0.55m m-14L	K1BL	MSL1	ROHS+HF	4500 units/ Tape and Reel





ABSOLUTE MAXIMUM RATINGS(NOTE1)

PARAMETER	PARAMETERS			
Supply Voltage Ran	Supply Voltage Range VDD			
Control Voltage Range	Control Voltage Range V1,V2,V3			
RF input power(RF1	RF input power(RF1 to RF8)			
Operating Free-air Tempe	Operating Free-air Temperature Range			
Storage Temperatu	-65°C to 150°C			
Lead Temperature (Solderi	260°C			
НВМ	±1kV			
CDM	CDM			

NOTE1: Conditions out of those ranges listed in "absolute maximum ratings" may cause permanent damages to the device. In spite of the limits above, functional operation conditions of the device should within the ranges listed in "recommended operating conditions". Exposure to absolute-maximum-rated conditions for prolonged periods may affect device reliability.

NOTE2: The human body model is a 100pF capacitor discharged through a 1.5k Ω resistor into each pin. Test method: MIL-STD-883J Method 3015.9

ELECTRICAL CHARACTERISTICS

VDD=2.8V, V1=V2=V3=0/1.8V, PIN=0dBm, TOP=+25°C, Z_0 =50 Ω . (unless otherwise noted)

	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT		
DC Specif	DC Specifications							
VDD	Supply Voltage		1.65	2.8	5	V		
IDD	Supply Current			43		μА		
VCTL_H VCTL_L	Control Voltage High Low		0.9 0	1.2 0	3 0.3	V		
ICTL	Control Current	VCTL = 1.8V		0.5	3	μА		
tON	Turn-on Switching Time	50% of final control voltage to 90% of final RF power, switching between RF1/2/3/4/5/6/7/8		0.7	1	μS		
RF Specif	RF Specifications							
IL	Insertion loss(ANT pin to RF1- RF8)	0.1-1.0G 1.0-2.0G 2.0-2.7G 2.7-3.8G		0.31 0.42 0.56 0.81	0.46 0.57 0.73 0.9	dB dB dB dB		
ISO	Isolation (ANT pin to RF1-RF8)	0.1-1.0G 1.0-2.0G	30 24	40 33		dB dB		



		2.0-2.7G	20	27	dB
		2.7-3.8G	18	23	
		0.1-1.0G	23	31	dB
RL	Input return loss (ANT pin to RF1-	1.0-2.0G	18	25	dB
INL.	RF8)	2.0-2.7G	15	21	dB
		2.7-3.8G	10	15	
	Second harmonics (ANT pin to	PIN=+26dBm,			
2fo	RF1-RF8)	0.1-3.8GHz		90	dBc
	Third harmonics (ANT pin to RF1-	PIN=+26dBm,		_	
3fo	RF8)	0.1-3.8GHz		85	dBc
P _{0.1dB}	0.1dB Compression Point (ANT pin to RF1-RF8)	0.1GHz–3.8GHz		37	dBm

TIMING DIAGRAM (POWER ON AND OFF SEQUENCE)

It is very important that the user adheres to the correct power-on/off sequence in order to avoid damaging the device. The control signal V1, V2, V3 should be set to 0V unless VDD is set in the operating voltage range.

Power ON:

- 1) Apply voltage supply --- VDD
- 2) Set Controls---V1, V2, V3
- 3) Apply RF input

Change switch position from one RF port to another:

- 1) Remove RF input
- 2) Change control voltages V1, V2, V3 to set the switch to desired RF port
- 3) Apply RF input

Power OFF:

- 1) Remove RF input
- 2) Remove control voltages-V1, V2, V3
- 3) Remove VDD input

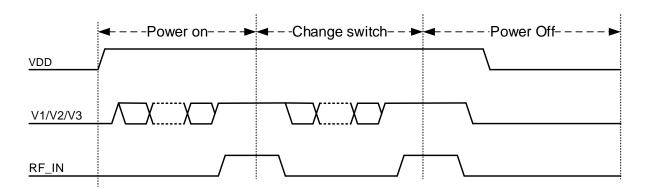


Figure 4 Power on/Change switch/Power off sequence



AW13418TQNR CONTROL LOGIC

C	ontro Pins	ol	Switch RF I/O							
V1	V2	V3	RF1	RF2	RF3	RF4	RF5	RF6	RF7	RF8
0	0	0	ON	Isolation						
0	0	1	Isolation	ON	Isolation	Isolation	Isolation	Isolation	Isolation	Isolation
0	1	0	Isolation	Isolation	ON	Isolation	Isolation	Isolation	Isolation	Isolation
0	1	1	Isolation	Isolation	Isolation	ON	Isolation	Isolation	Isolation	Isolation
1	0	0	Isolation	Isolation	Isolation	Isolation	ON	Isolation	Isolation	Isolation
1	0	1	Isolation	Isolation	Isolation	Isolation	Isolation	ON	Isolation	Isolation
1	1	0	Isolation	Isolation	Isolation	Isolation	Isolation	Isolation	ON	Isolation
1	1	1	Isolation	Isolation	Isolation	Isolation	Isolation	Isolation	Isolation	ON



APPLICATION CIRCUITS

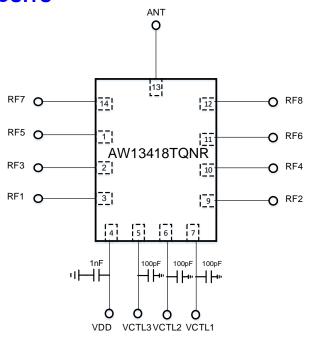


Figure 5 AW13418TQNR EVB Schematic

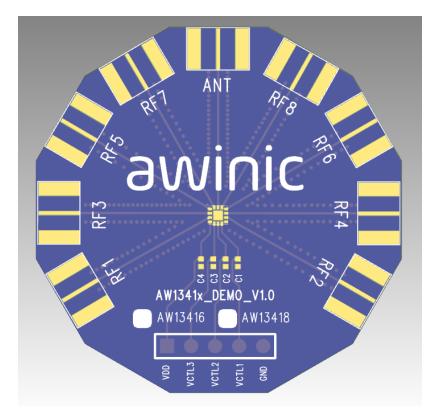
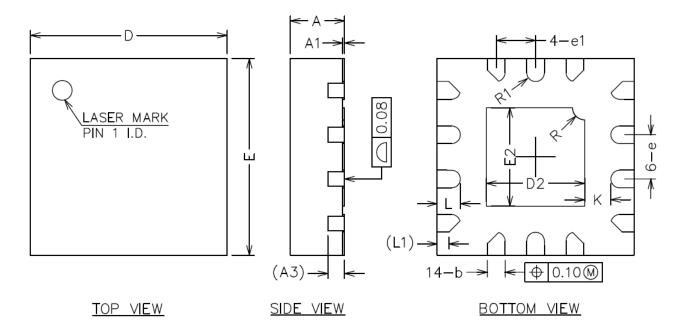


Figure 6 AW13418TQNR EVB



PACKAGE OUTLINE DIMENSIONS



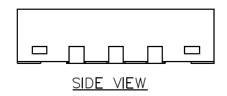


Figure 7 Package Outline

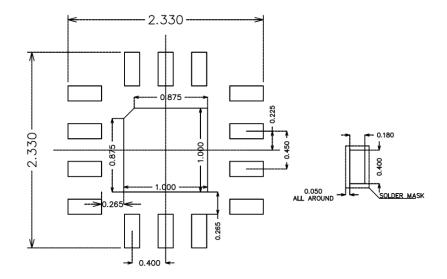
AW13418TQNR OUTLINE DIMENSIONS

COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
Α	0.50	0.55	0.60
A1	0.00	0.02	0.05
А3		0.15R EF	
b	0.13	0.18	0.23
D	1.90	2.00	2.10
E	1.90	2.00	2.10
D2	0.90	1.00	1.10
E2	0.90	1.00	1.10
е	0.40	0.45	0.50
e1	0.35	0.40	0.45
K	0.15	_	1
L	0.185	0.235	0.285
L1		0.118REF	
R		0.125REF	·
R1	0.075	_	_



LAND PATTERN DATA



Unit: mm

TAPE AND REEL INFORMATION

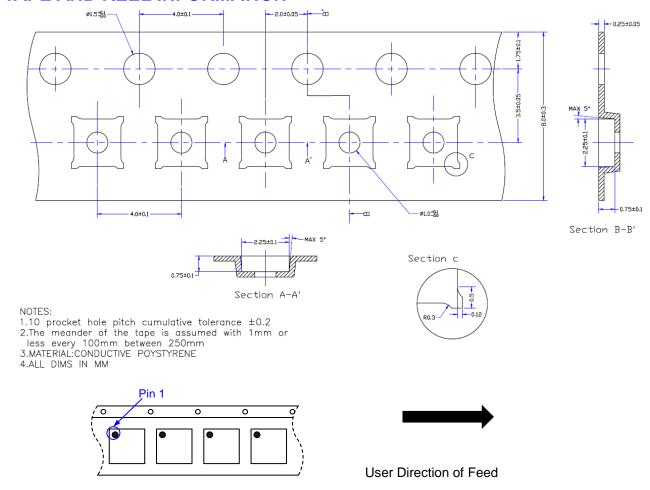


Figure 8-1 Tape and Reel

Unit: mm

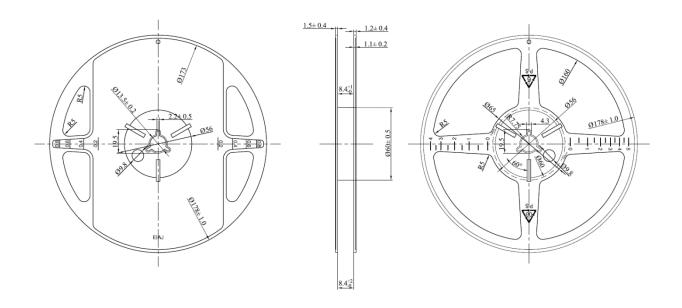


Figure 8-2 Tape and Reel



REVISION HISTORY

Vision	Date	Change Record
V1.0	Aug 2021	Officially Released



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